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Abstract

A process for designing and manufacturing semi-conductor memory components, in particular DRAM components

The invention relates to a process for designing and a process for manufacturing semi-conductor memory components (1), especially DRAM components, whereby the process comprises the following steps:

- the design of a first layout (2') for a module (2) of the semi-conductor memory component (1), to be used in a first configuration of the semi-conductor memory component (1);
- the design of a second layout (2'') for the semi-conductor memory component module (2), to be used in a second configuration of the semi-conductor memory component (1);
- using the first layout (2') or the second layout (2'') for the total layout of the semi-conductor memory component (1), depending on the current configuration of the semi-conductor memory component (1).

- Figure 1 -