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(54) **Title:** INTEGRATED CIRCUIT HAVING MICROVAULT MEMORIES

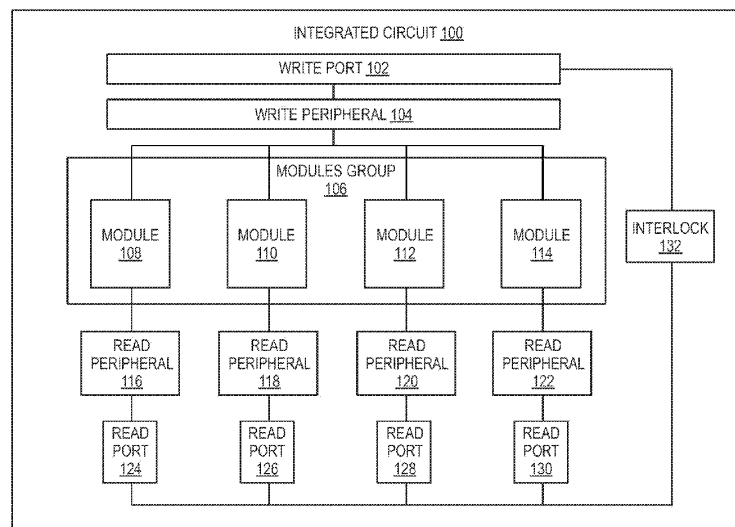


FIG. 1

(57) **Abstract:** An integrated circuit is disclosed herein that may comprise a plurality of microvaults, with each of the microvaults disposed in a spaced relation relative to one another and adjacent to a first surface. The plurality of microvaults includes a first microvault. Additionally, the integrated circuit may comprise a plurality of bonding areas, with each bonding area disposed on the first surface and adjacent to a respective one of the plurality of microvaults. The plurality of bonding areas includes a first bonding area in operative communication with the first microvault.

[Continued on next page]



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