



(51) International Patent Classification:

H01L 21/8242 (2006.01) *H01L 21/768* (2006.01)
H01L 27/02 (2006.01) *H01L 27/105* (2006.01)
H01L 27/108 (2006.01) *H01L 27/115* (2006.01)
G11C 11/401 (2006.01) *H01L 27/24* (2006.01)
H01L 21/02 (2006.01) *H01L 29/78* (2006.01)

(21) International Application Number:

PCT/US2009/032354

(22) International Filing Date:

29 January 2009 (29.01.2009)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

12/033,768 19 February 2008 (19.02.2008) US

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM,

AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))

Published:

- with international search report (Art. 21(3))

[Continued on next page]

(54) Title: MEMORY DEVICE WITH LOCAL DATA LINES AND METHOD OF MAKING AND OPERATING THE SAME

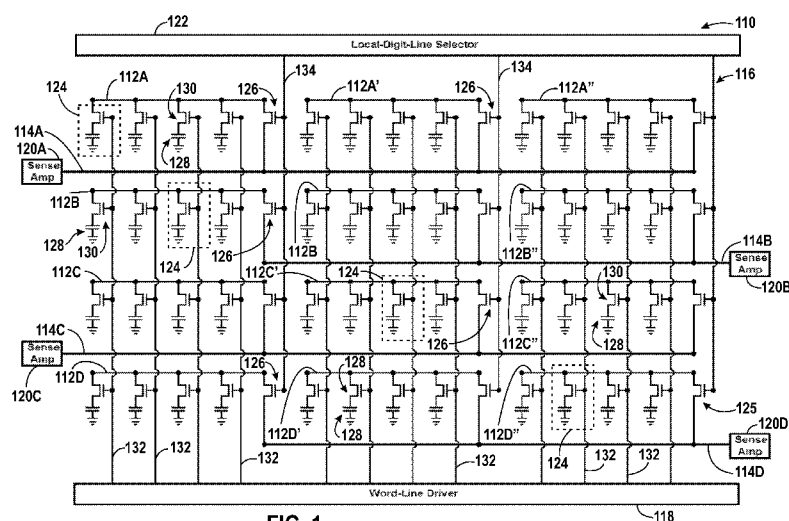


FIG. 1

(57) Abstract: The parasitic capacitance of global data lines (114) in a memory device is avoided or reduced by means of local data lines (112). The local data lines can be connected to the global data lines via an access transistor (126) and by an interconnect that connects the lower electrode of a storage capacitor (154) to the global data line through an aperture (158) in a capacitor plate electrode (156).



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- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))*