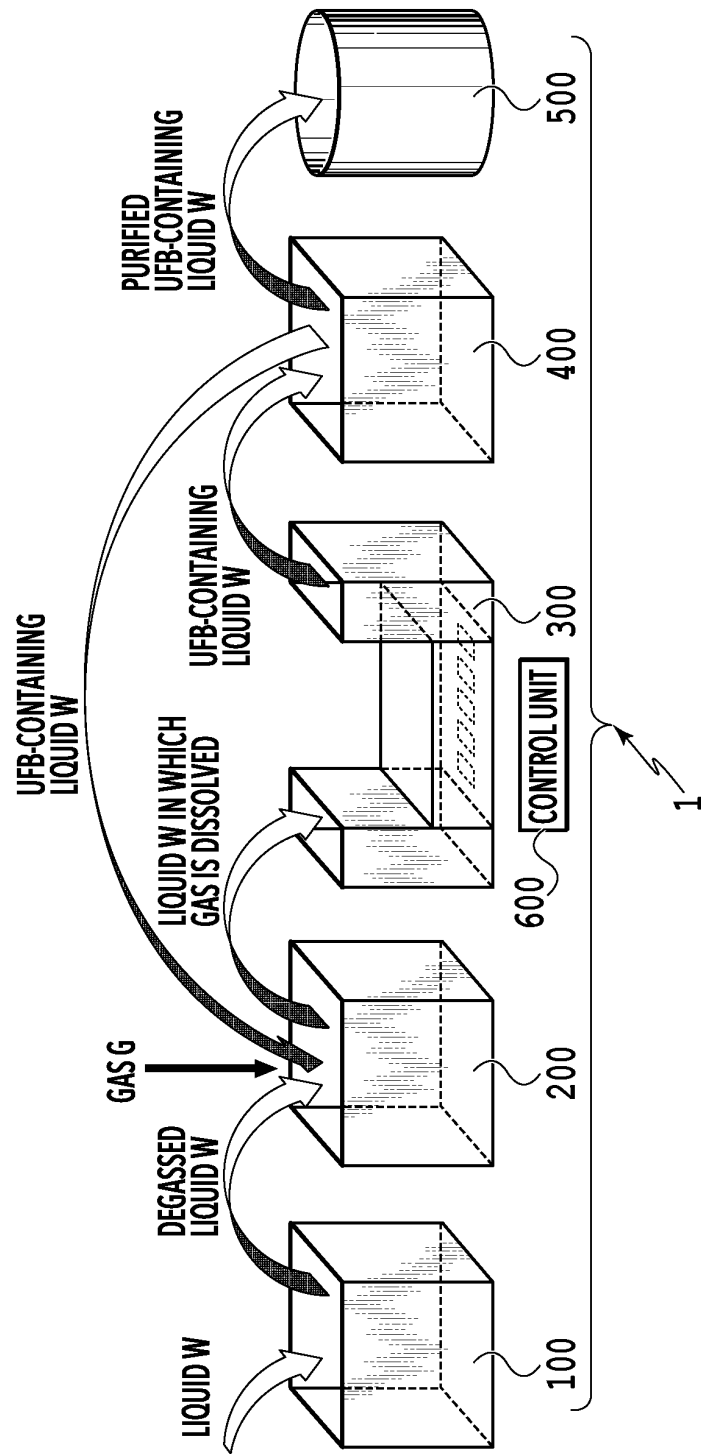
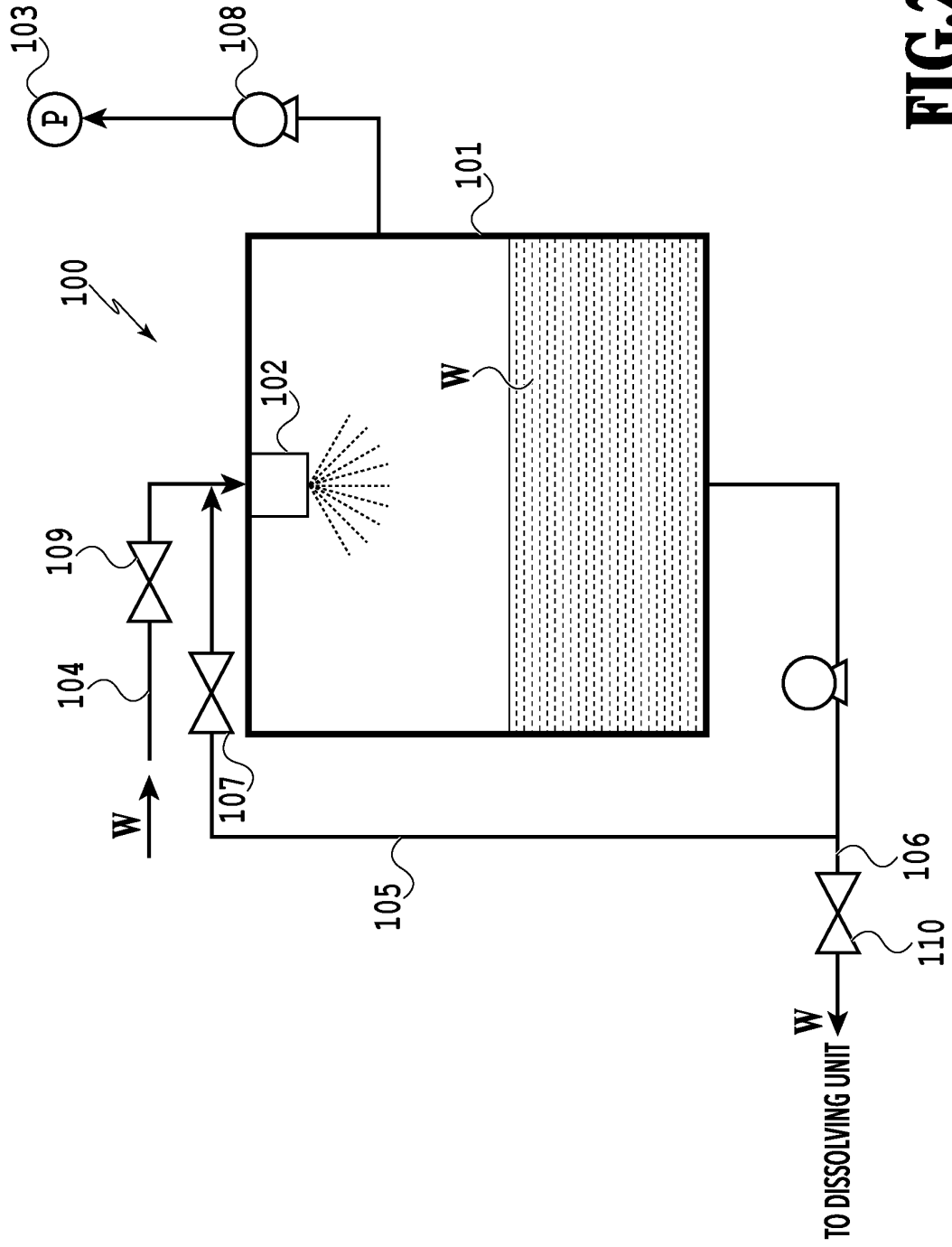
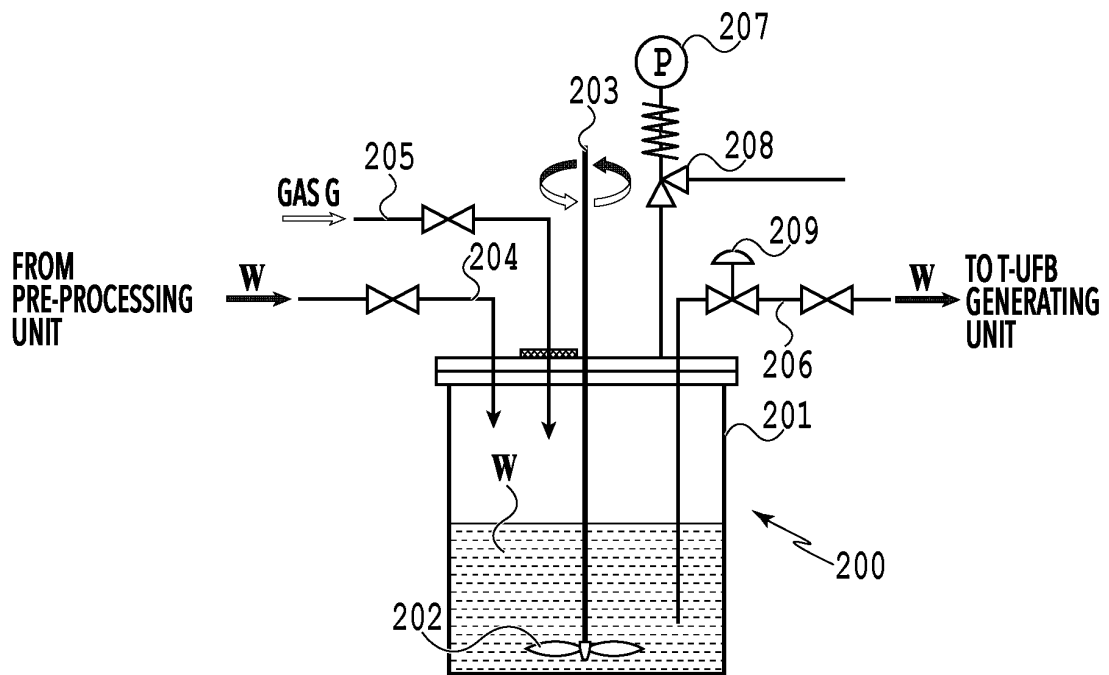
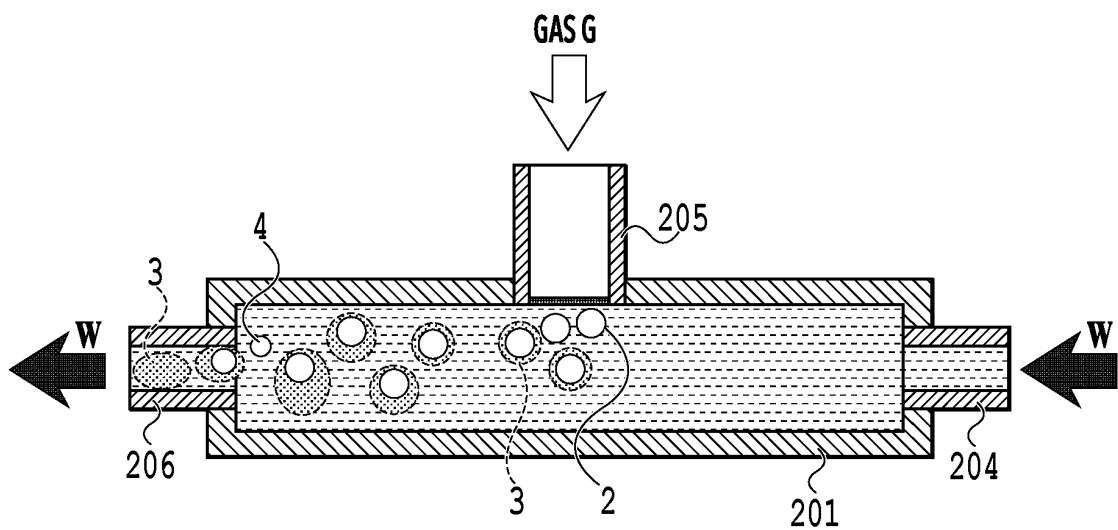
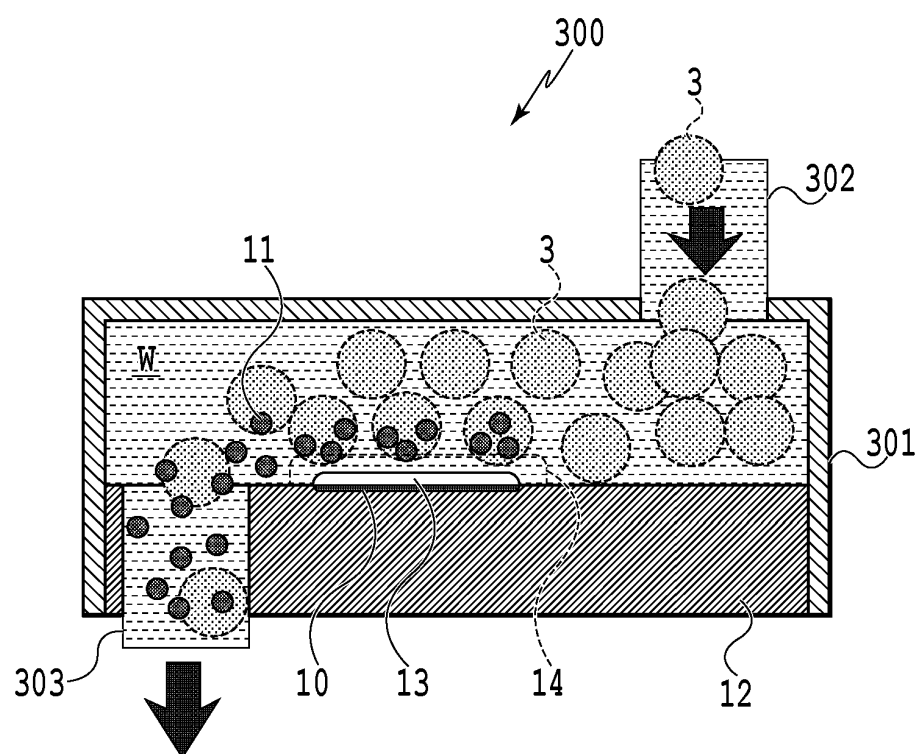




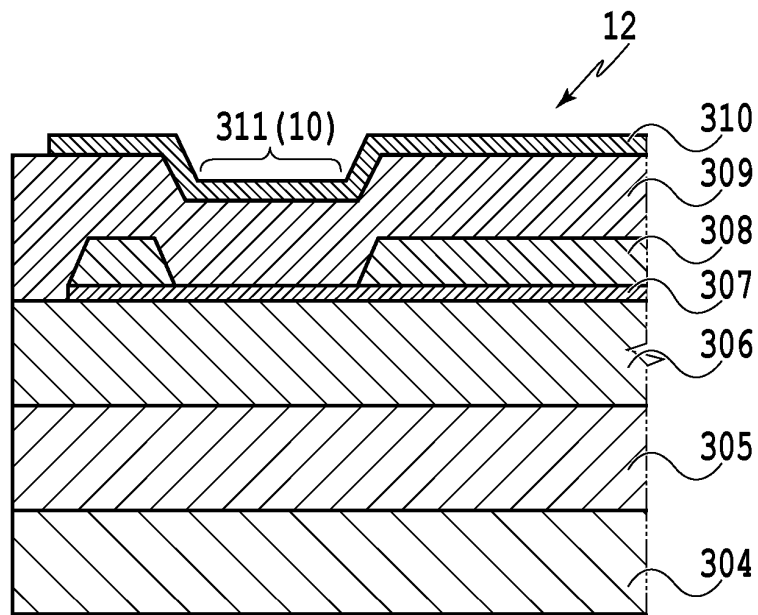
**FIG.1**

**FIG. 2**

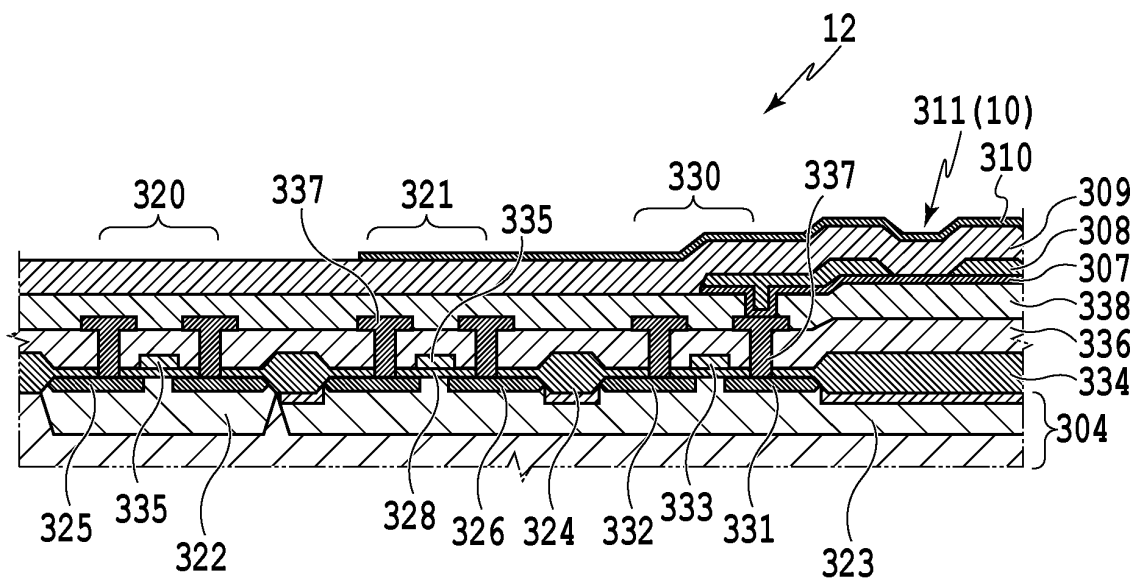
**FIG.3A****FIG.3B**



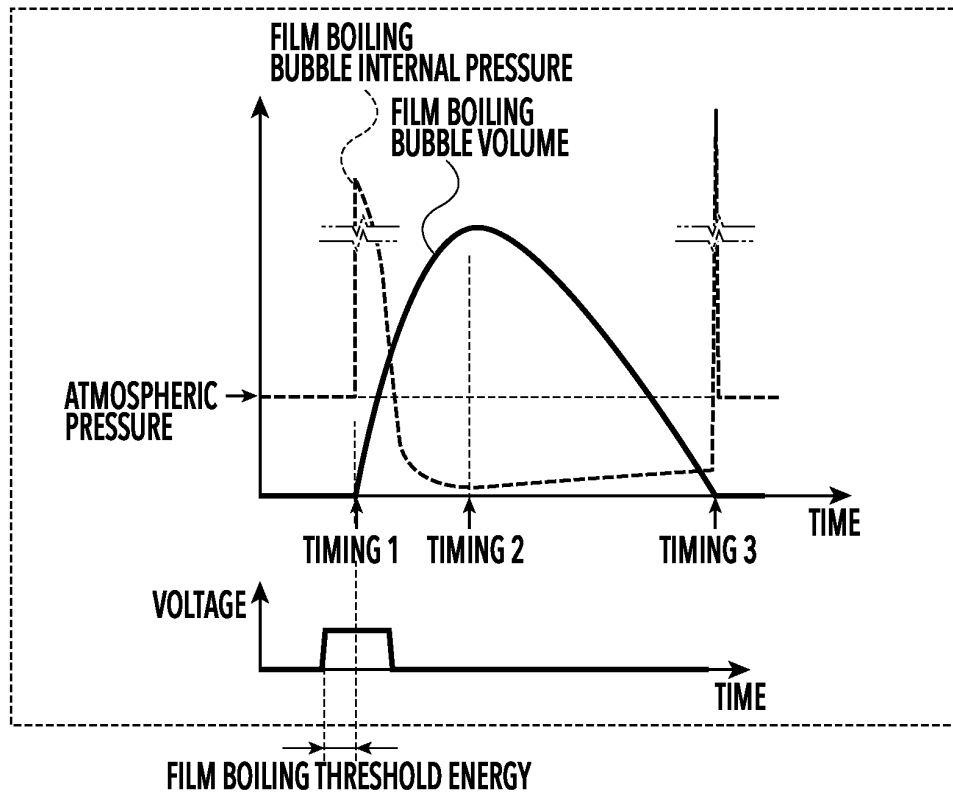
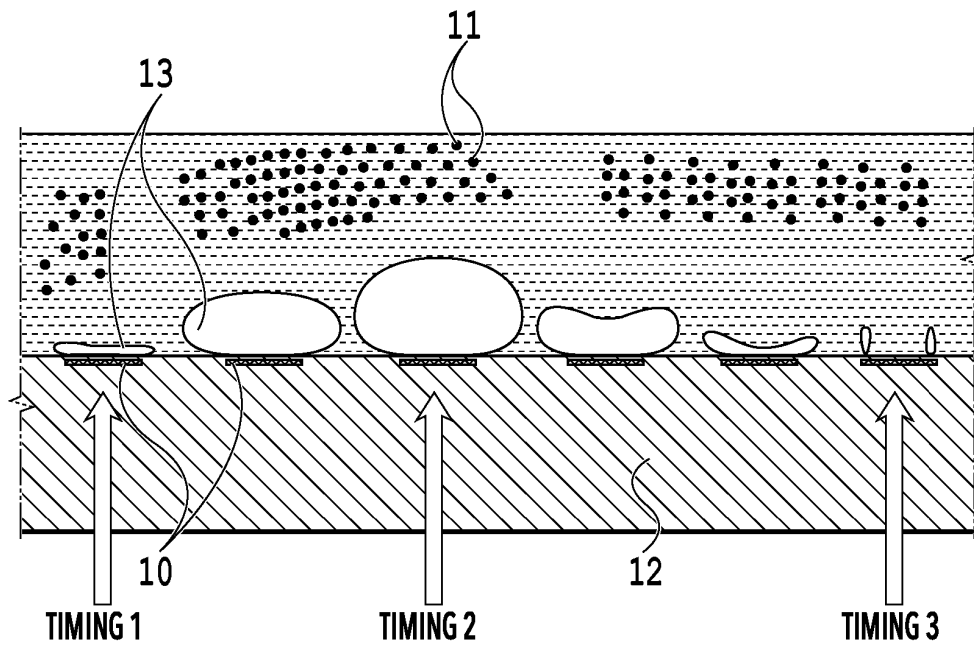
**FIG.4**



**FIG.5A**

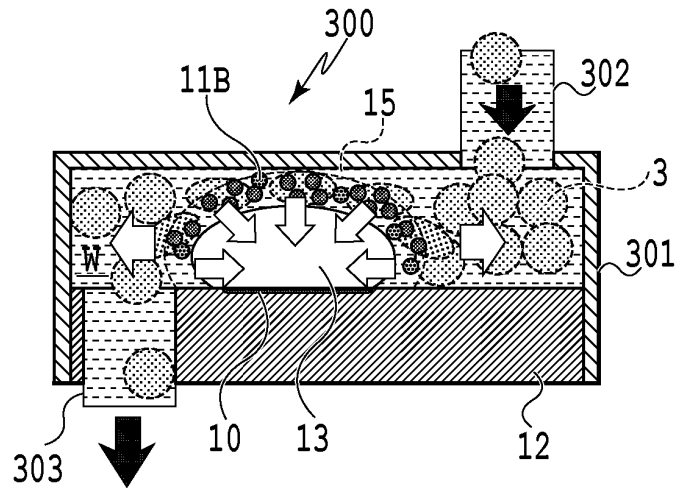


**FIG.5B**

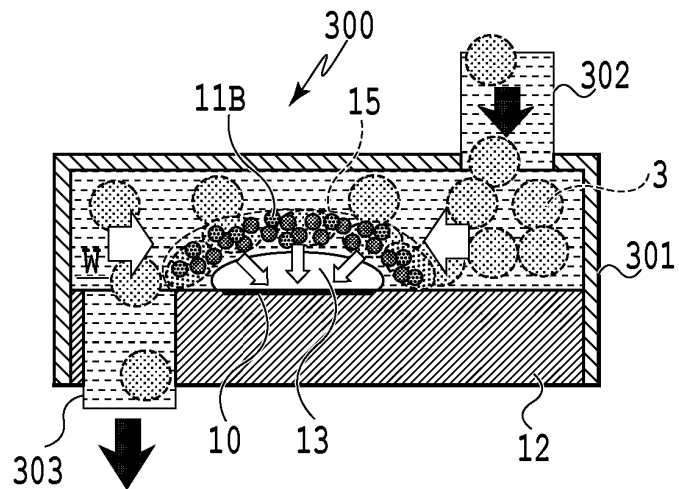
**FIG.6A****FIG.6B**



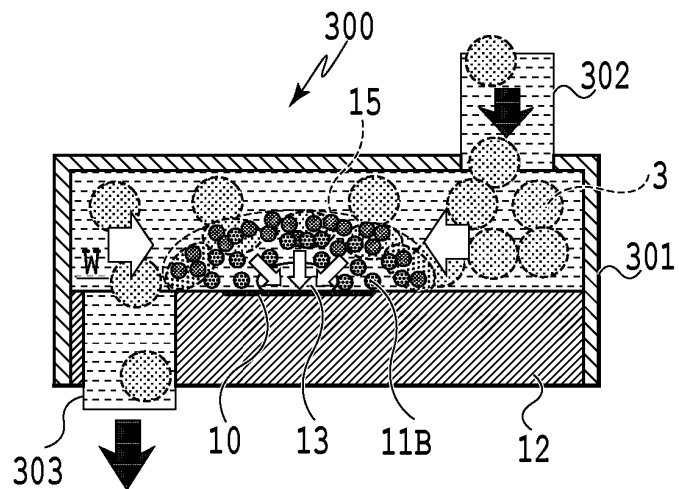
**FIG.8A**



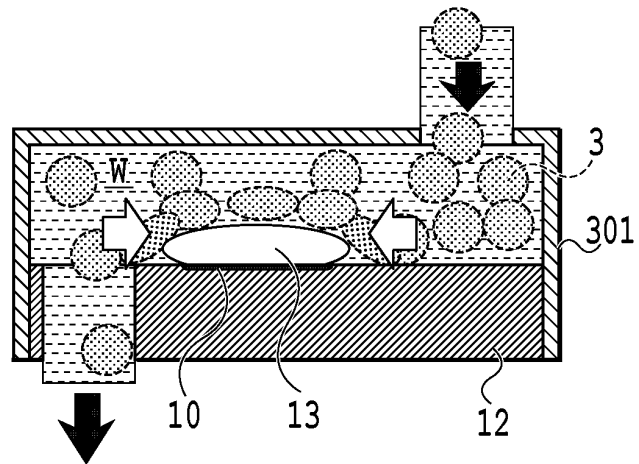
**FIG.8B**



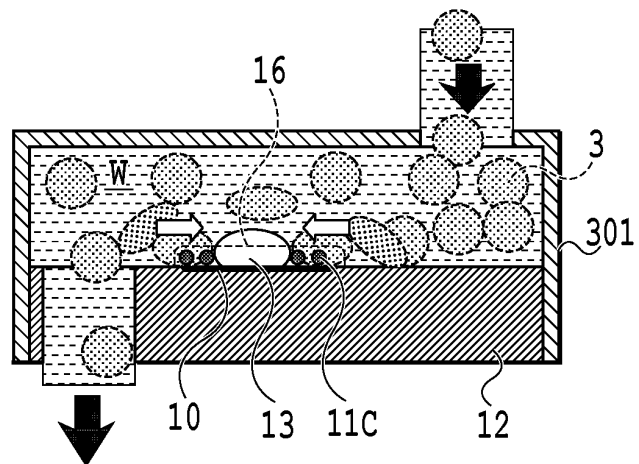
**FIG.8C**



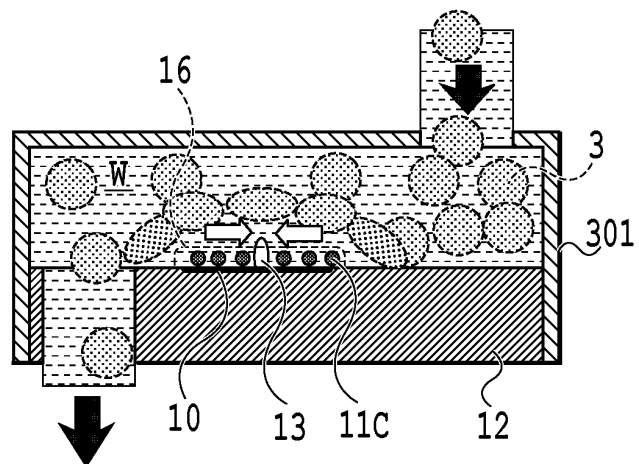
**FIG.9A**



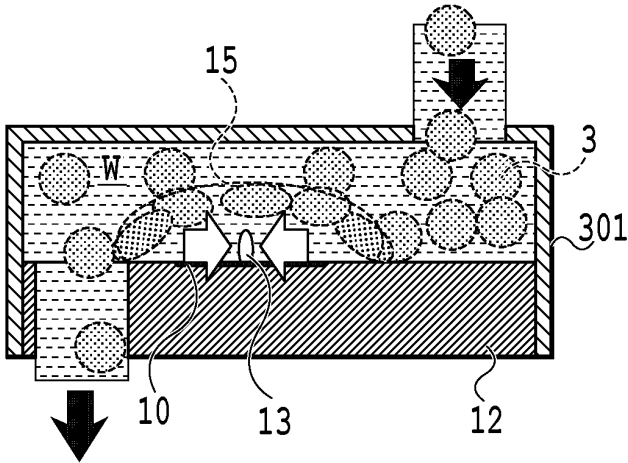
**FIG.9B**



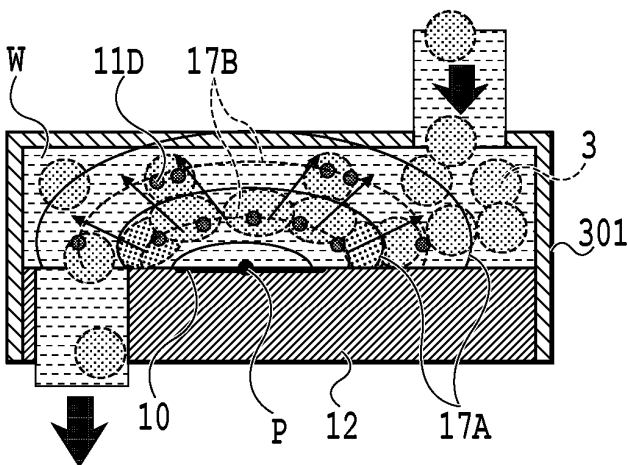
**FIG.9C**

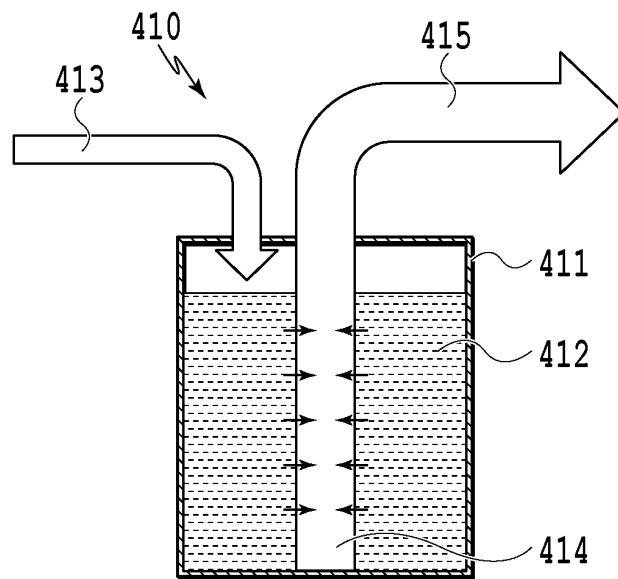


**FIG.10A**

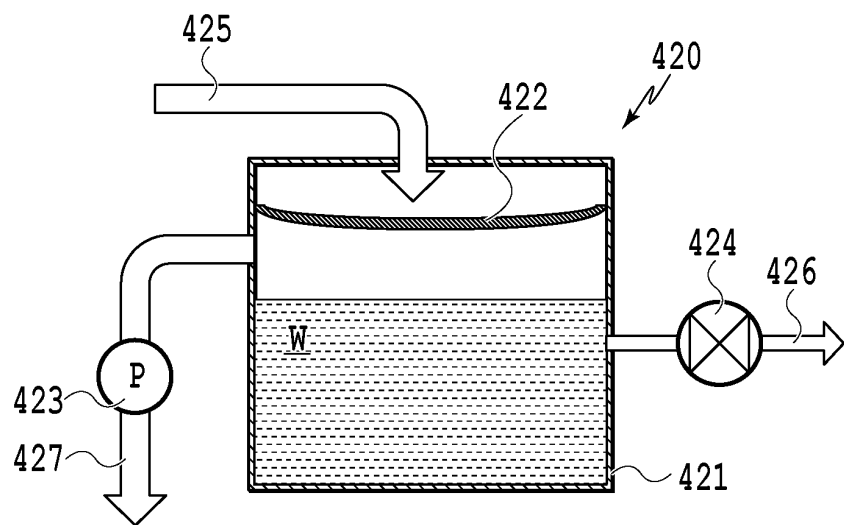


**FIG.10B**

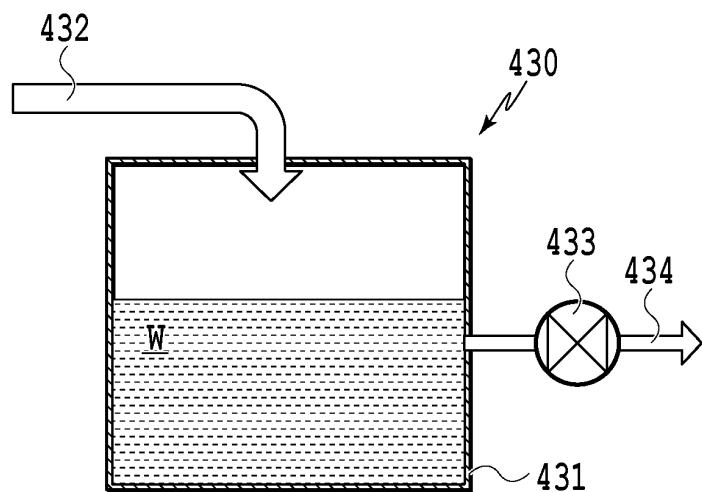




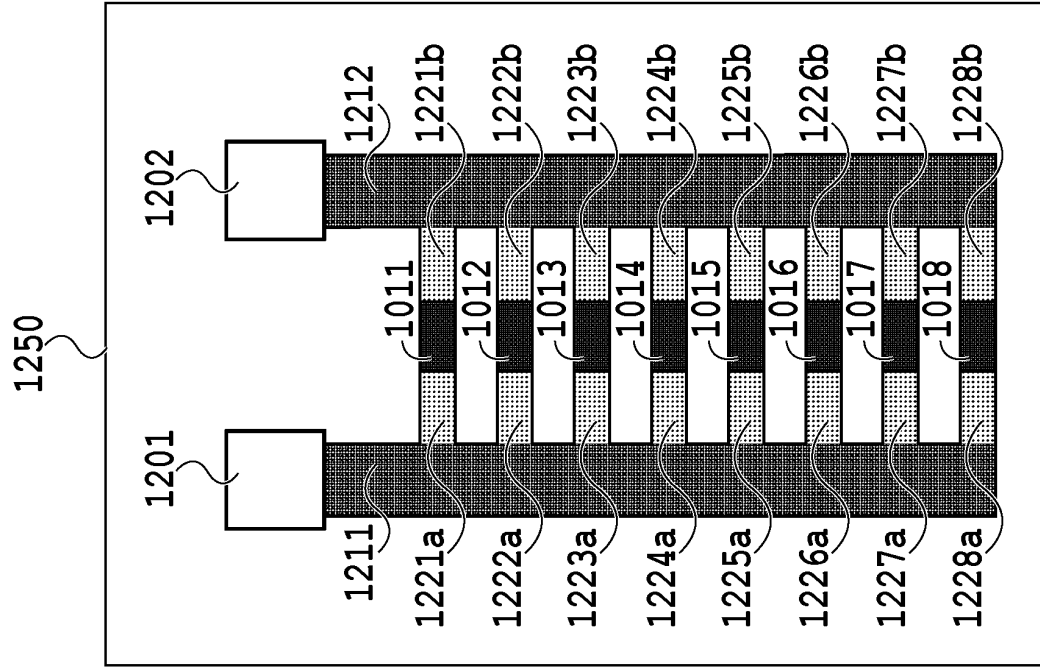
**FIG.11A**



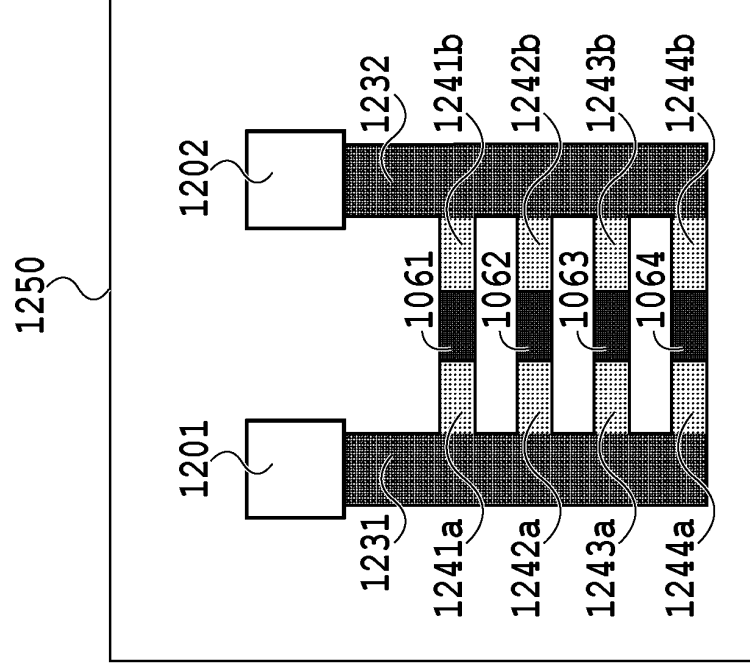
**FIG.11B**



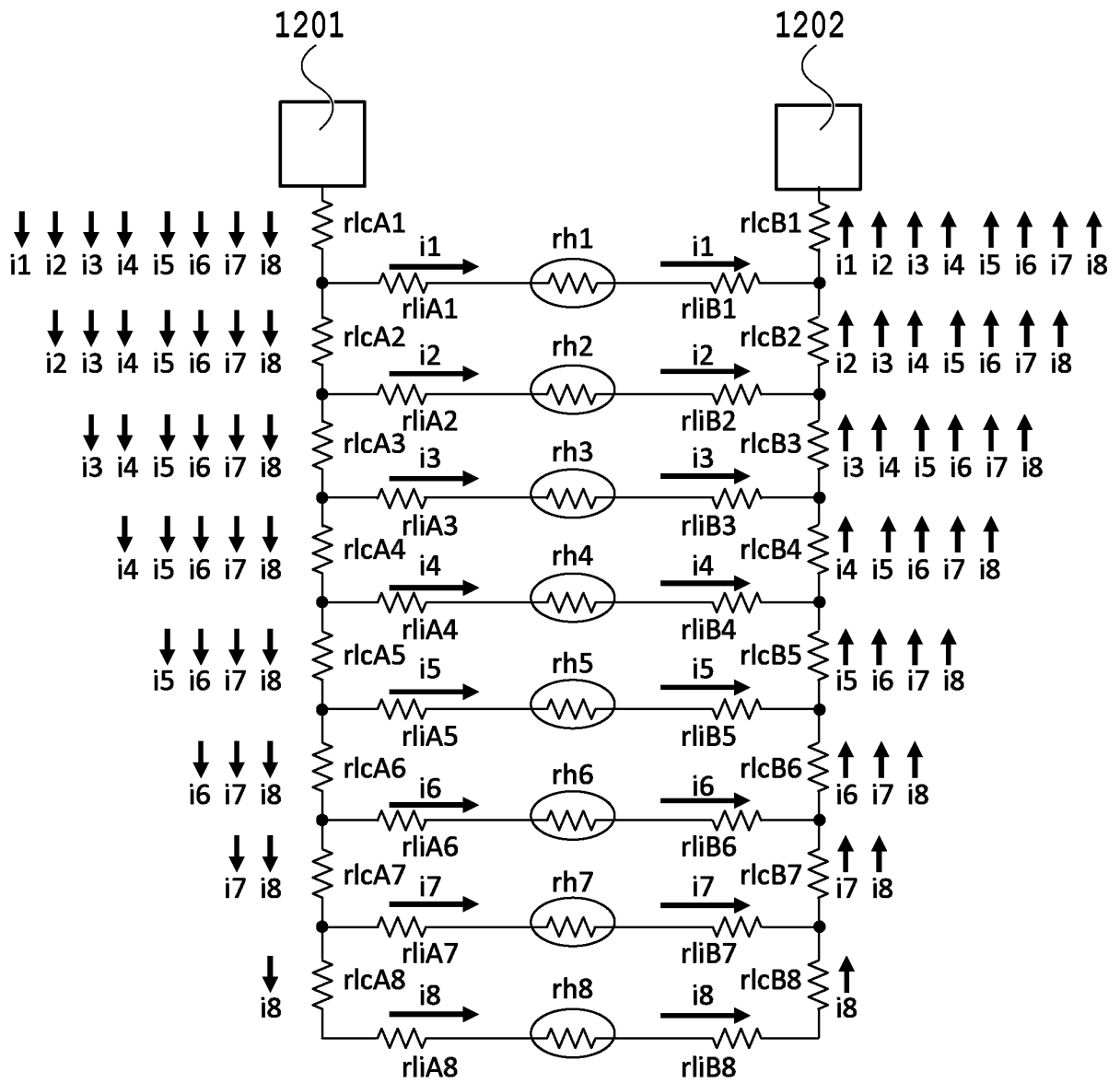
**FIG.11C**

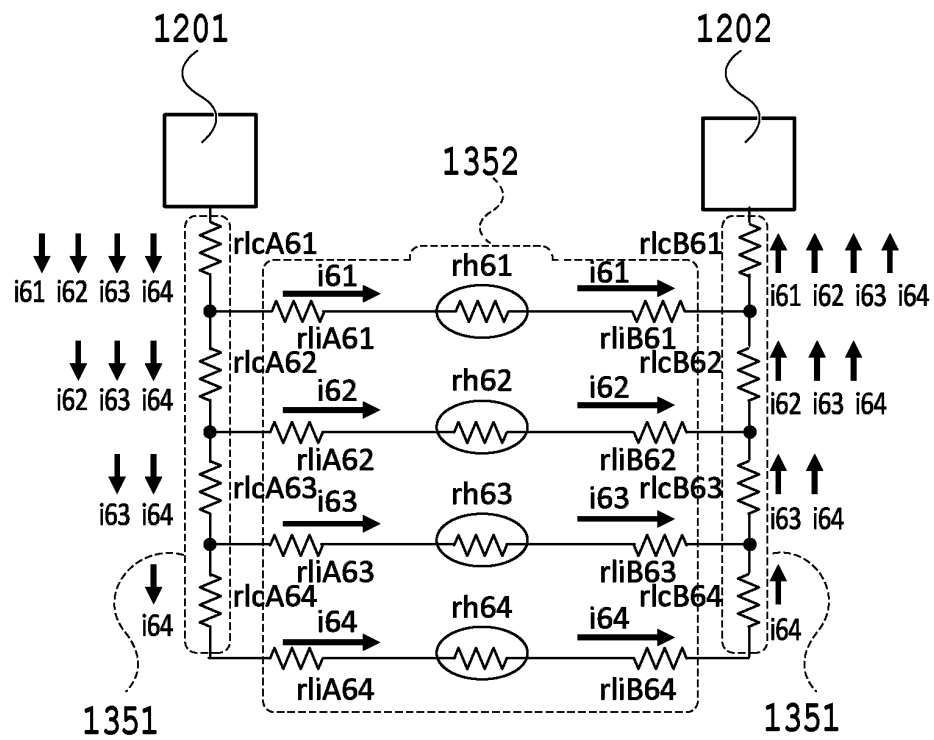


**FIG. 12A**

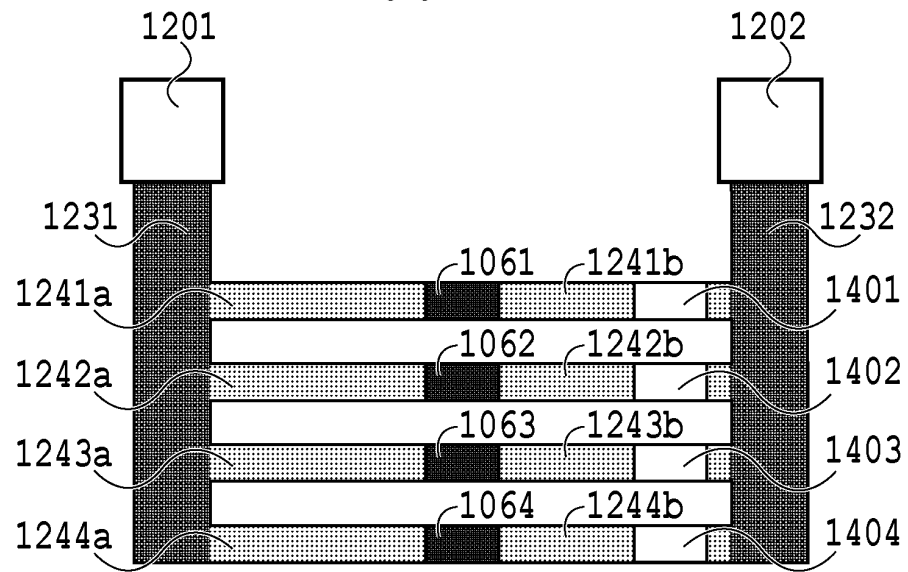


**FIG. 12B**

**FIG.13A**

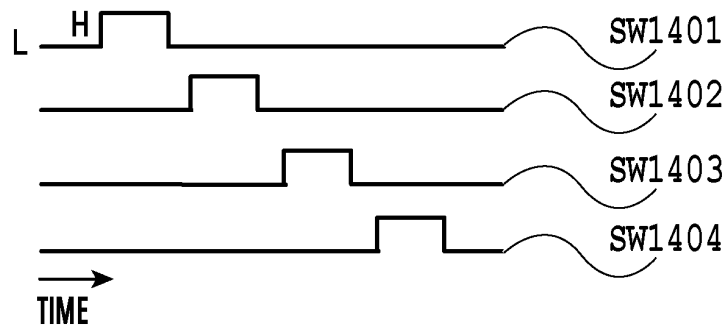
**FIG.13B**

15/34

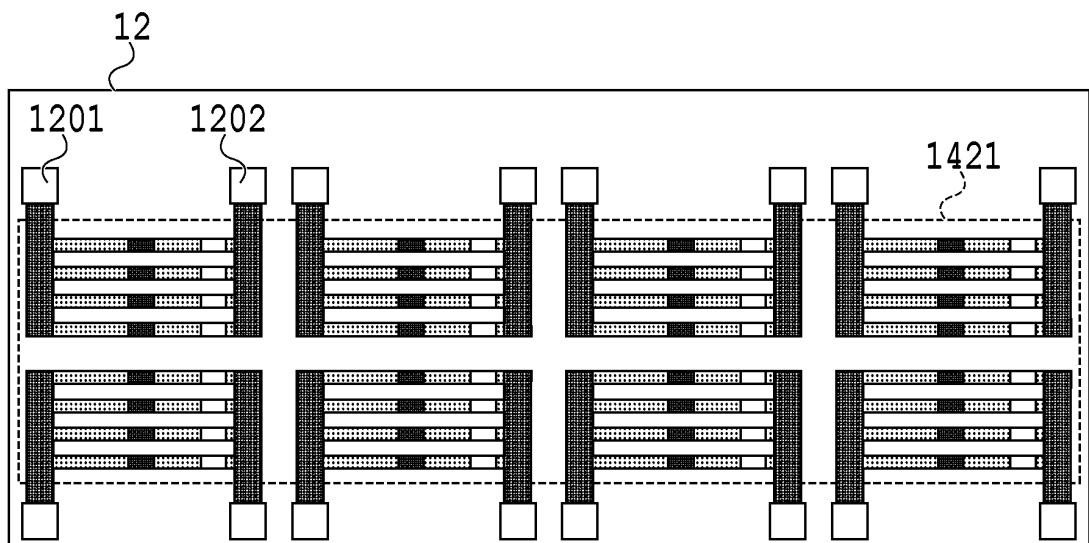


**FIG.14A**

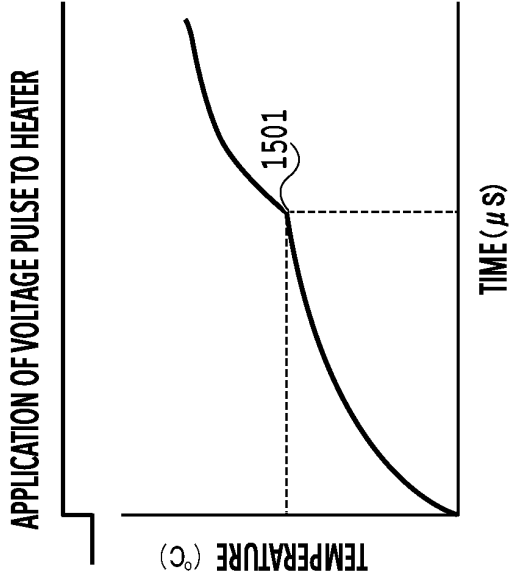
HEATING ELEMENT DRIVING SW LOGIC WAVEFORM



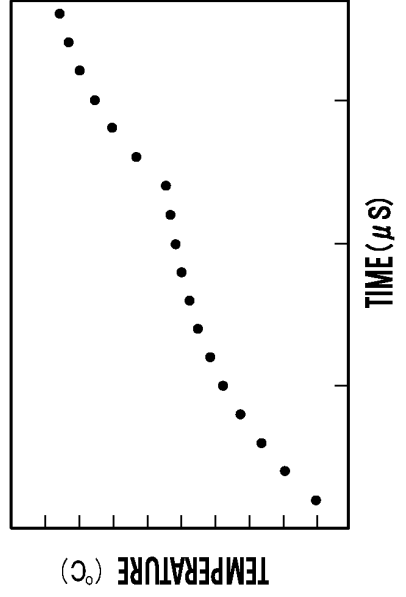
**FIG.14B**



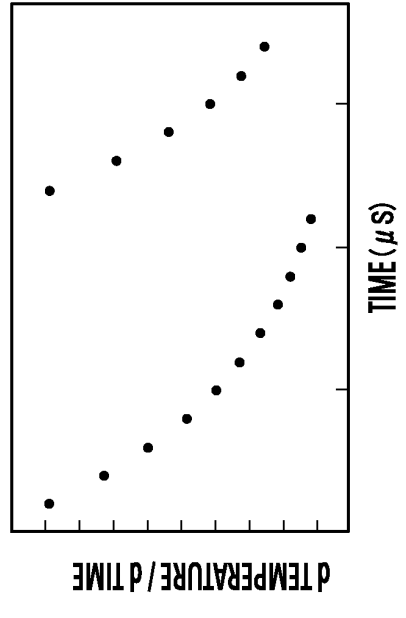
**FIG.14C**



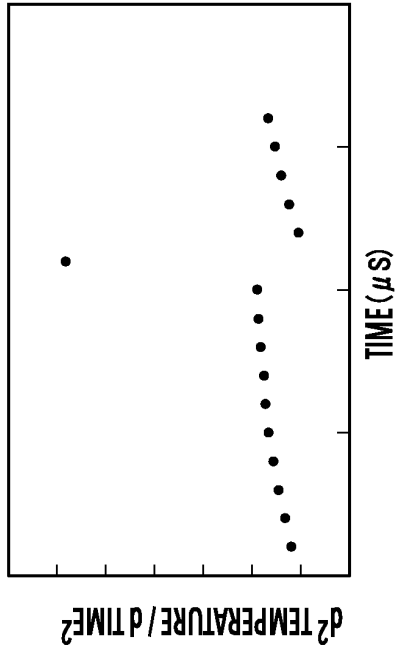
**FIG.15A**



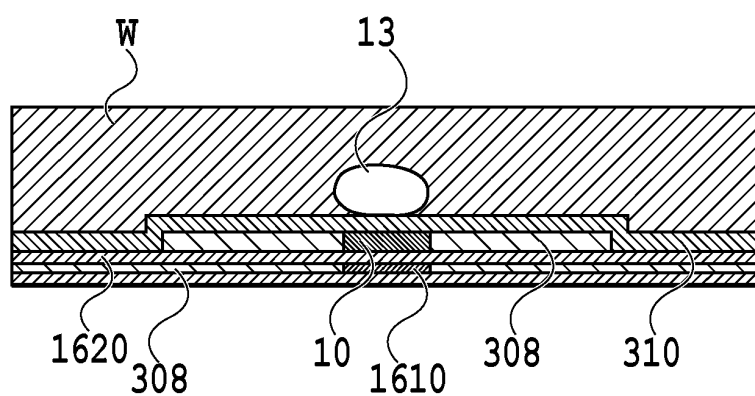
**FIG.15B**



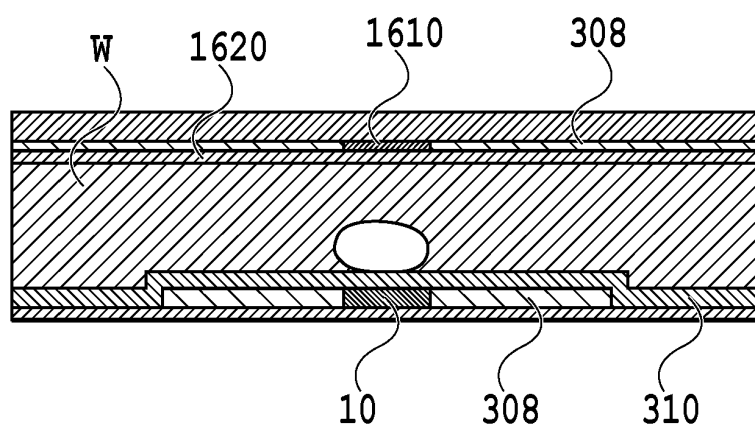
**FIG.15C**



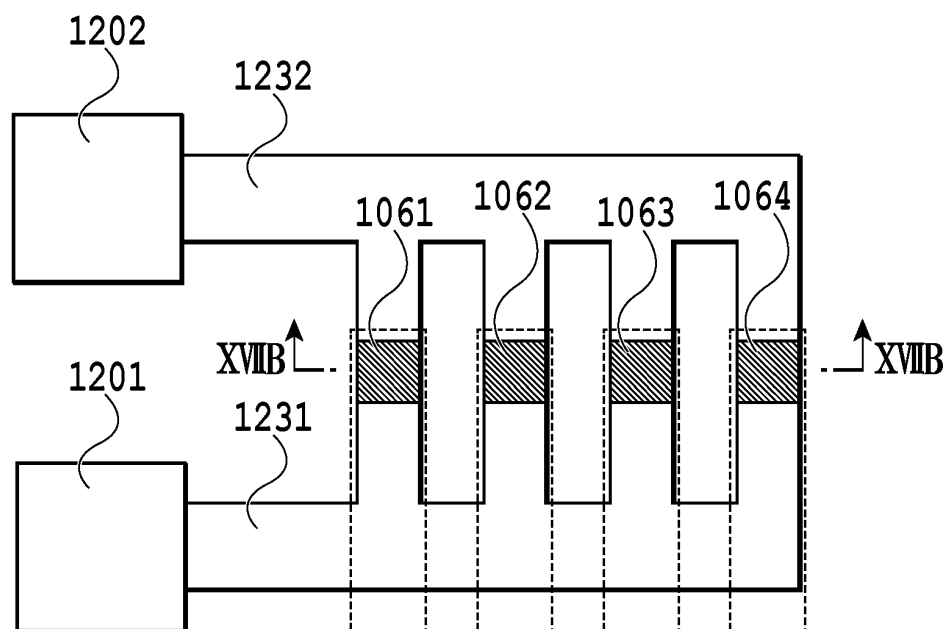
**FIG.15D**



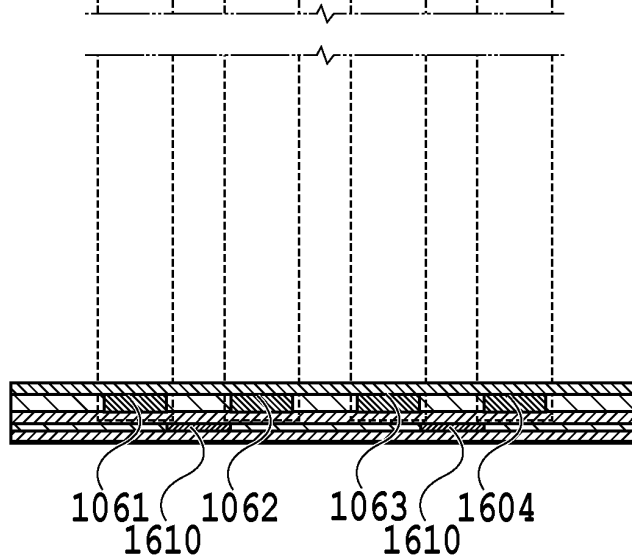
**FIG.16A**



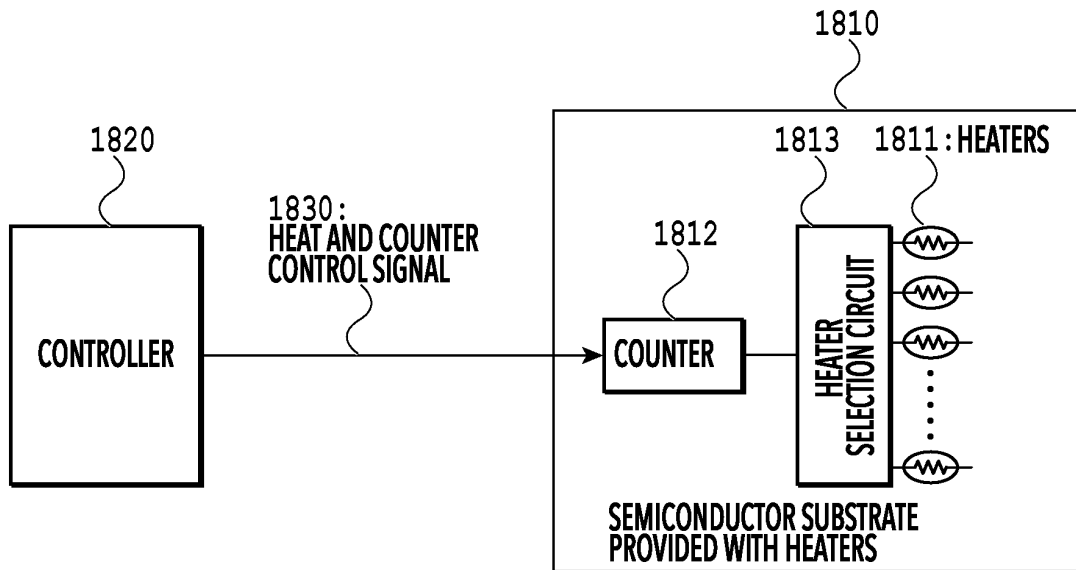
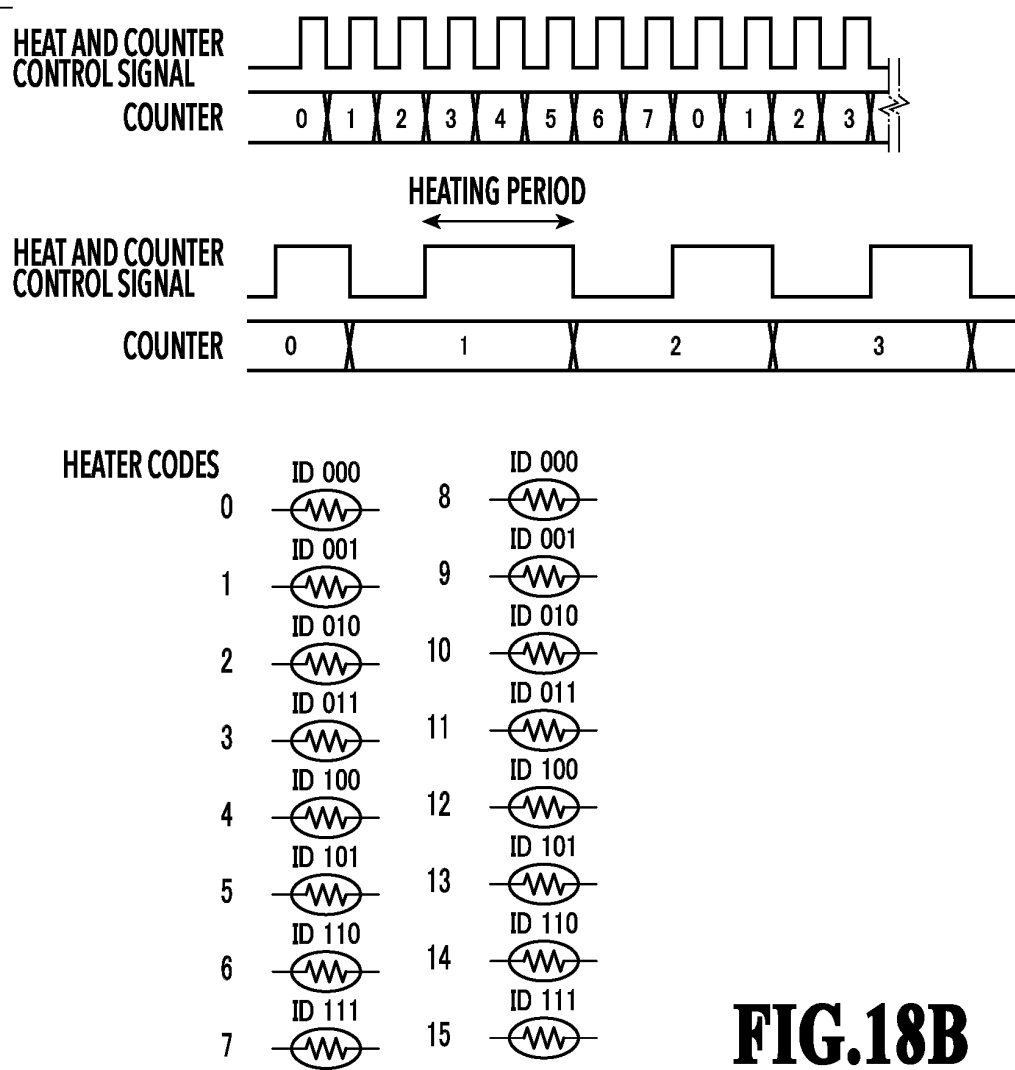
**FIG.16B**

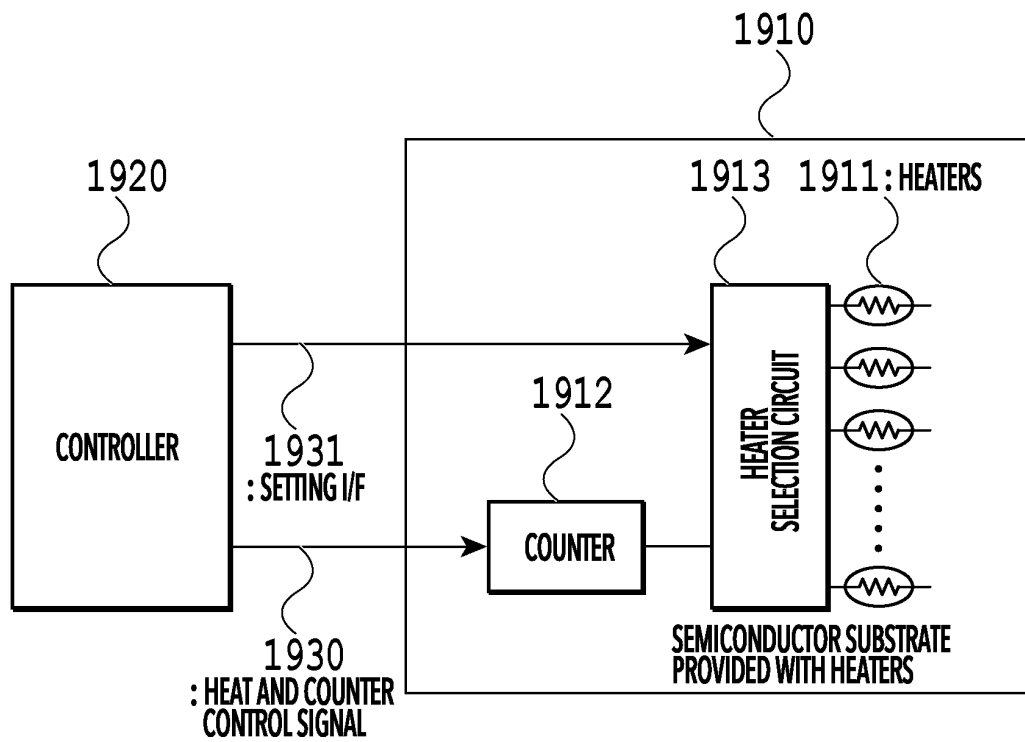
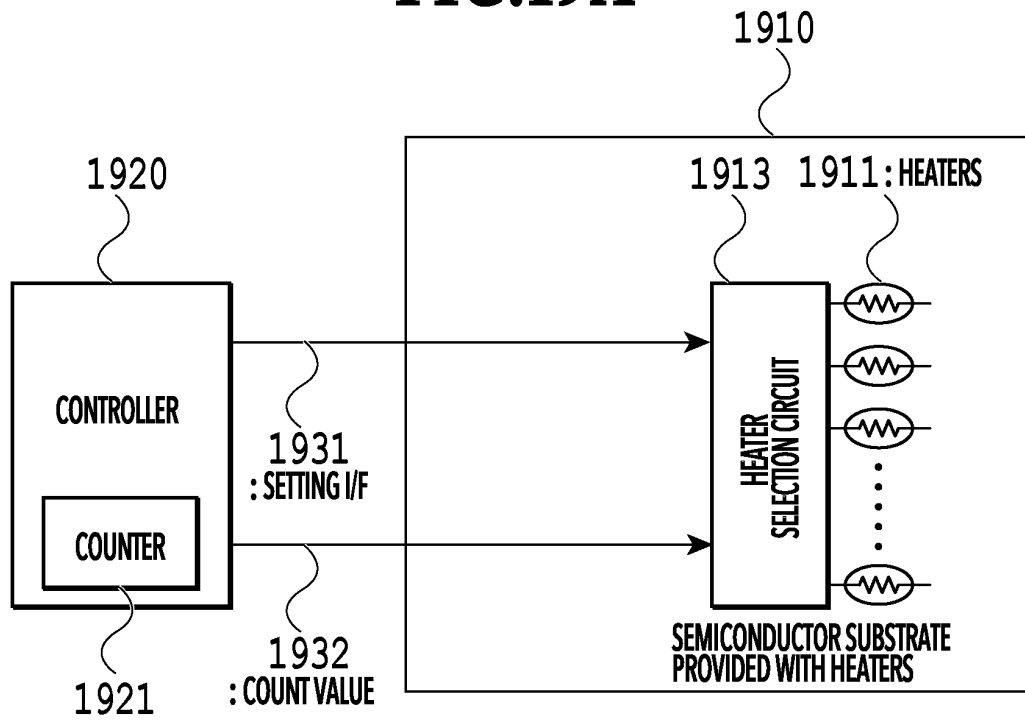


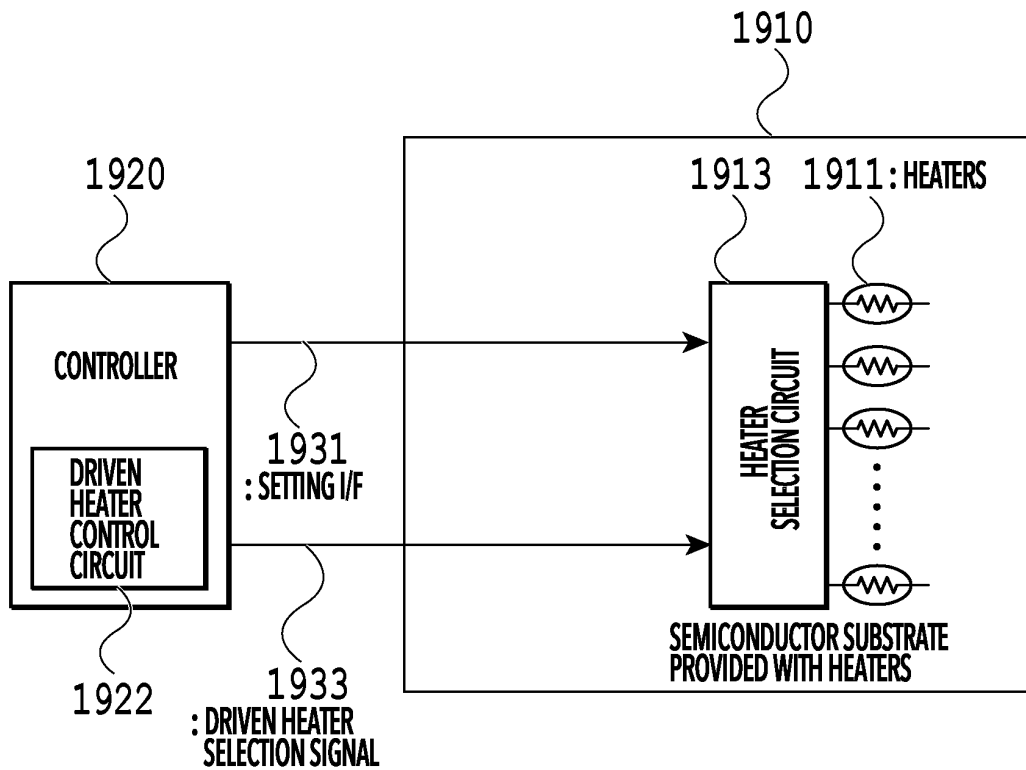
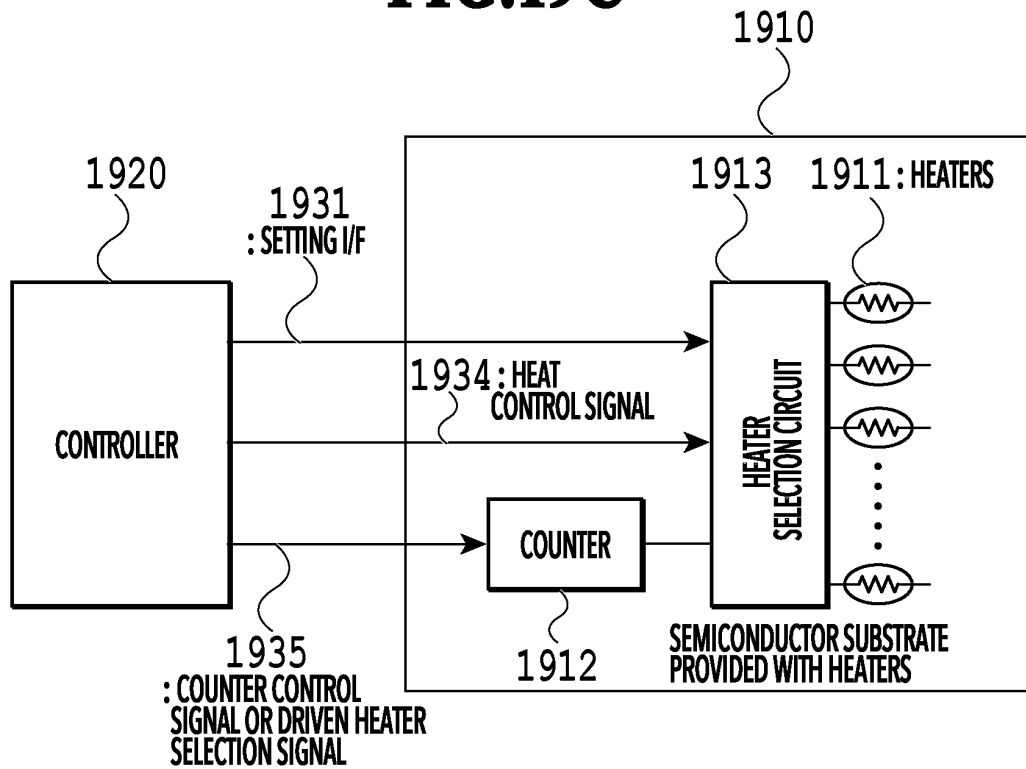
**FIG.17A**



**FIG.17B**

**FIG.18A****FIG.18B**

**FIG.19A****FIG.19B**

**FIG.19C****FIG.19D**

| COUNT<br>VALUE | DRIVEN<br>HEATER<br>SELECTION<br>NUMBER |
|----------------|-----------------------------------------|
| 0              | 1                                       |
| 1              | 6                                       |
| 2              | 7                                       |
| 3              | 3                                       |
| 4              | 2                                       |
| 5              | 5                                       |
| 6              | 0                                       |
| 7              | 4                                       |

DRIVEN HEATER  
SELECTION TABLE

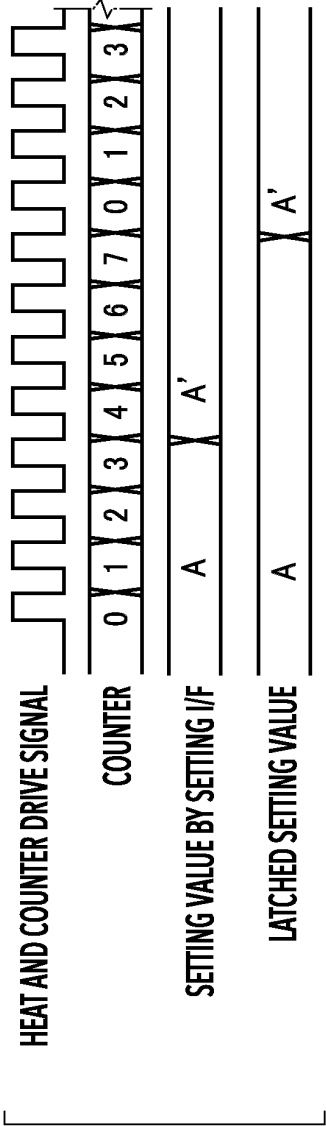


FIG.20A

FIG.20B

## HEATER CODES

|    |        |                                                                                     |
|----|--------|-------------------------------------------------------------------------------------|
| 0  | ID 000 |    |
| 1  | ID 001 |    |
| 2  | ID 010 |    |
| 3  | ID 011 |    |
| 4  | ID 100 |    |
| 5  | ID 101 |    |
| 6  | ID 110 |   |
| 7  | ID 111 |  |
| 8  | ID 000 |  |
| 9  | ID 001 |  |
| 10 | ID 010 |  |
| 11 | ID 011 |  |
| 12 | ID 100 |  |
| 13 | ID 101 |  |
| 14 | ID 110 |  |
| 15 | ID 111 |  |

## HEATER ID

| bit2 | bit1 | bit0 |
|------|------|------|
| 0    | 0    | 0    |
| 0    | 0    | 1    |
| 0    | 1    | 0    |
| 0    | 1    | 1    |
| 1    | 0    | 0    |
| 1    | 0    | 1    |
| 1    | 1    | 0    |
| 1    | 1    | 1    |

## COUNTER

| bit2 | bit1 | bit0 |
|------|------|------|
| 0    | 0    | 0    |
| 0    | 0    | 1    |
| 0    | 1    | 0    |
| 0    | 1    | 1    |
| 1    | 0    | 0    |
| 1    | 0    | 1    |
| 1    | 1    | 0    |
| 1    | 1    | 1    |

2101

## FIRST DRIVING EXAMPLE

## HEATER ID

| bit2 | bit1 | bit0 |
|------|------|------|
| 0    | 0    | 0    |
| 0    | 0    | 1    |
| 0    | 1    | 0    |
| 0    | 1    | 1    |
| 1    | 0    | 0    |
| 1    | 0    | 1    |
| 1    | 1    | 0    |
| 1    | 1    | 1    |

## COUNTER

| bit2 | bit1 | bit0 |
|------|------|------|
| 0    | 0    | 0    |
| 0    | 0    | 1    |
| 0    | 1    | 0    |
| 0    | 1    | 1    |
| 1    | 0    | 0    |
| 1    | 0    | 1    |
| 1    | 1    | 0    |
| 1    | 1    | 1    |

2102

## SECOND DRIVING EXAMPLE

## HEATER ID

| bit2 | bit1 | bit0 |
|------|------|------|
| 0    | 0    | 0    |
| 0    | 0    | 1    |
| 0    | 1    | 0    |
| 0    | 1    | 1    |
| 1    | 0    | 0    |
| 1    | 0    | 1    |
| 1    | 1    | 0    |
| 1    | 1    | 1    |

## COUNTER

| bit2 | bit1 | bit0 |
|------|------|------|
| 0    | 0    | 0    |
| 0    | 0    | 1    |
| 0    | 1    | 0    |
| 0    | 1    | 1    |
| 1    | 0    | 0    |
| 1    | 0    | 1    |
| 1    | 1    | 0    |
| 1    | 1    | 1    |

2103

## THIRD DRIVING EXAMPLE

FIG.21

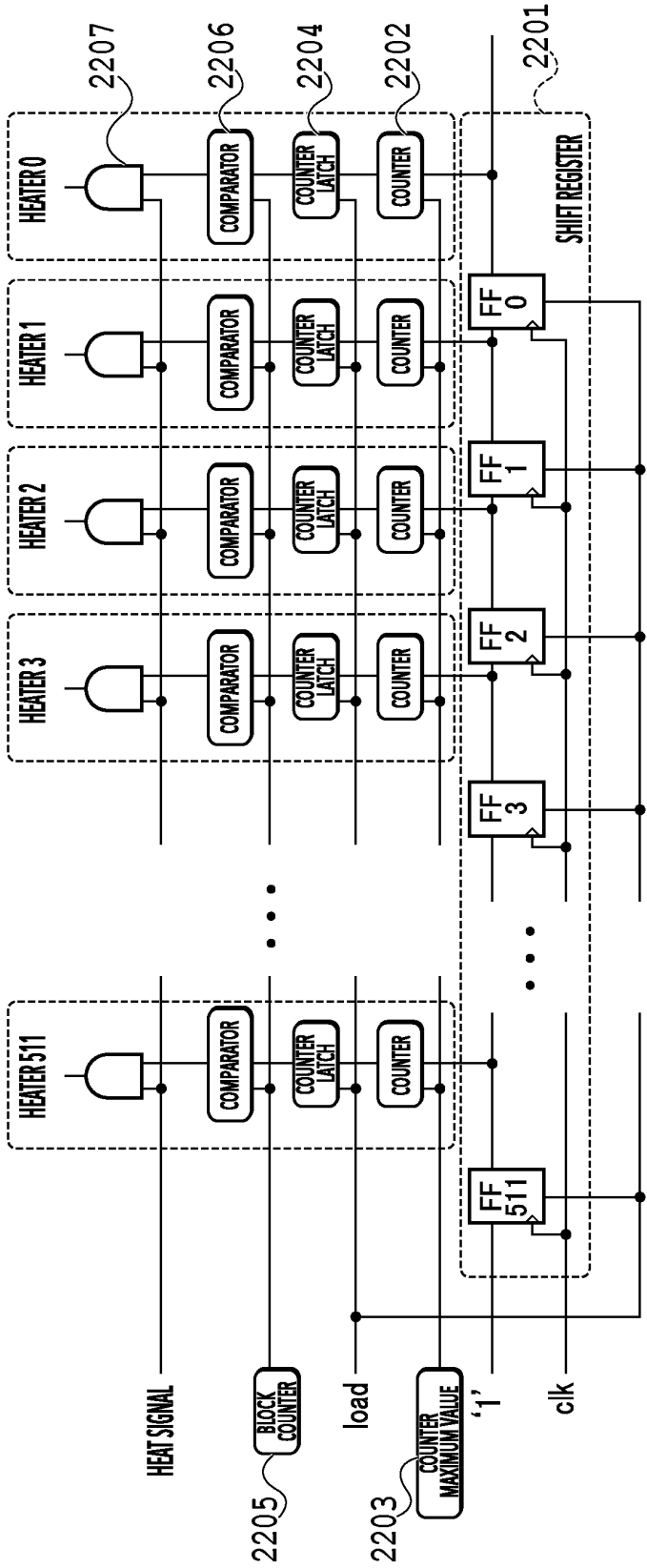


FIG. 22A

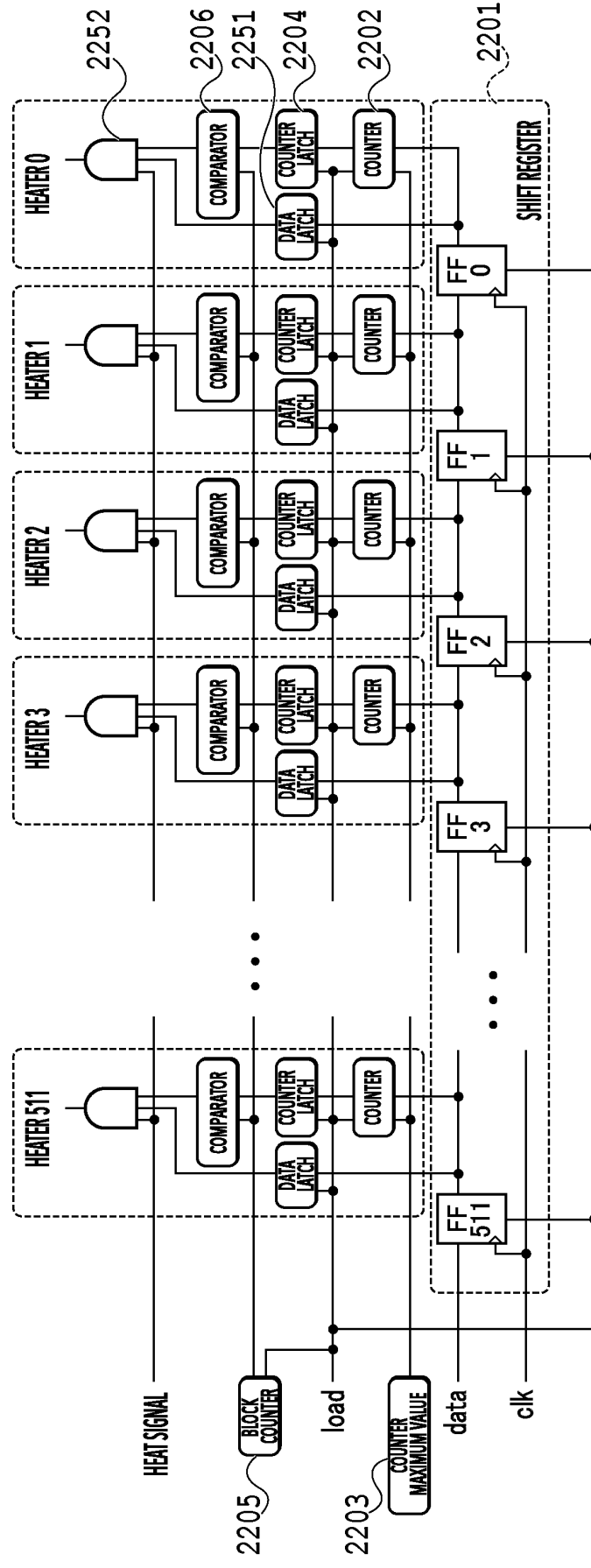


FIG.22B

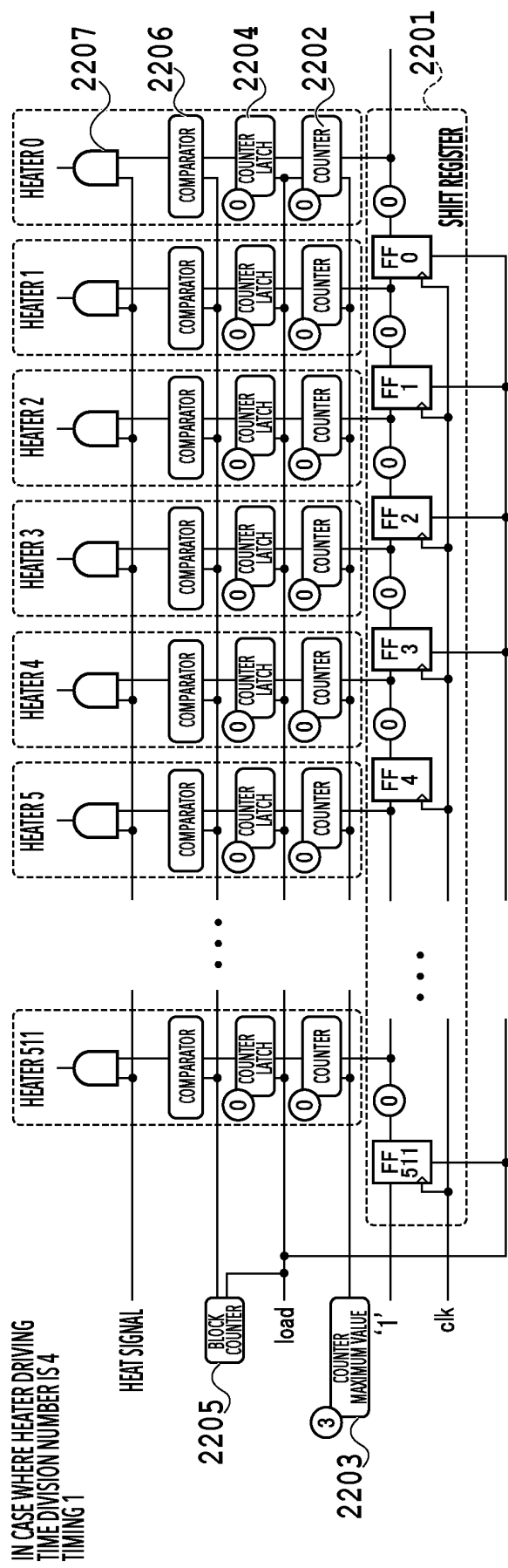
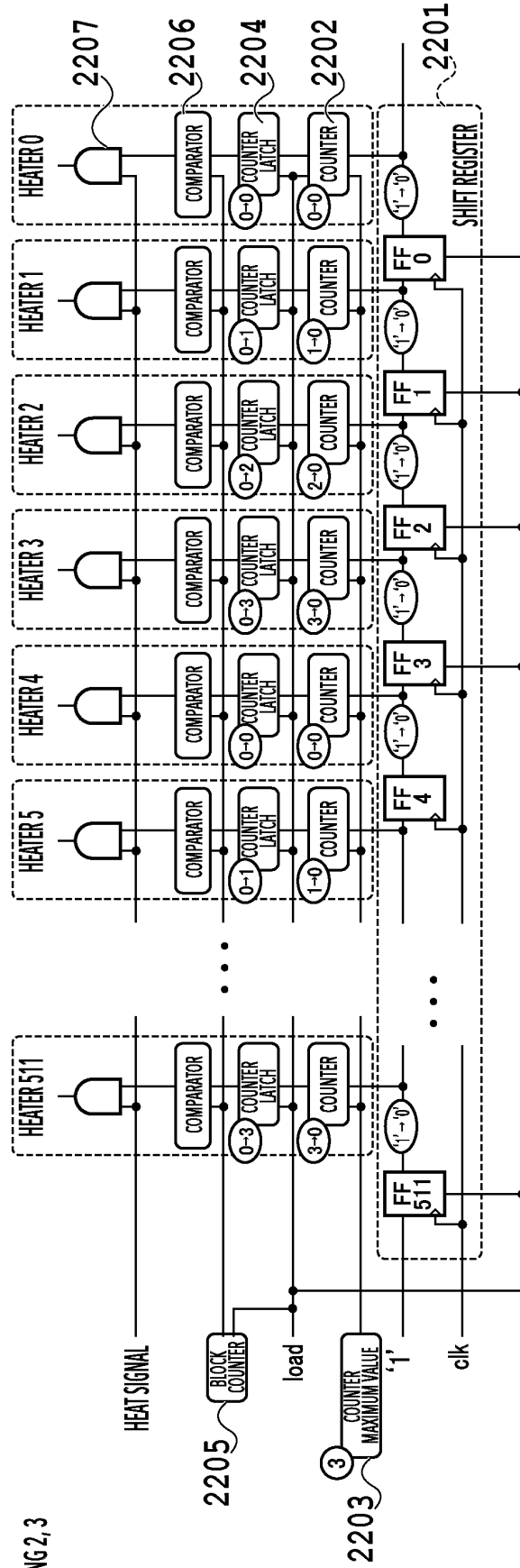


FIG.23A

**IN CASE WHERE HEATER DRIVING  
TIME DIVISION NUMBER IS 4**

## TIMING 2, 3



**FIG. 23B**

┌

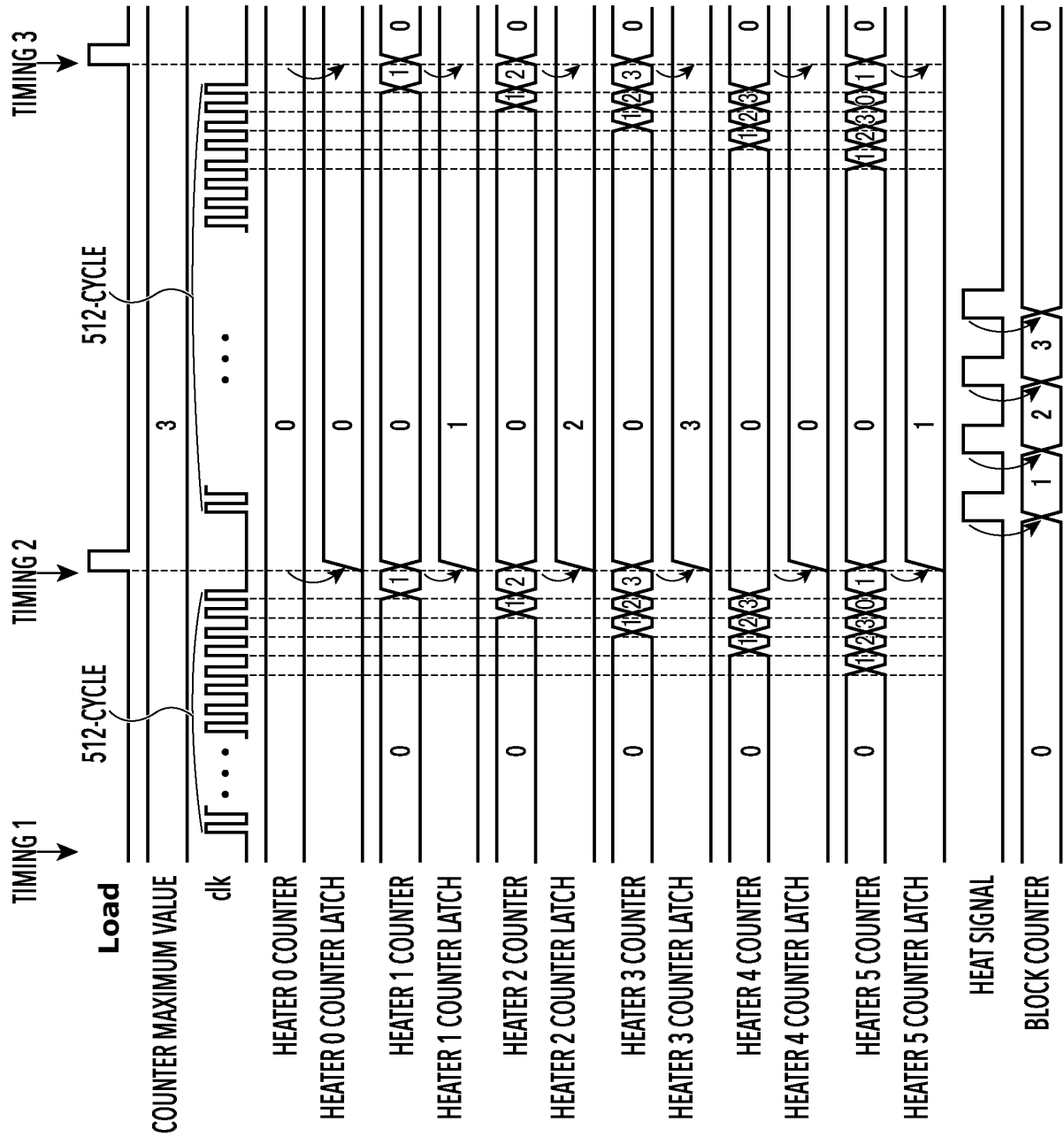


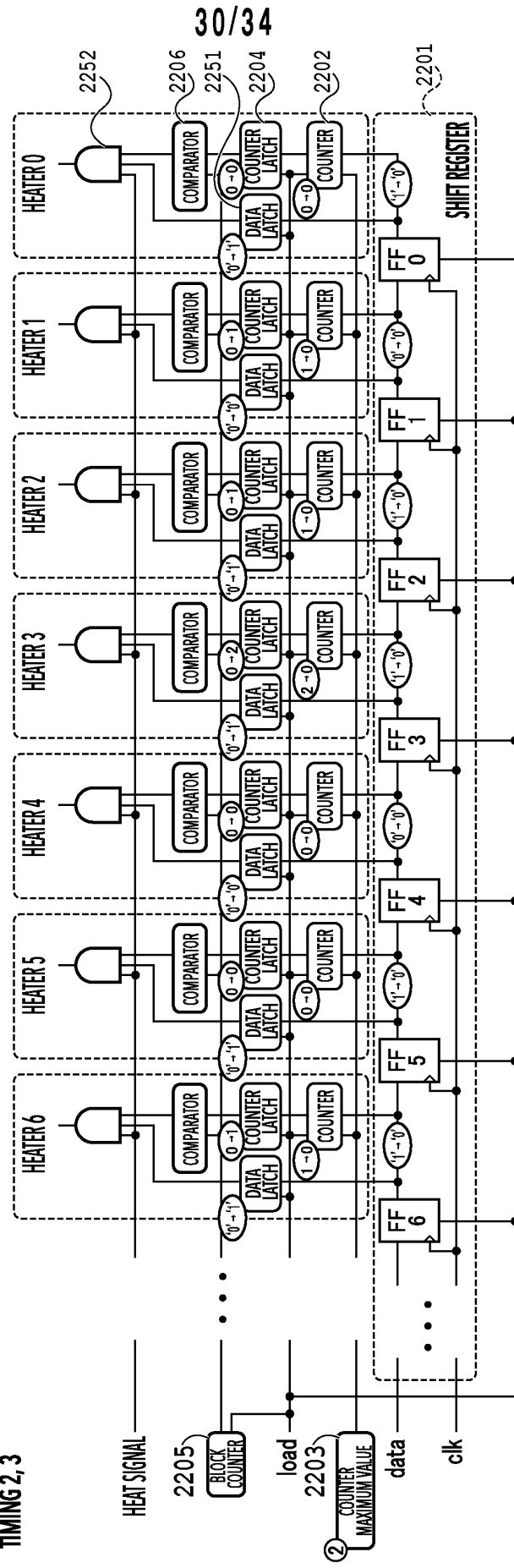
FIG.24

## TIMING 1

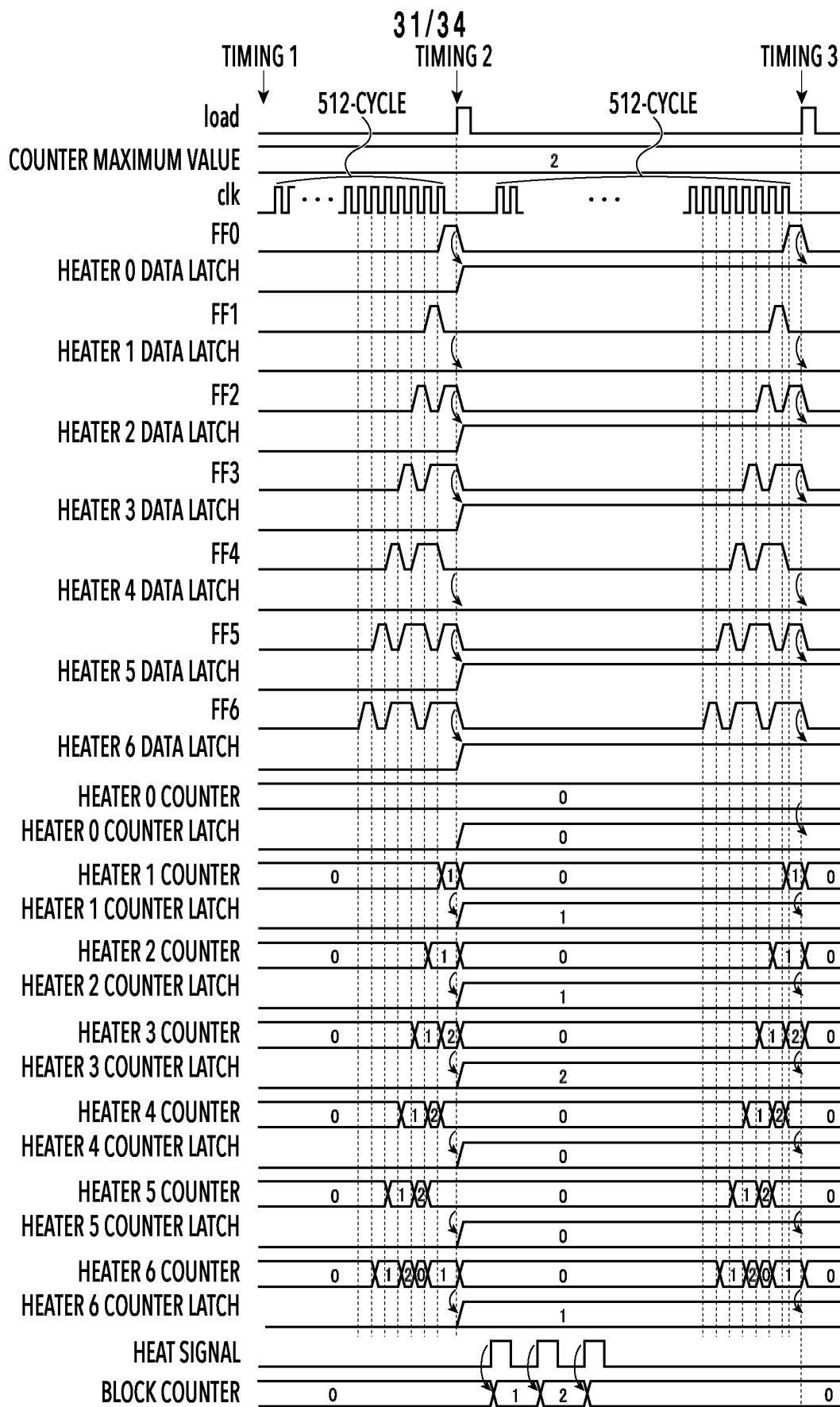


IN CASE WHERE HEATER DRIVING TIME DIVISION NUMBER IS  
3 AND HEATER 1 AND HEATER 4 ARE DISABLED

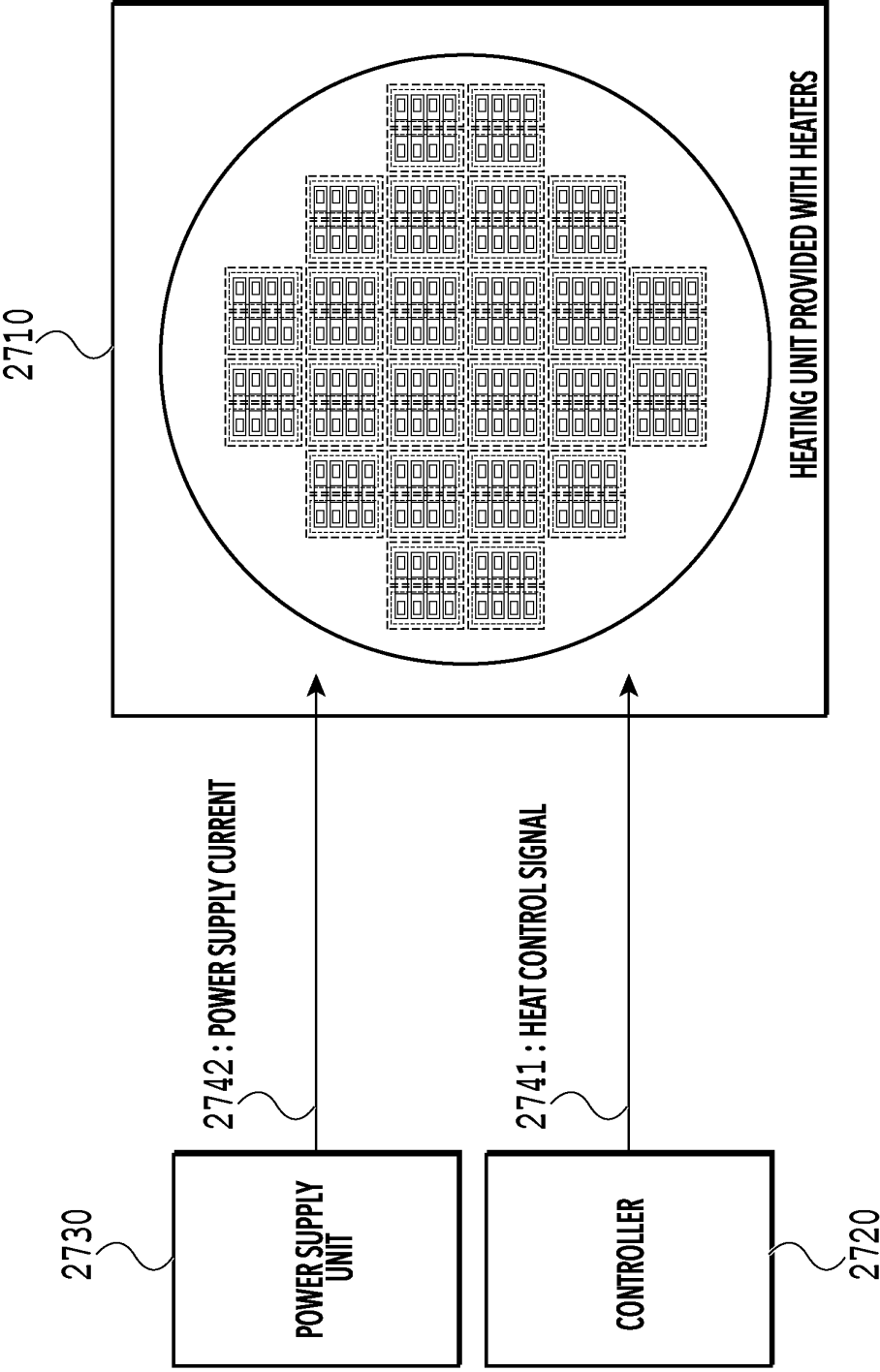
TIMING 2, 3



**FIG.25B**



**FIG.26**



**FIG.27**

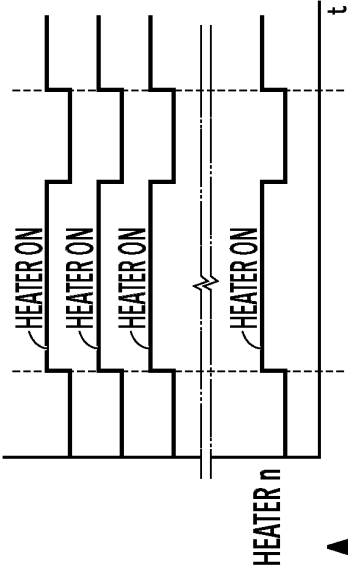


FIG.28A

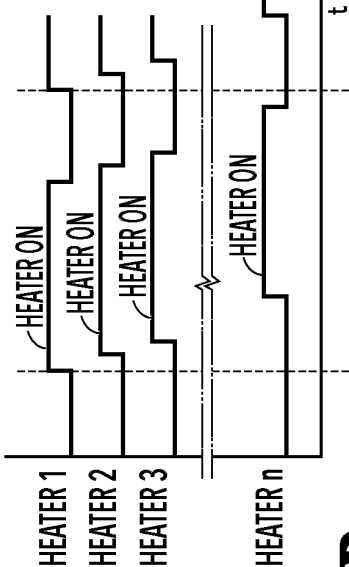


FIG.28B

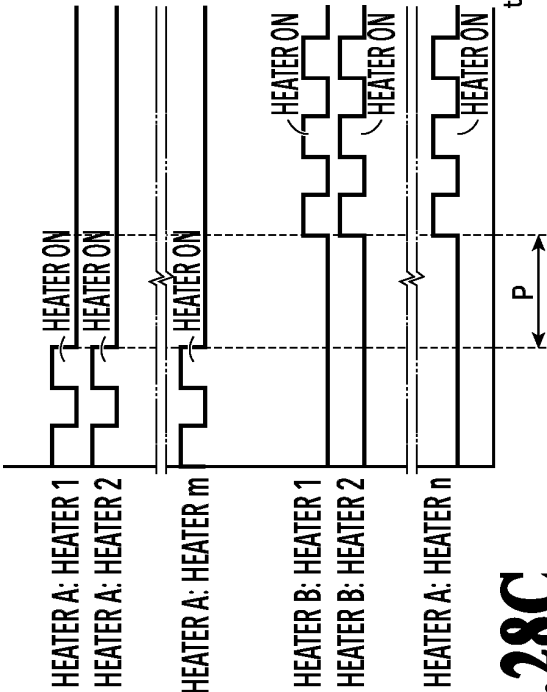


FIG.28C

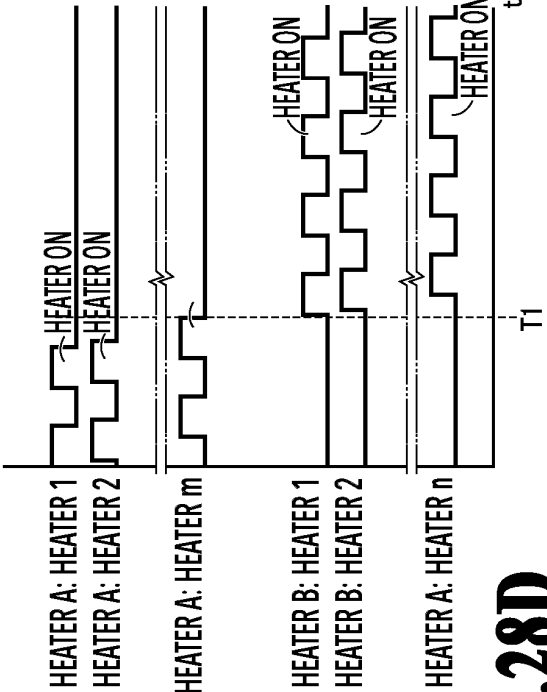


FIG.28D

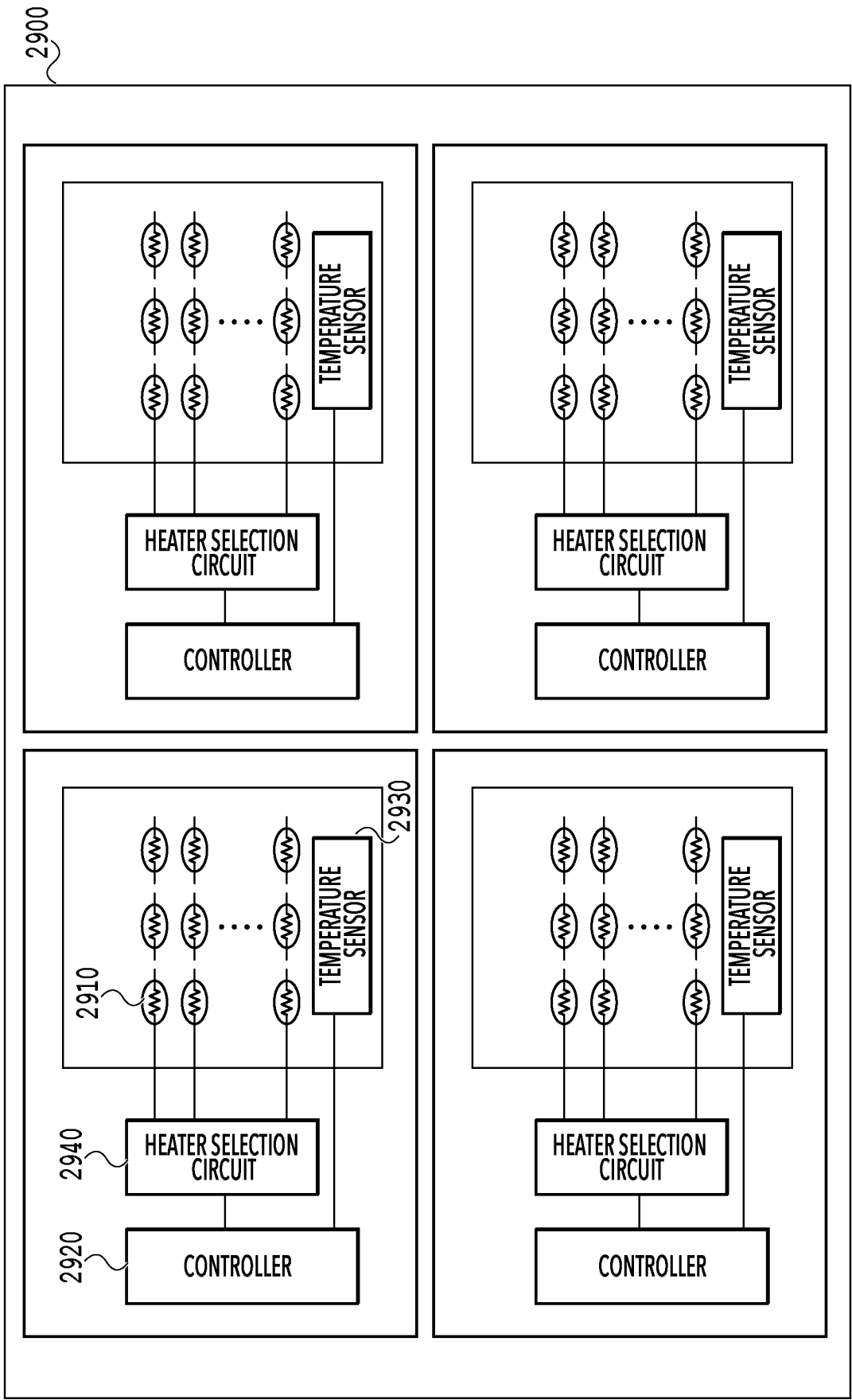


FIG. 29