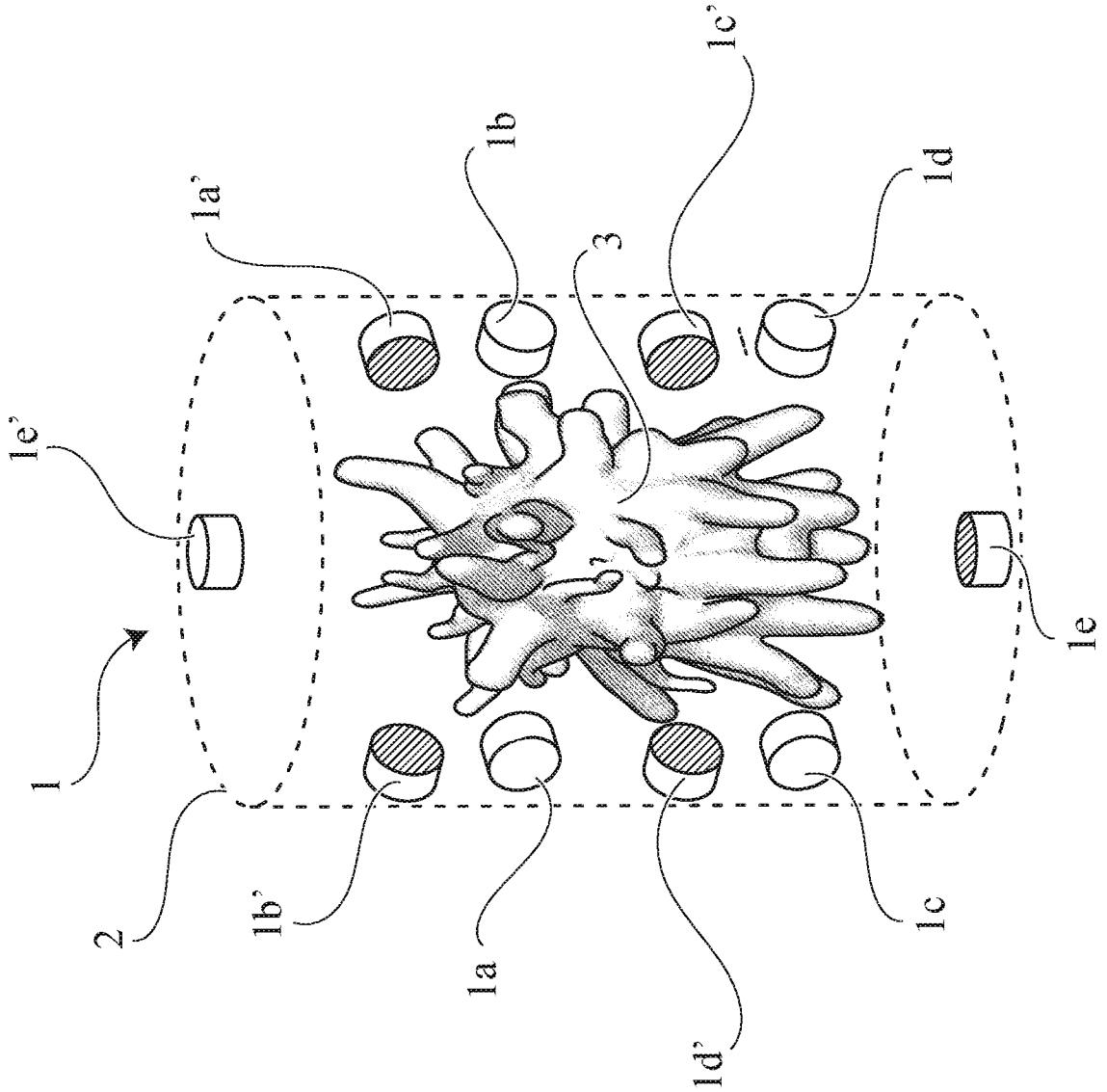


FIG. 1

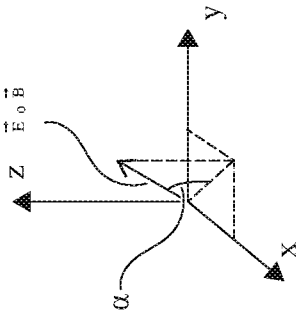


FIG. 2A

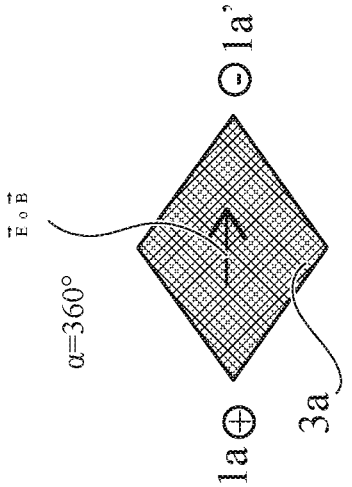


FIG. 2E

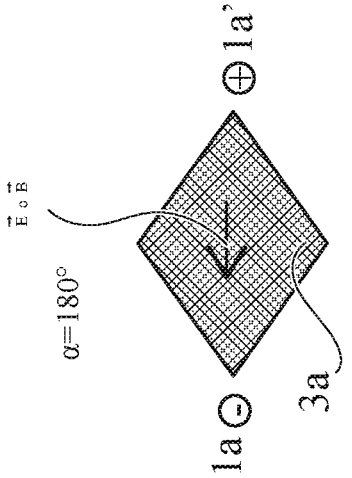


FIG. 2D

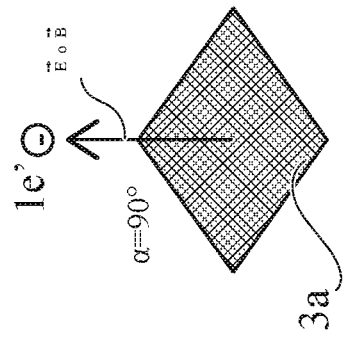


FIG. 2C

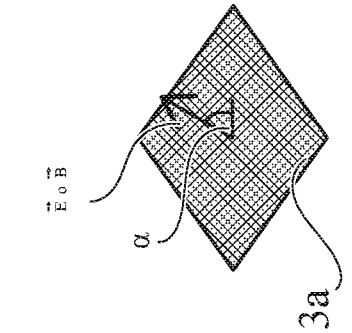


FIG. 2B

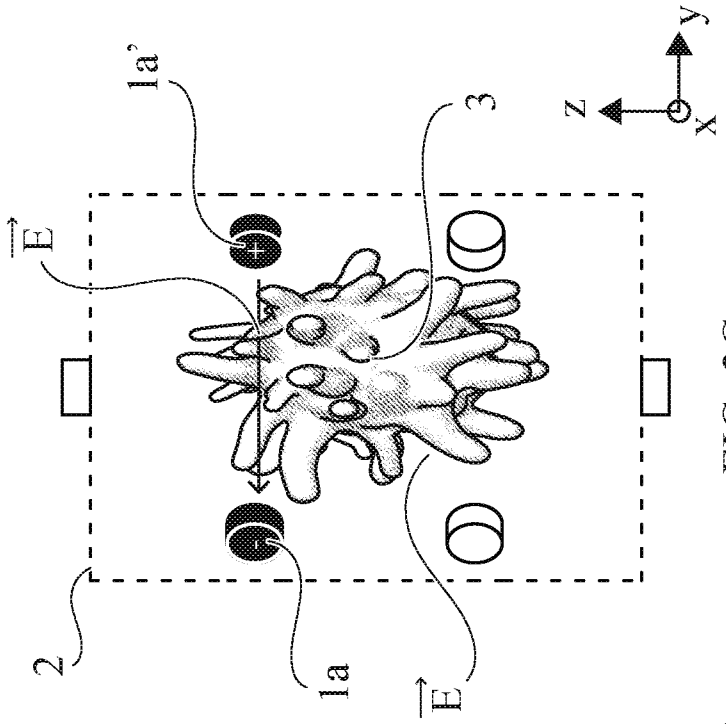


FIG. 3C

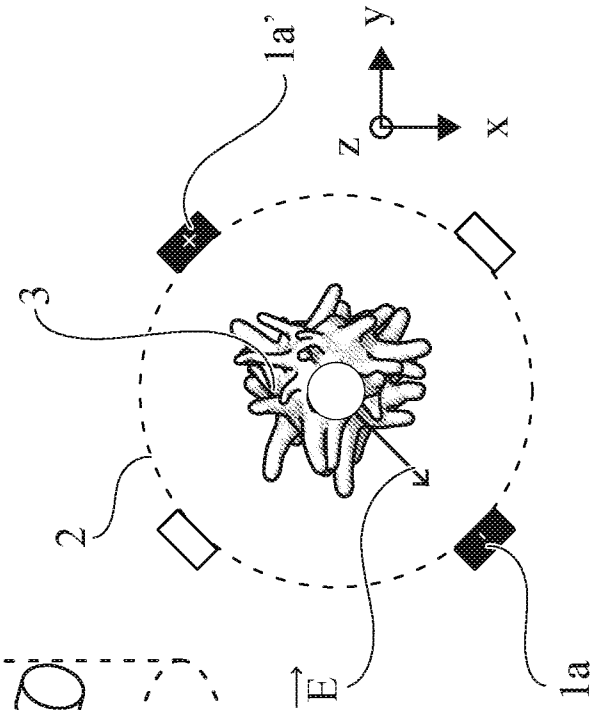


FIG. 3B

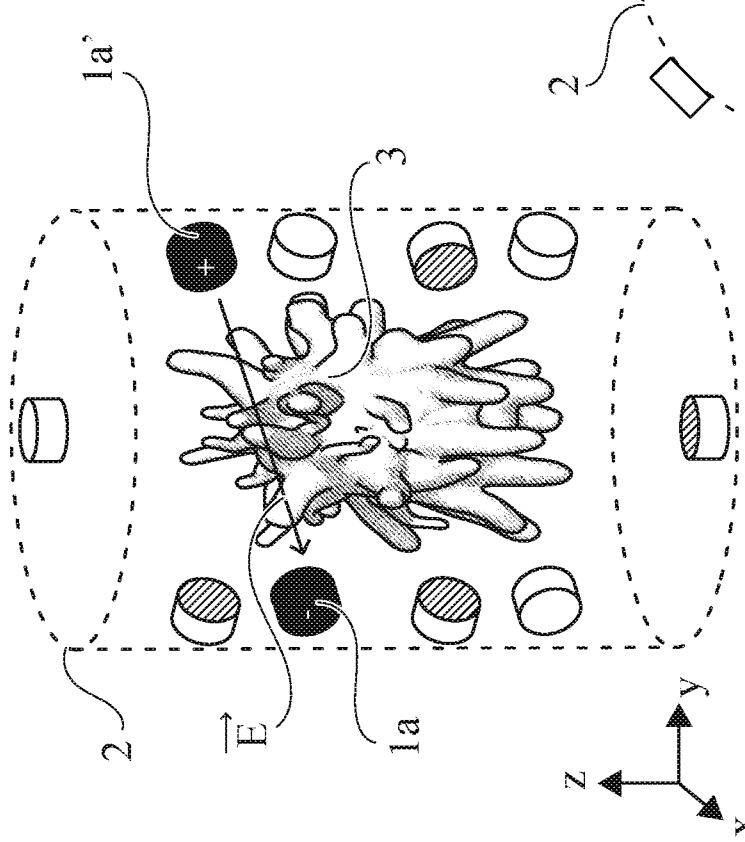


FIG. 3A

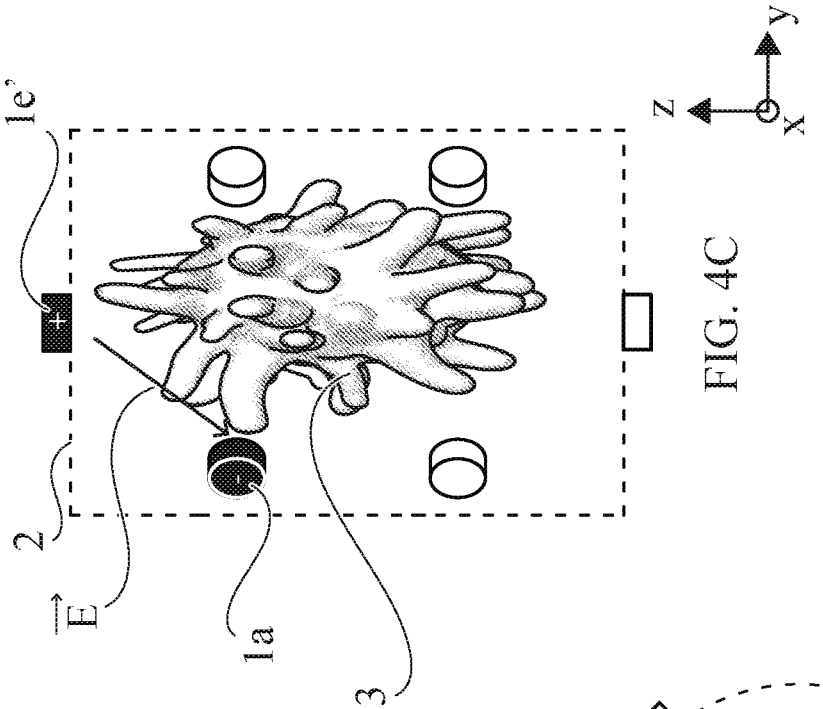


FIG. 4C

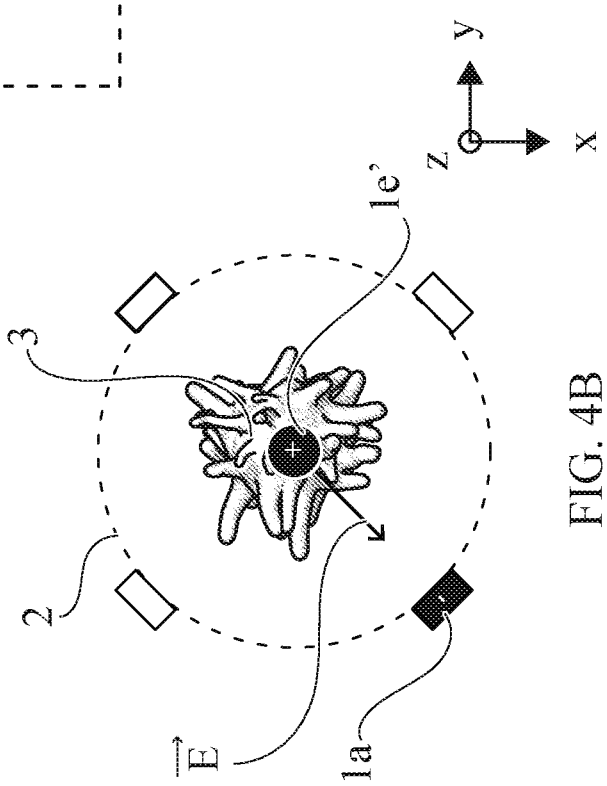


FIG. 4B

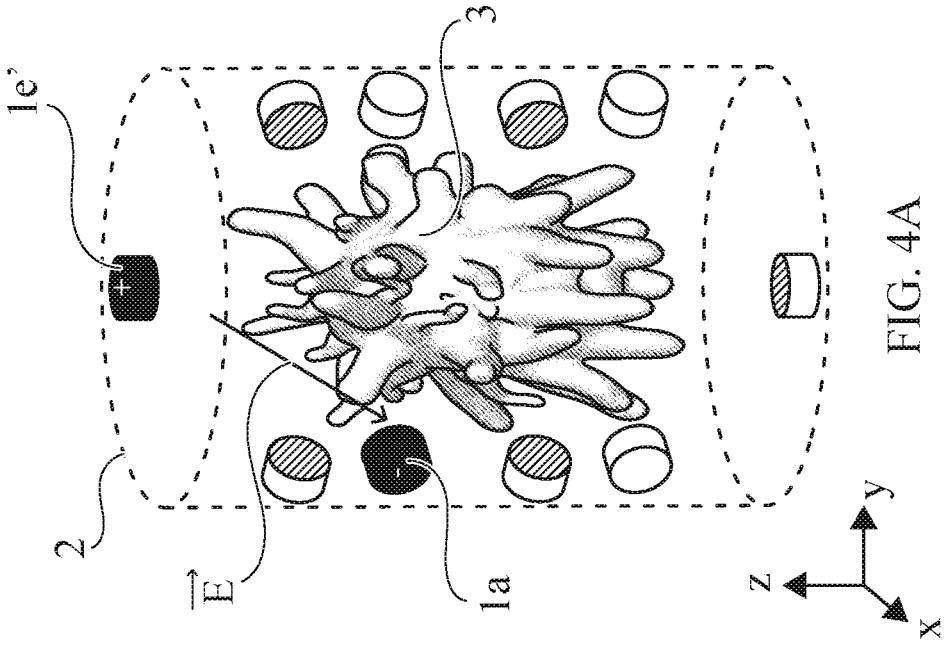


FIG. 4A

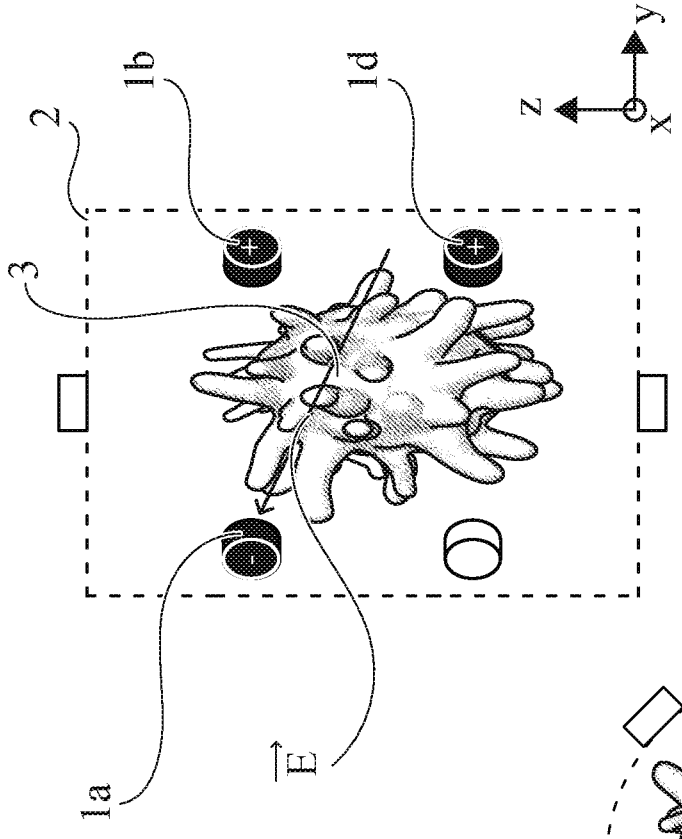


FIG. 5C

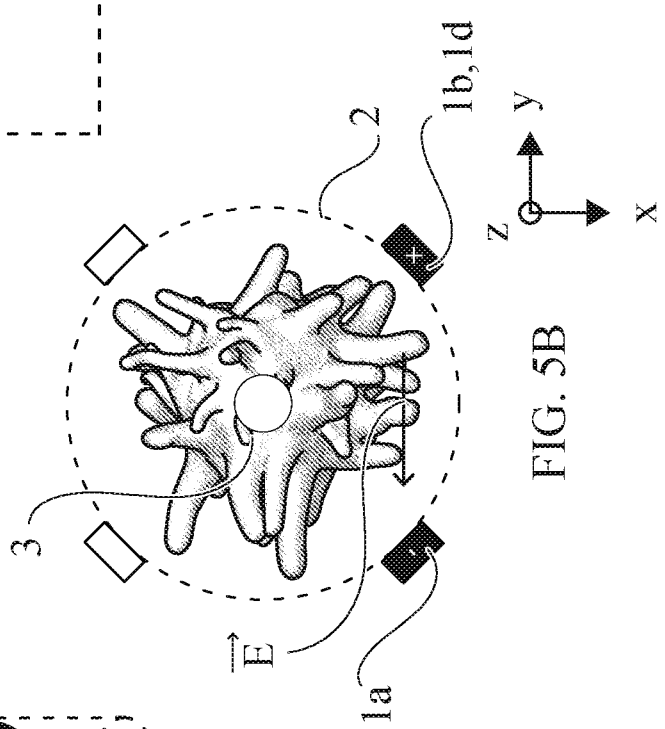


FIG. 5B

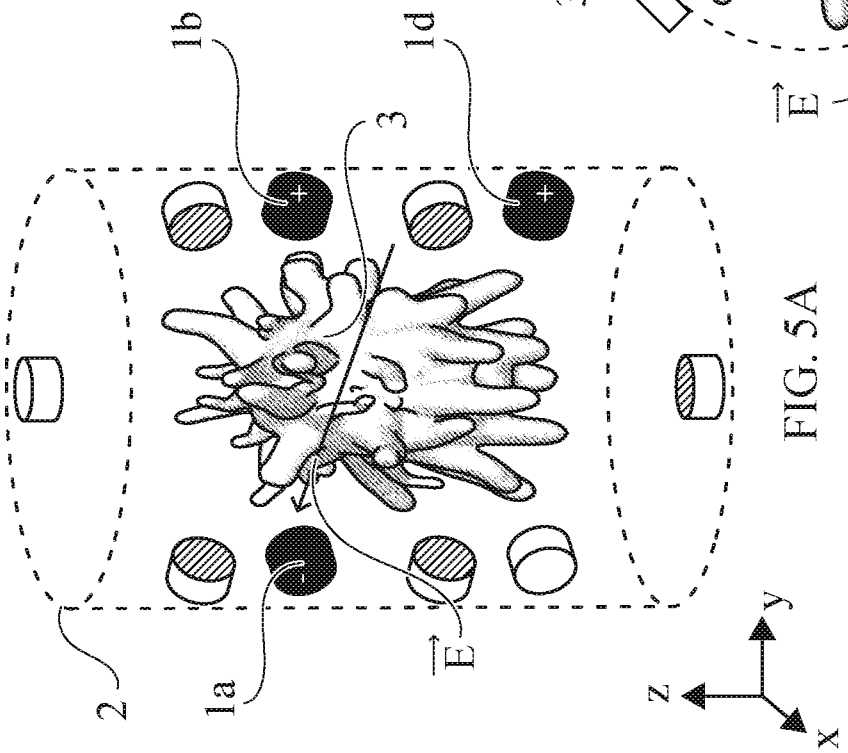


FIG. 5A

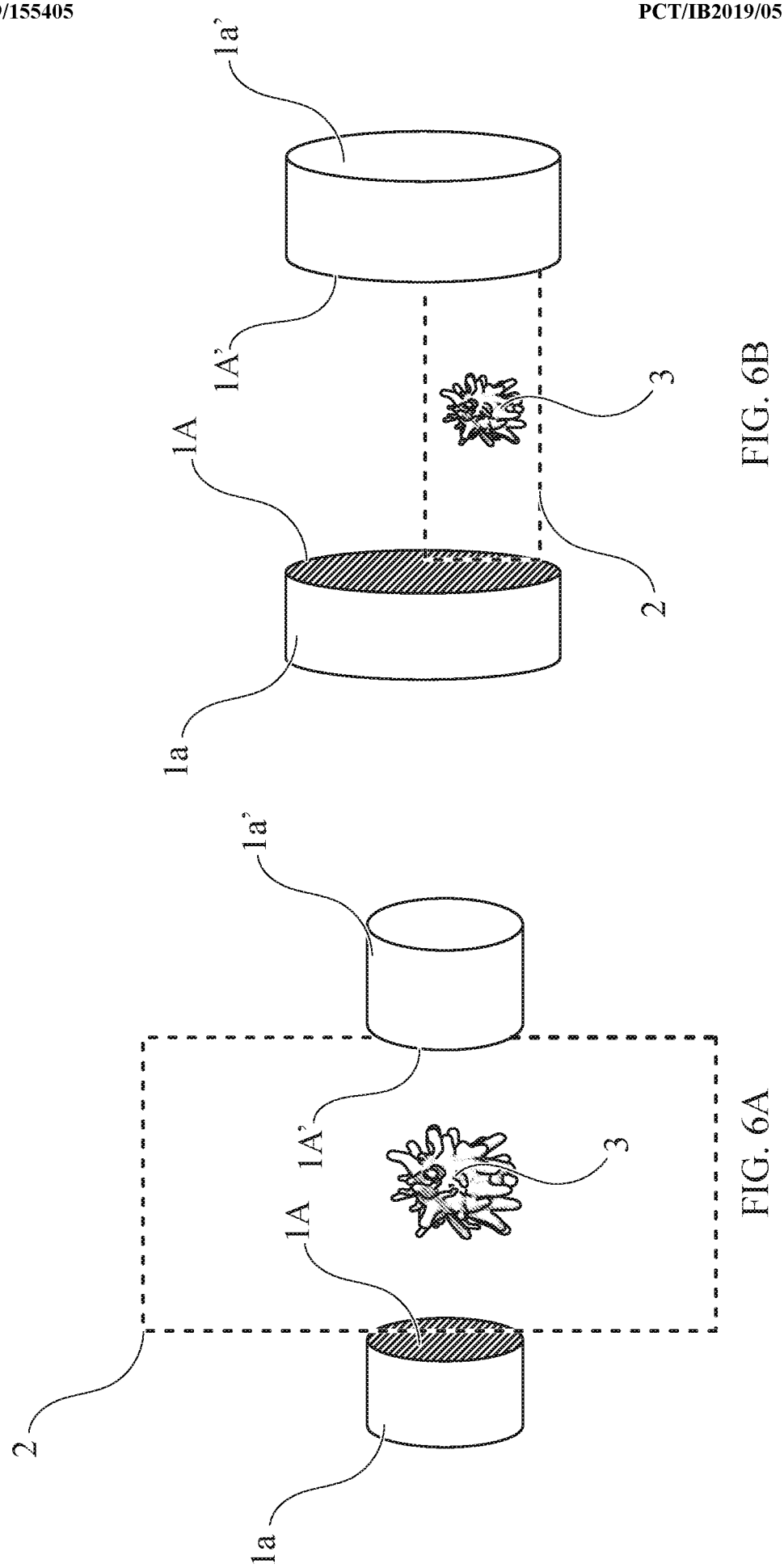


FIG. 6B

FIG. 6A

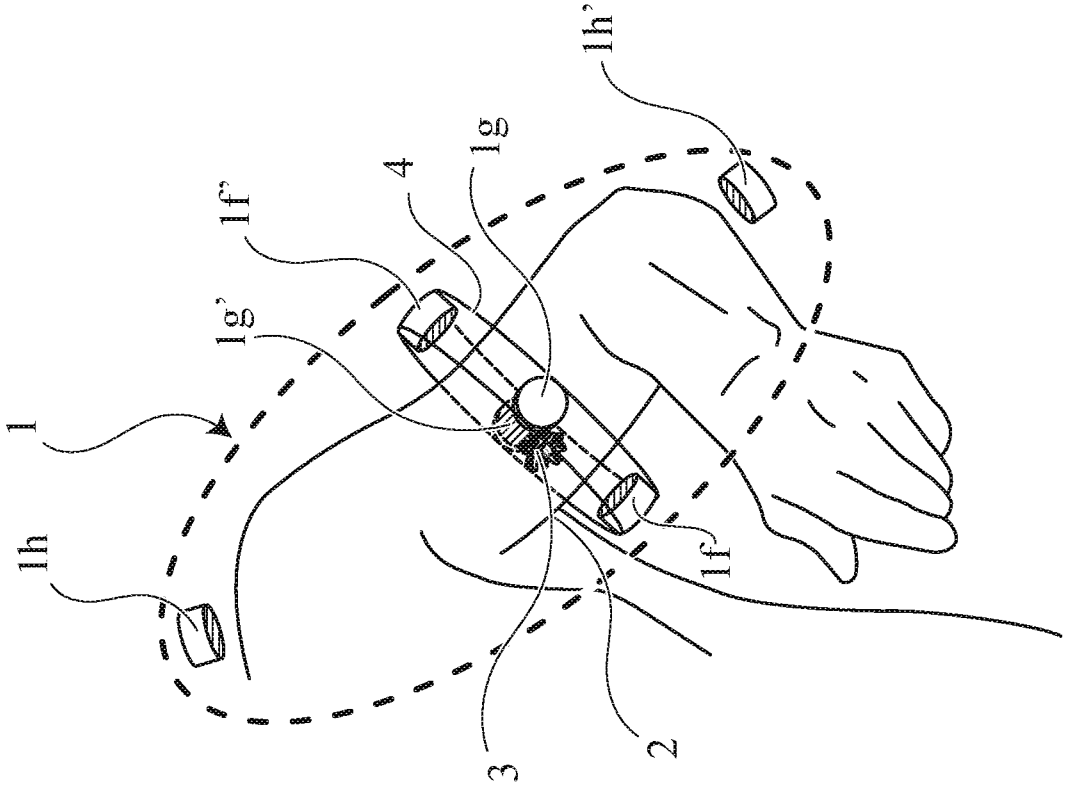


FIG. 7B

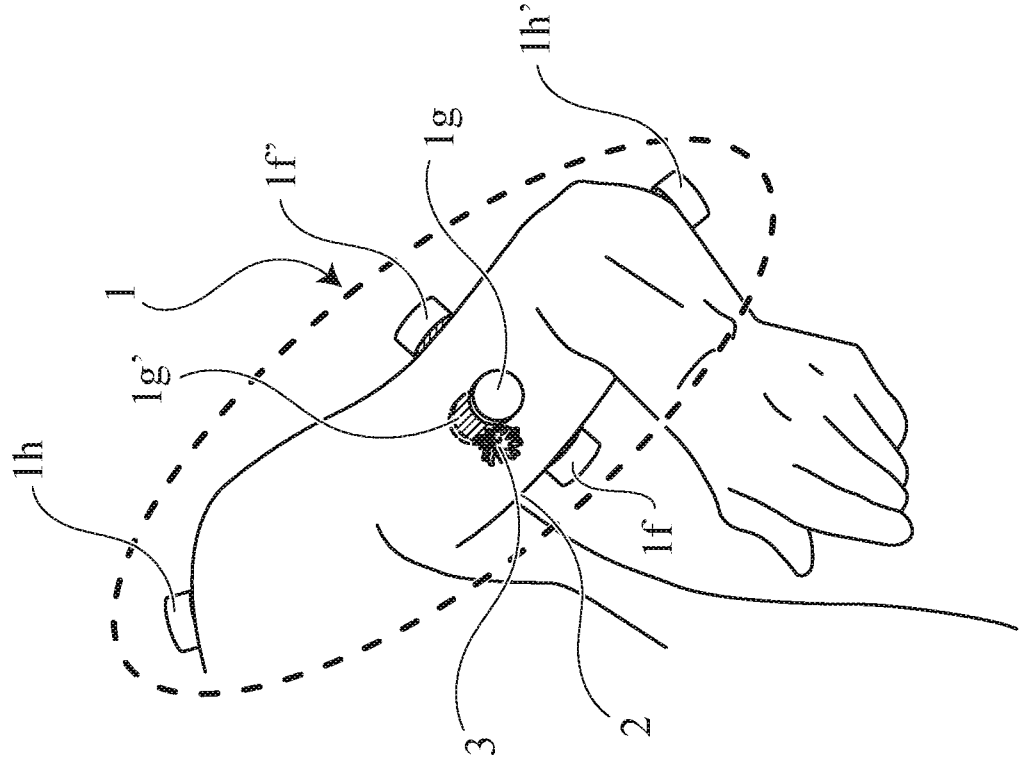


FIG. 7A

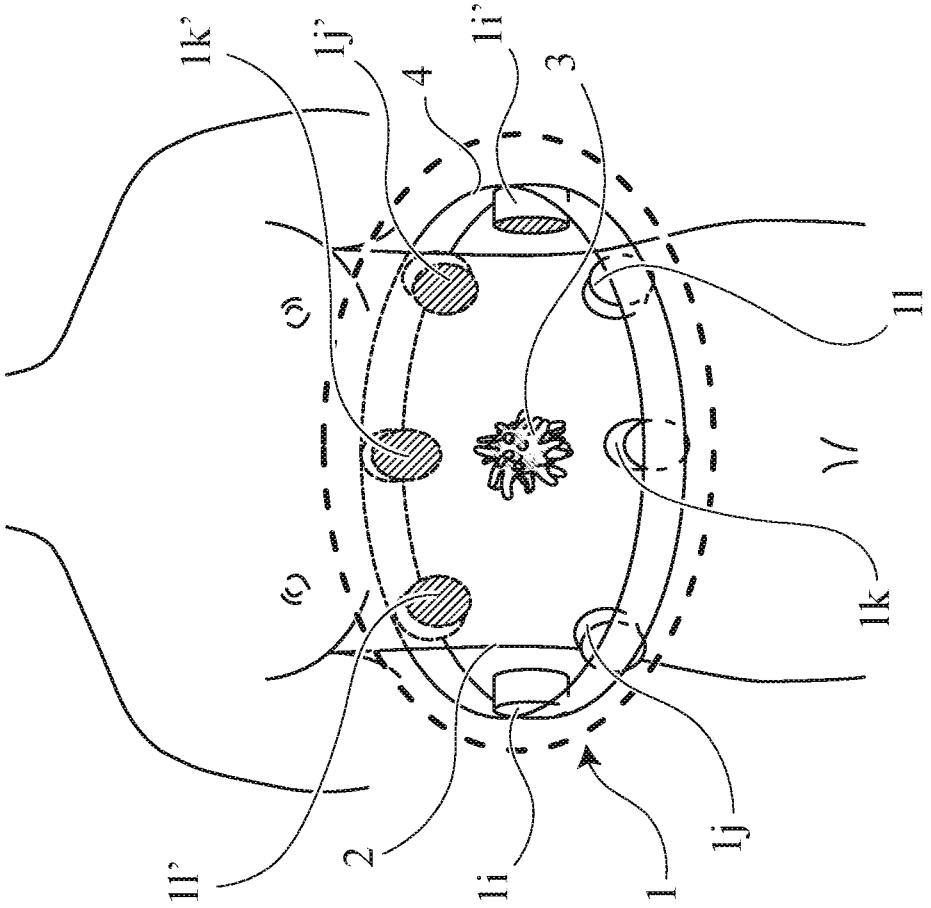


FIG. 8B

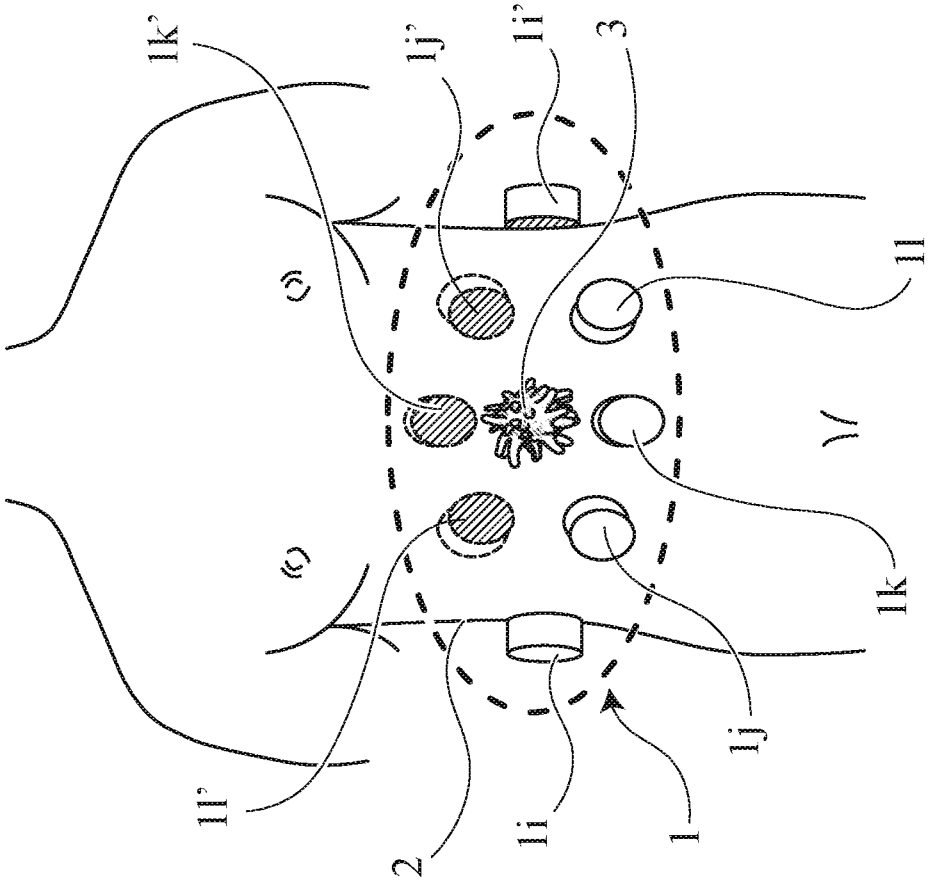


FIG. 8A

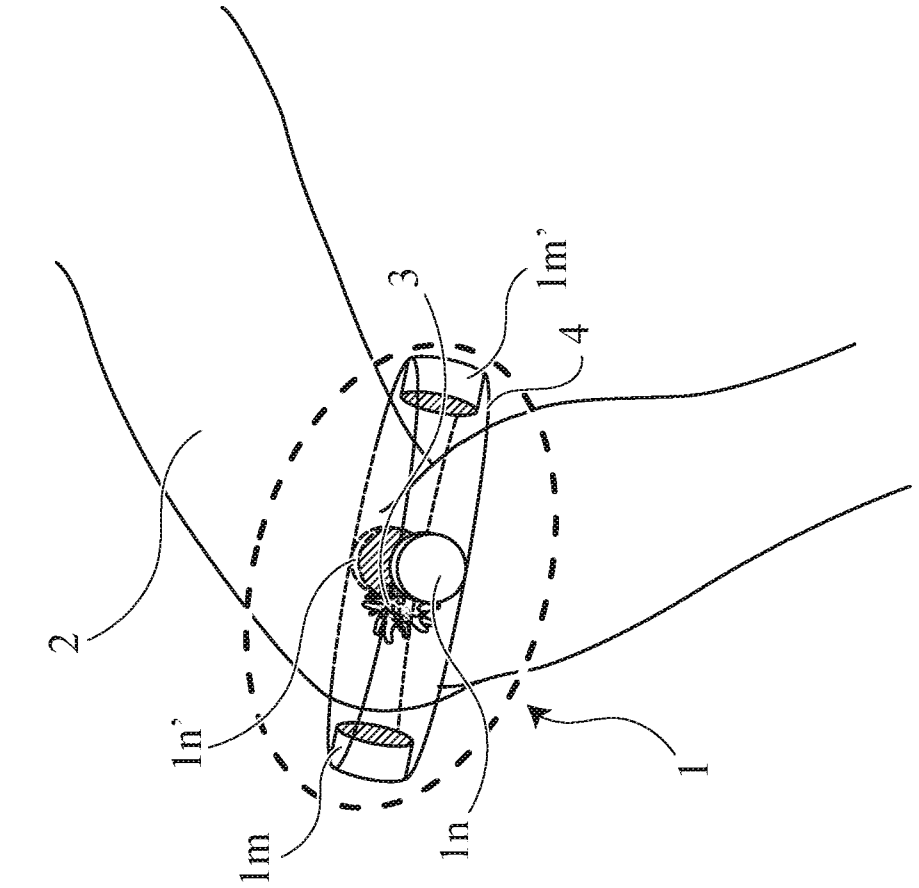


FIG. 9B

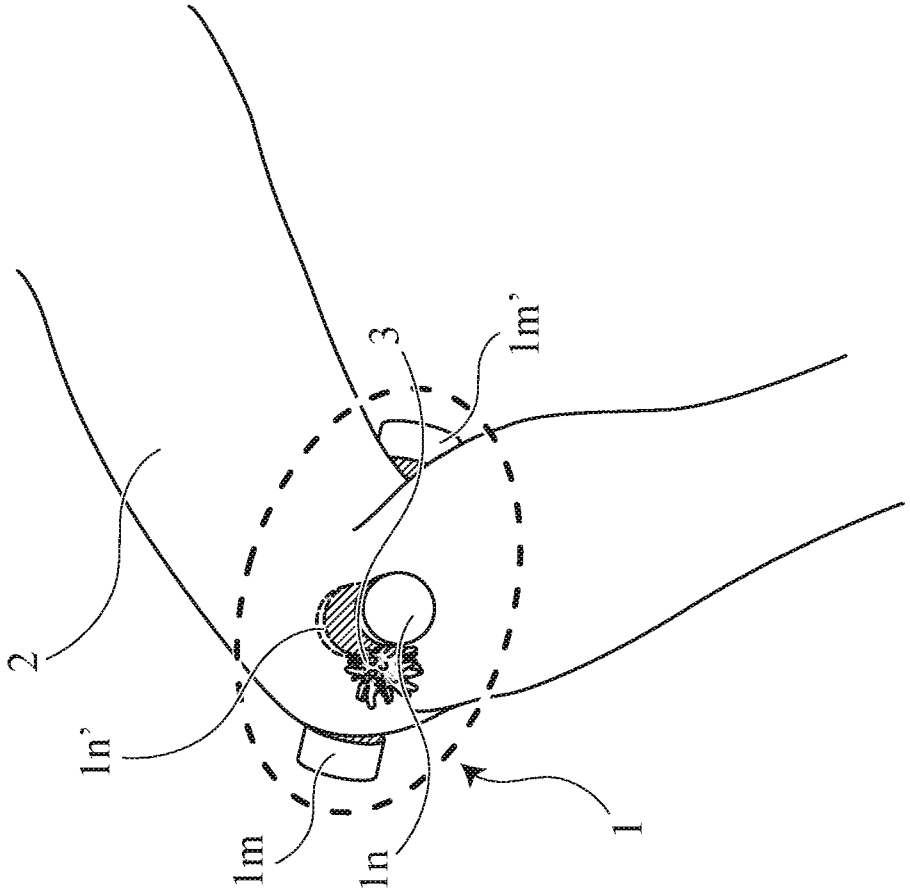


FIG. 9A

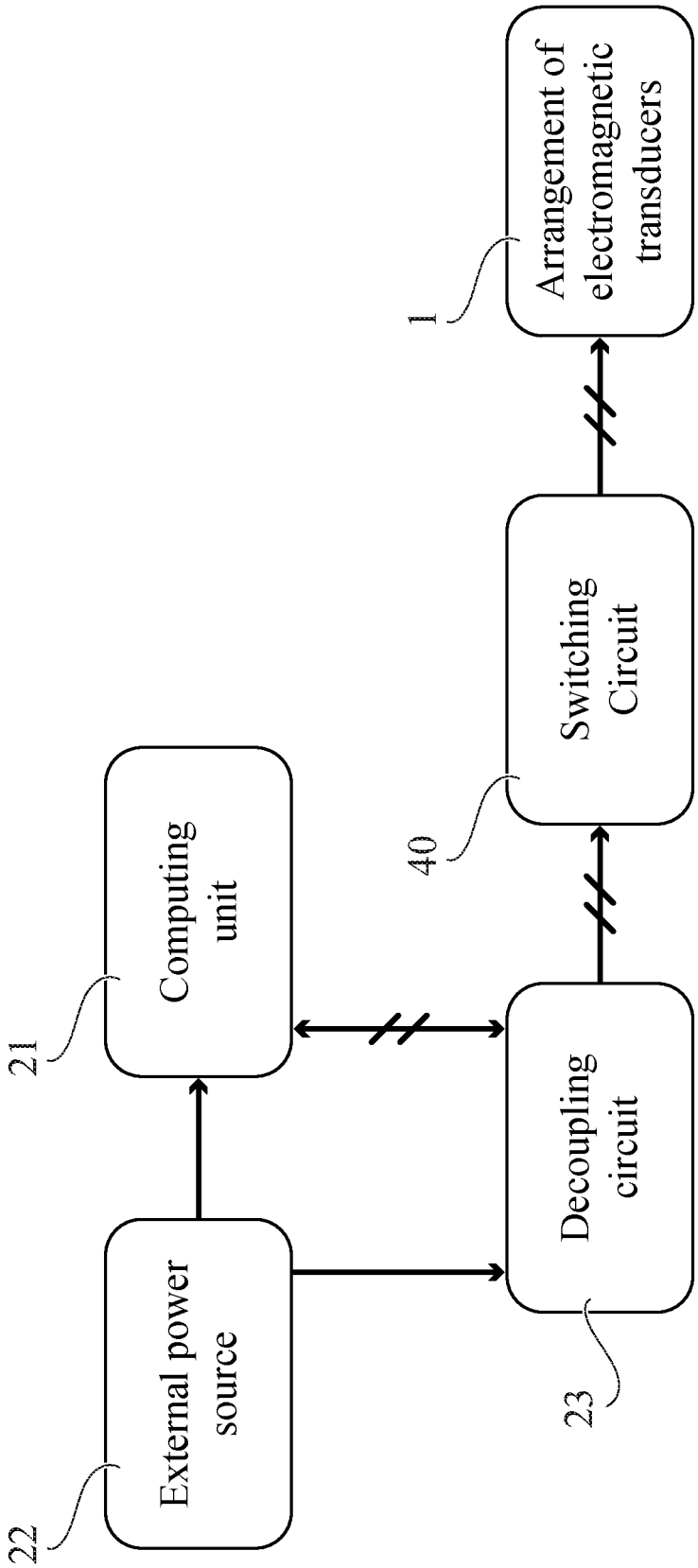

Arrangement of electromagnetic transducers
1
Switching Circuit
40
Computing unit
21
Decoupling circuit
23
External power source
22

FIG. 10

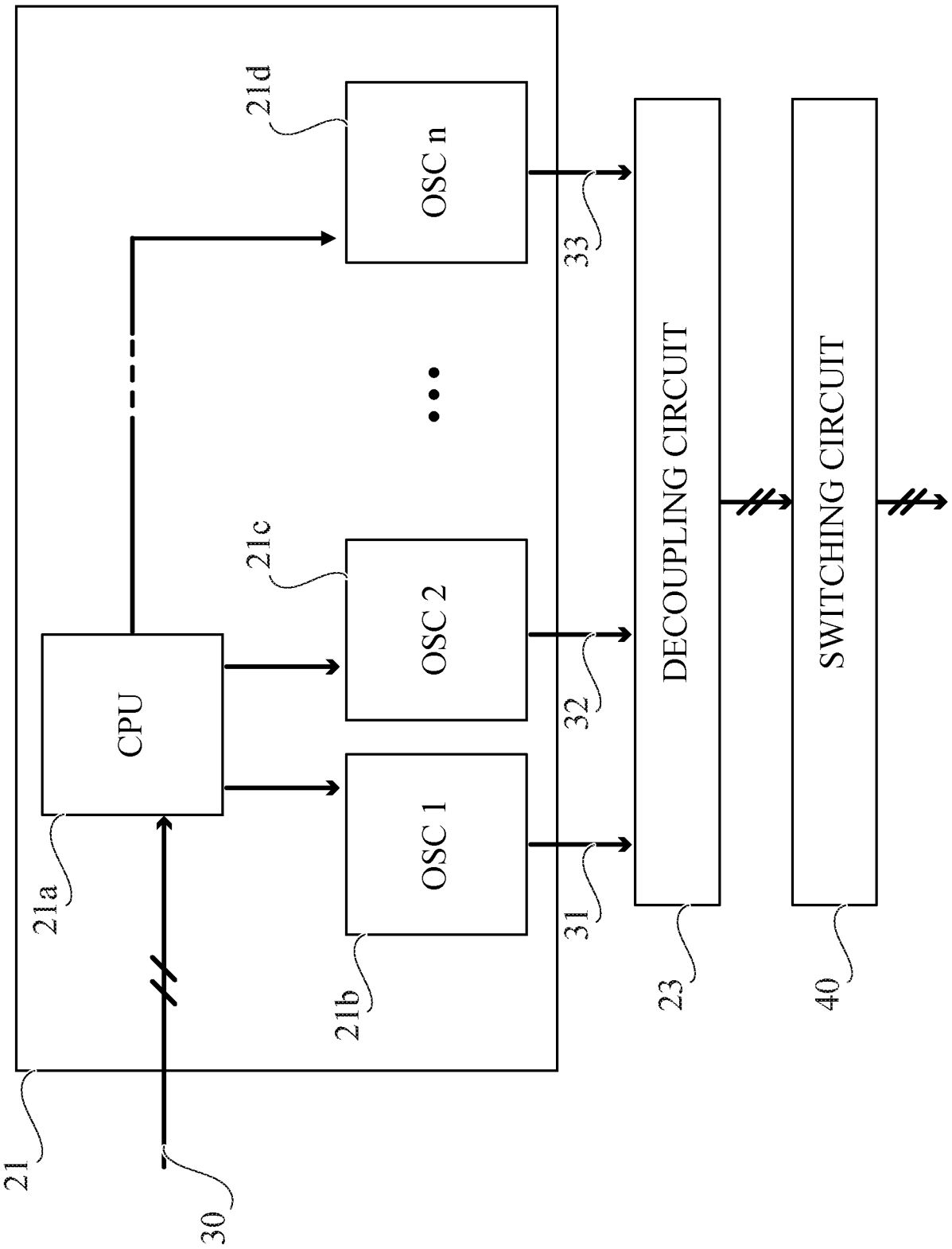

21
30
CPU
21a
OSC 1
21b
OSC 2
21c
OSC n
21d
31
32
33
DECOUPLING CIRCUIT
23
SWITCHING CIRCUIT
40

FIG. 11