ABSTRACT OF THE DISCLOSURE

A background picture display apparatus includes a microprocessor and an external storage unit, and dot data, character designating data and mode data are programmed in advance in a ROM in the external storage unit. Character designating data constituting background screens designated by the mode data and dot data of the respective characters used in the background screens are loaded in a RAM, that is, a screen RAM and a character RAM by a microprocessor. The character designating data is read from the screen RAM, and the dot data of the character is read from the character RAM. The dot data is converted into serial data by a parallel-serial conversion circuit, and a dot data combination circuit generates a color code for each background screen by combining the dot data and applies the same to a color generation circuit.

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