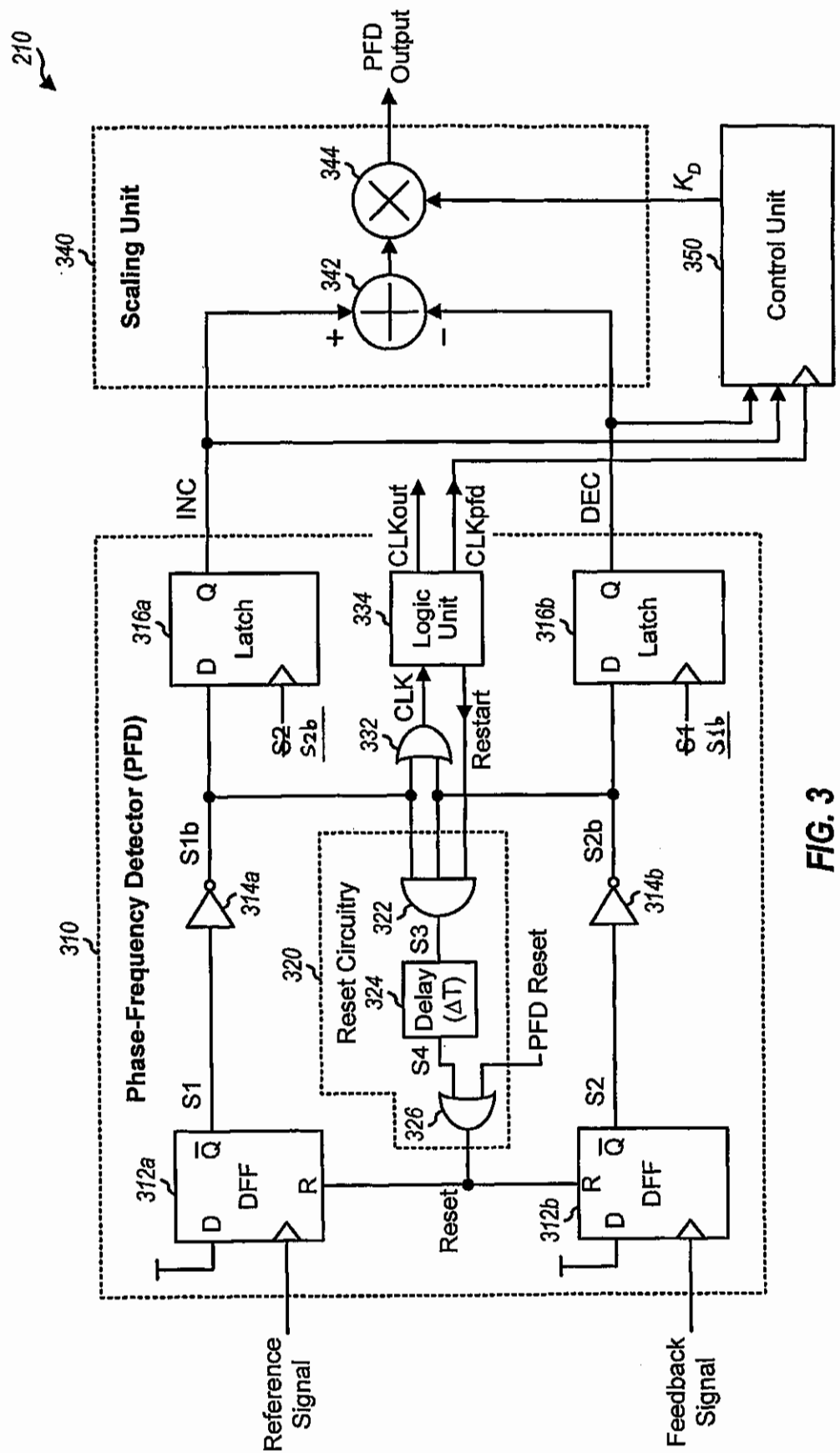
210
PFD Output
344
K_D
340
Scaling Unit
342
+
−
350
Control Unit
INC
CLKout
CLKpfd
DEC
316a
Latch
D
Q
334
Logic Unit
316b
Latch
D
Q
S2
S2b
332
CLK
Restart
S1
S1b
310
Phase-Frequency Detector (PFD)
314a
Reset Circuitry
322
S3
320
324
Delay (ΔT)
S4
PFD Reset
326
S2b
314b
S1
S2
312a
DFF
D
Q
R
Reset
312b
DFF
R
Q
D
Reference Signal
Feedback Signal

FIG. 3