

Abstract

Data Generation Device for an Electronic
Musical Instrument

5 Data of desired variable data length is stored in a
memory. When desired data is to be taken out of the
memory, data length of the data to be taken out is
designated and necessary data is selectively taken out
of the memory in accordance with the designated data
length. Single data may be stored over plural
10 addresses. In that case, MSB or LSB of data to be taken
out of the data stored over the plural addresses is
pointed out and desired data of a desired bit number is
taken out in accordance with the pointed out position and
the bit number of the data. In the memory, first data
15 and second data may be mixedly stored. The second data
consists of plural bits and is divided in plural
sections, each section being separately stored in the
first data. The first data and the second data are
separated from the data which has been read from the
20 memory and the respective sections of the second data
are assembled together to reproduce completed second
data. This storage and reproduction of data contributes
to an efficient utilization of the memory.

(Figure 3)

