

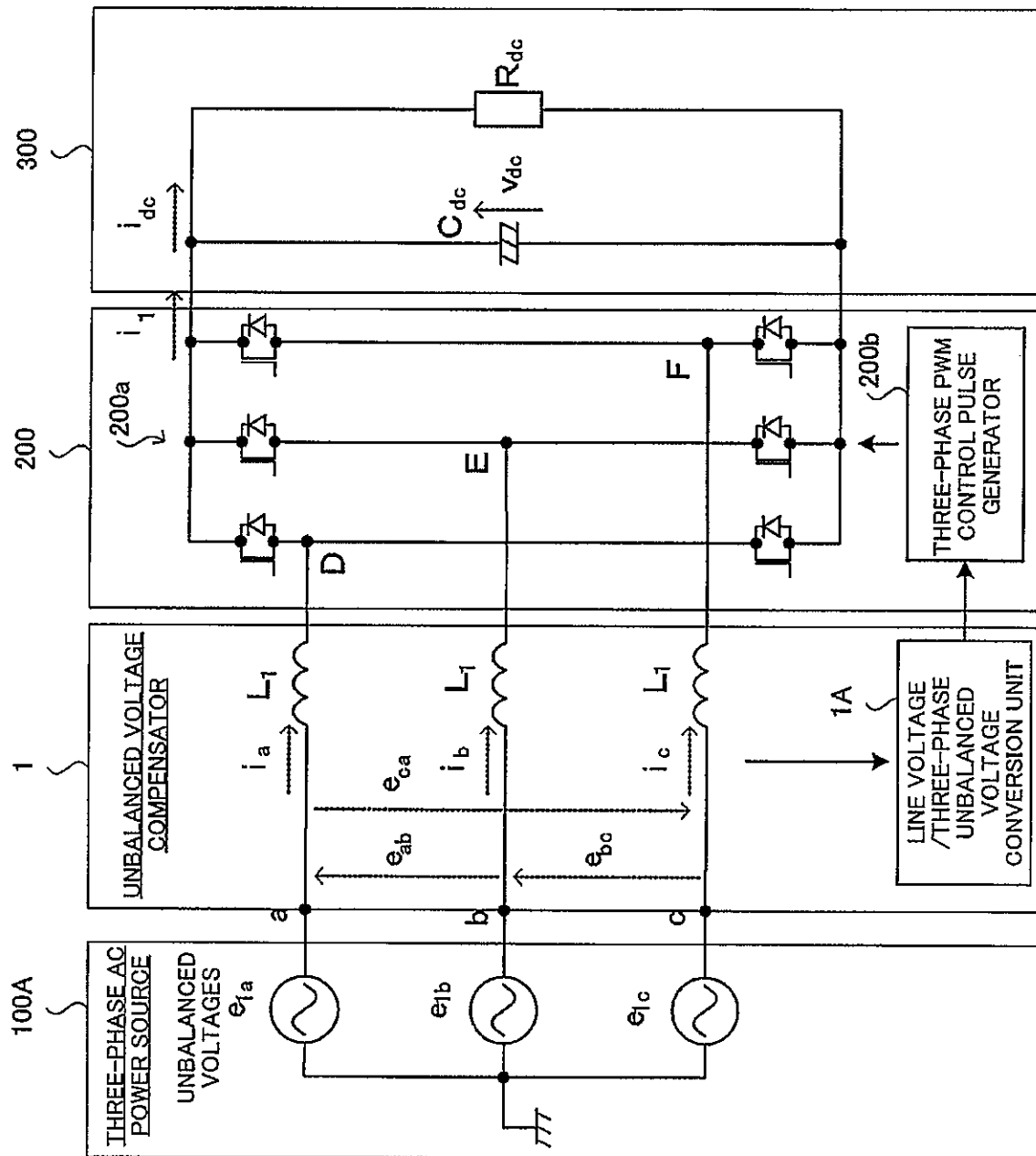


FIG. 1

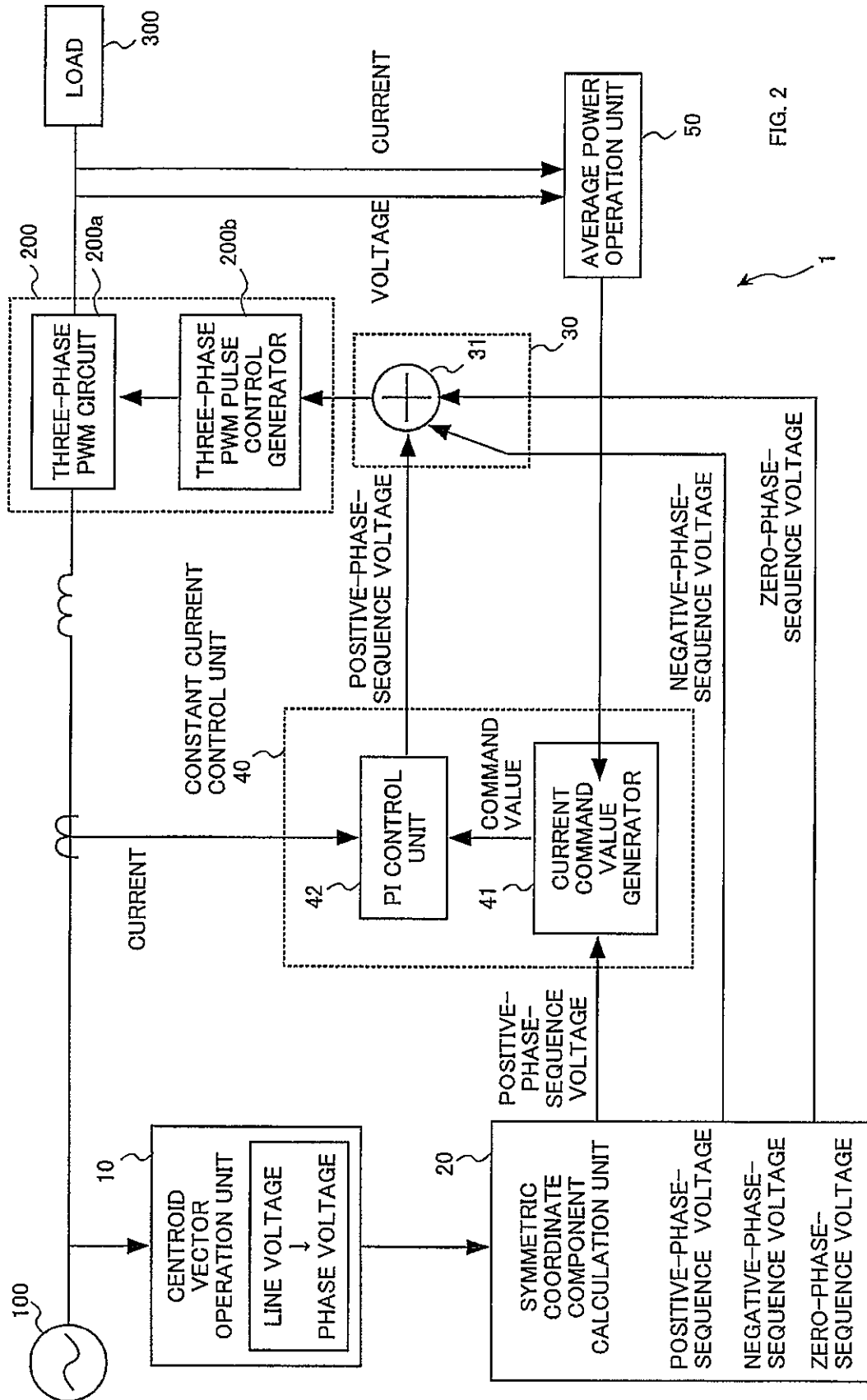


FIG. 2

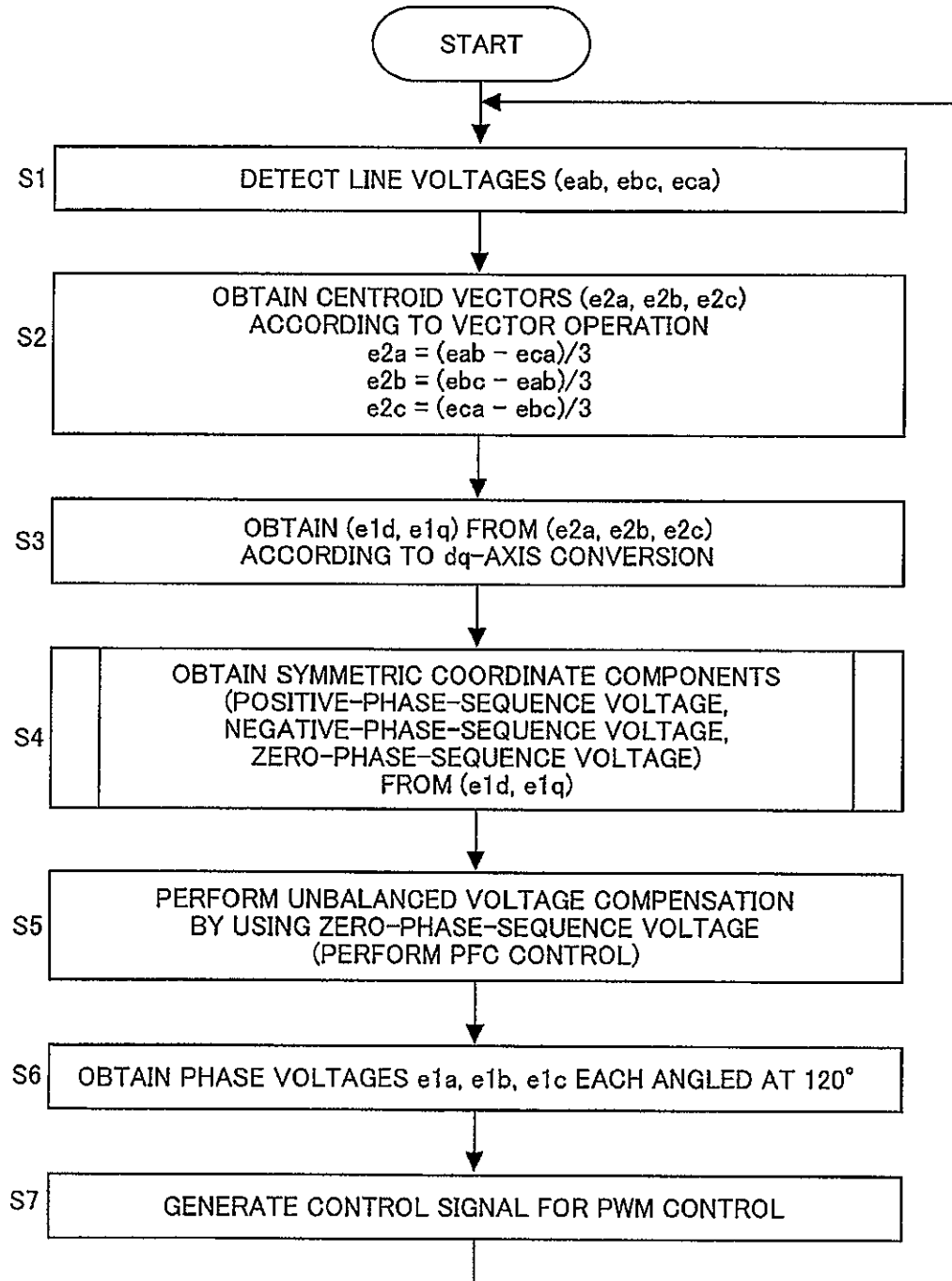


FIG. 3

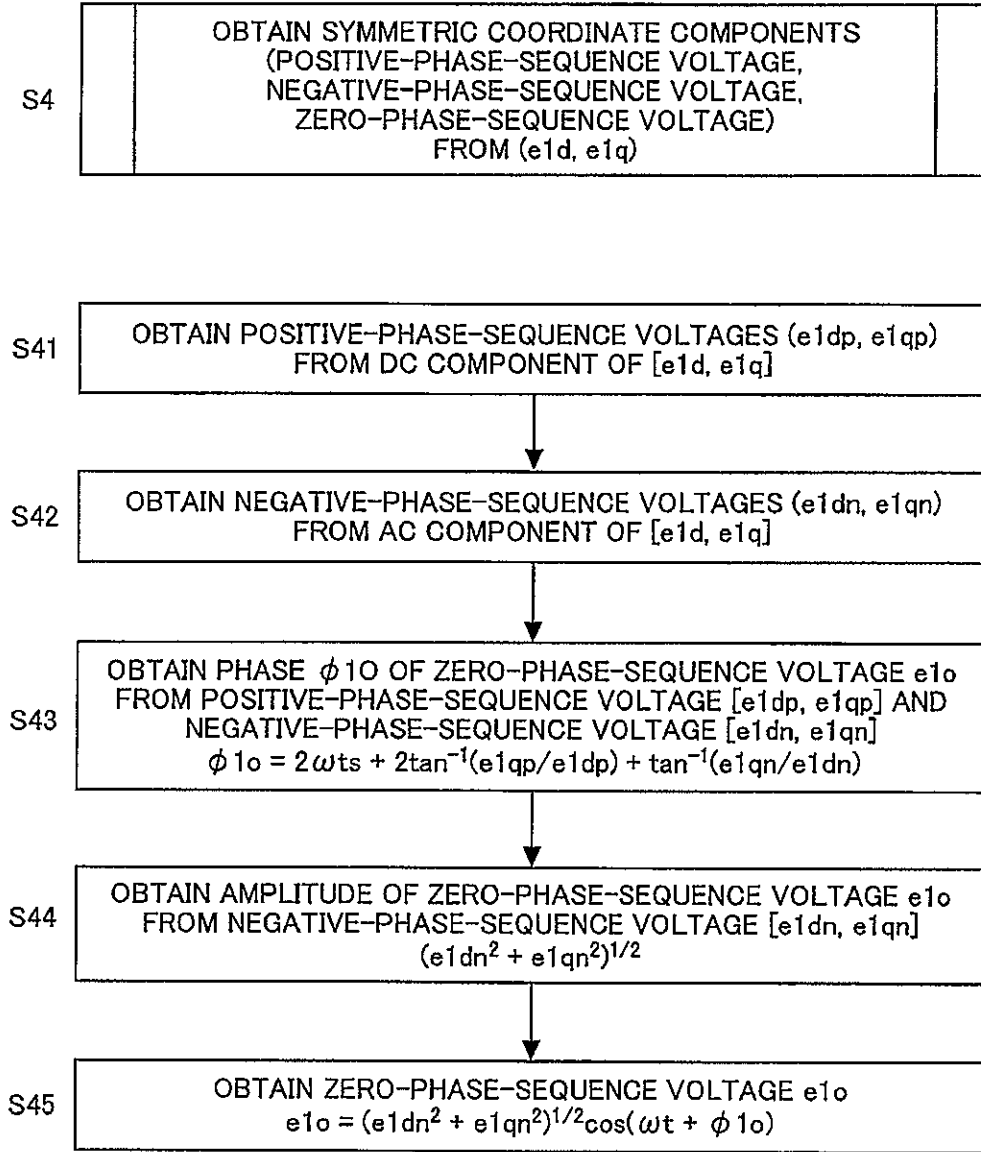


FIG. 4

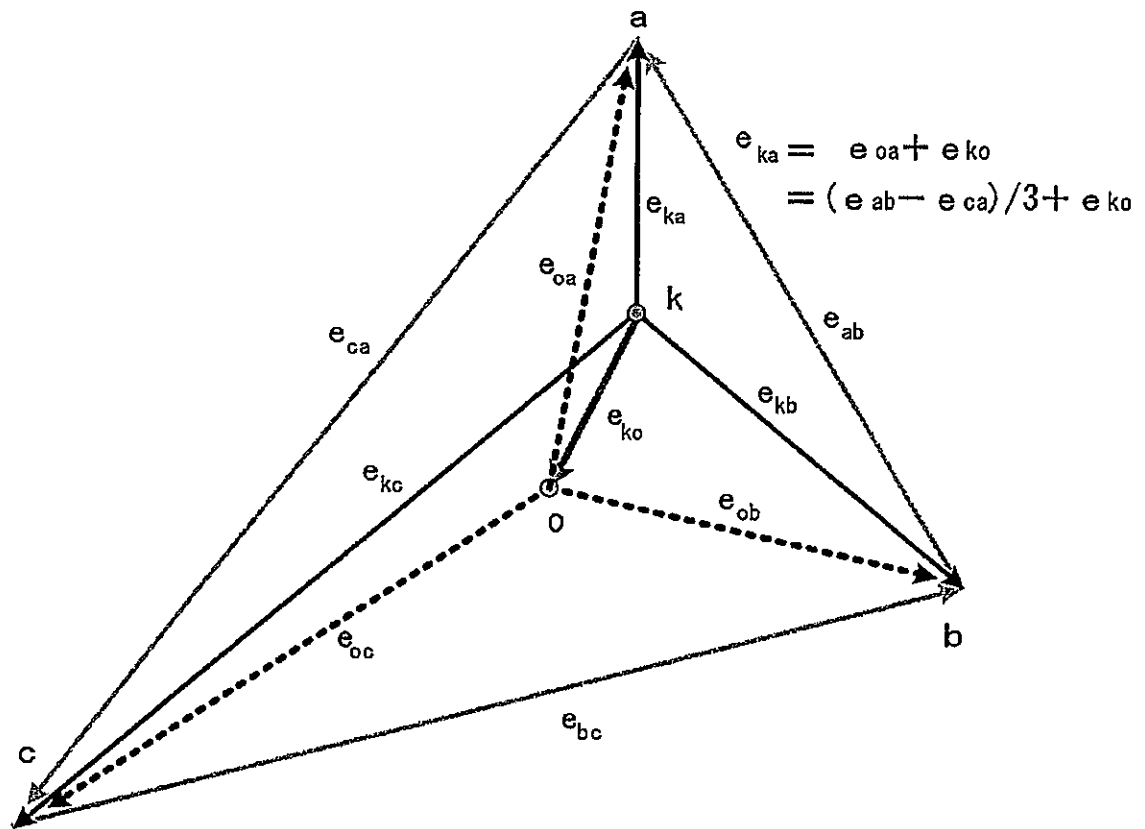


FIG. 5

FIG. 6A

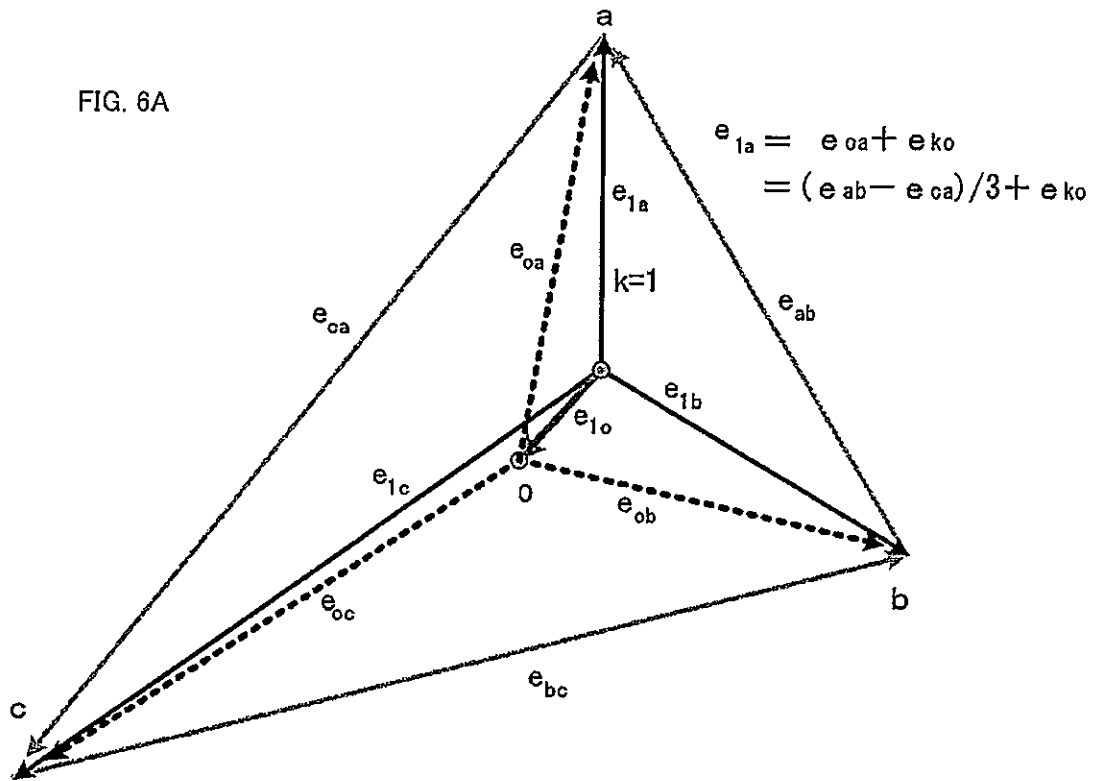


FIG. 6B

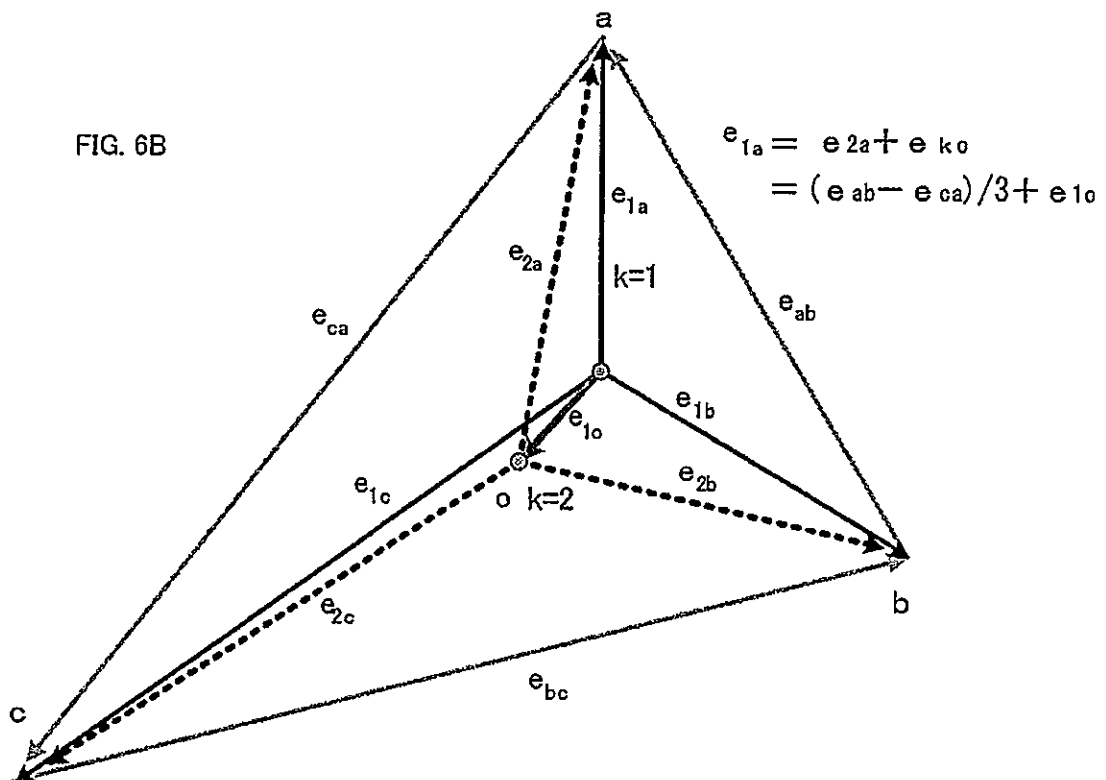




FIG. 7

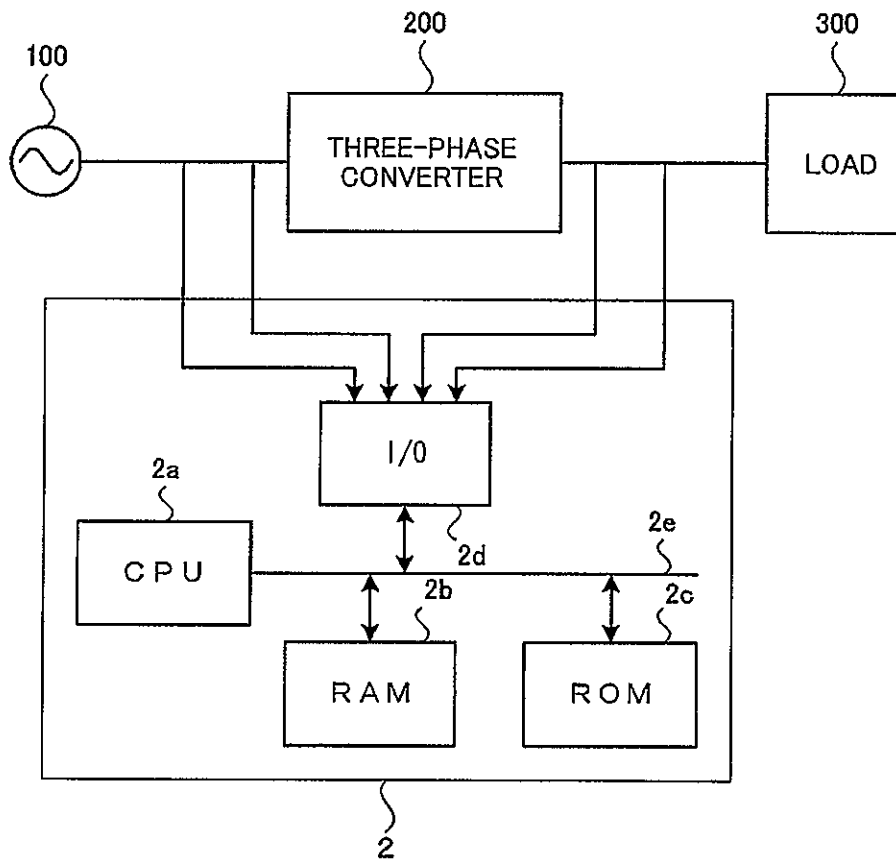


FIG. 8

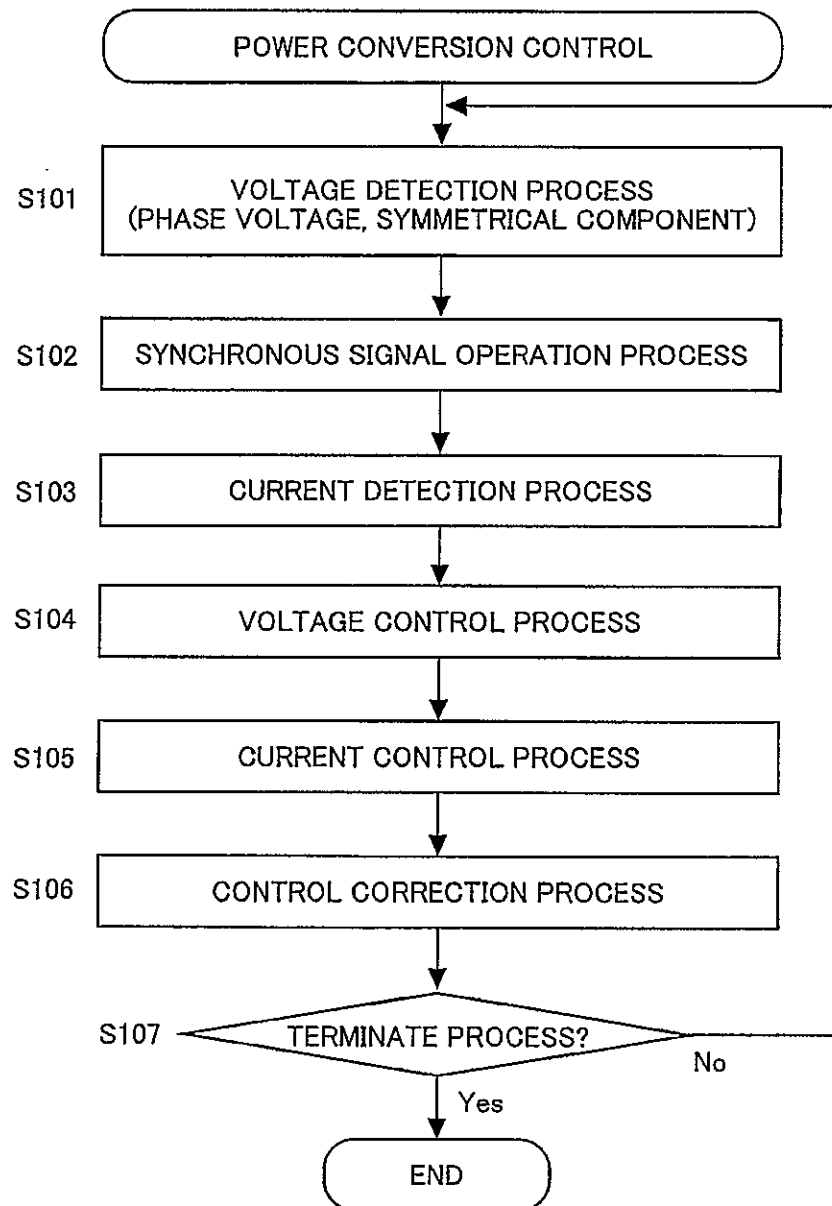


FIG. 9

FIG. 10A
COMPENSATION FOR VOLTAGE SAG
IS NOT PERFORMED

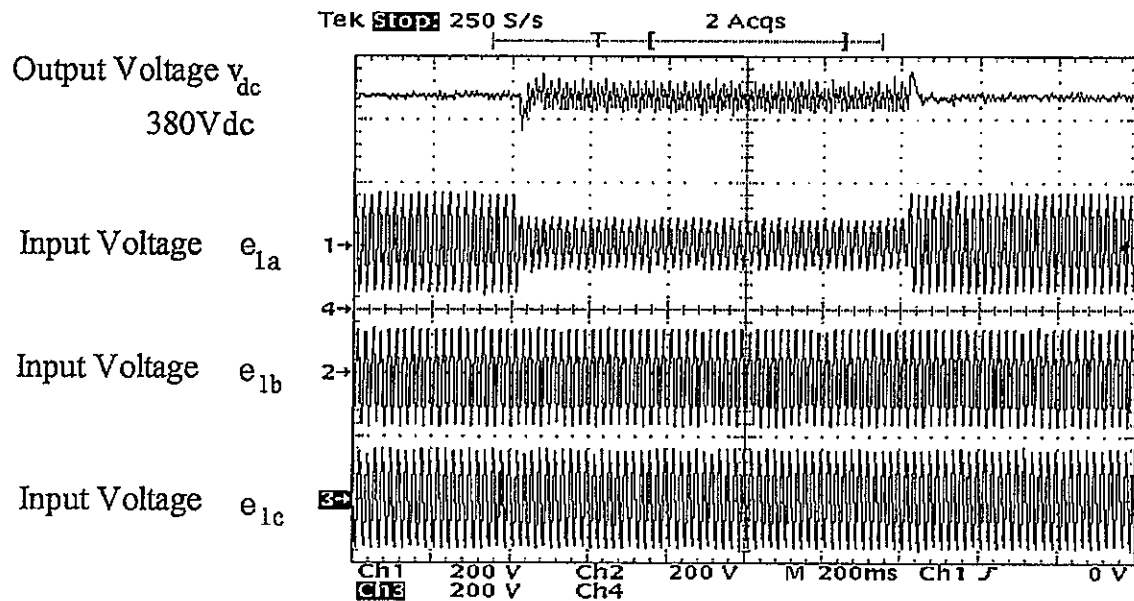
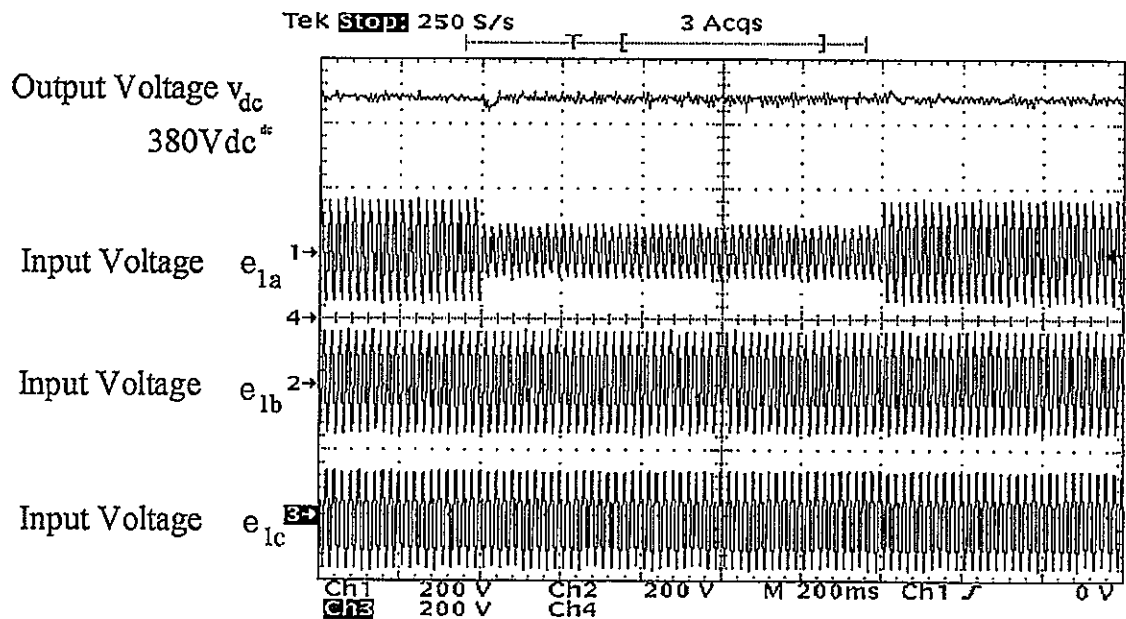


FIG. 10B
VOLTAGE SAG COMPENSATION
IS PERFORMED



e_{1a} : 100%→50%→100%

FIG. 11A
COMPENSATION FOR VOLTAGE SAG
IS NOT PERFORMED

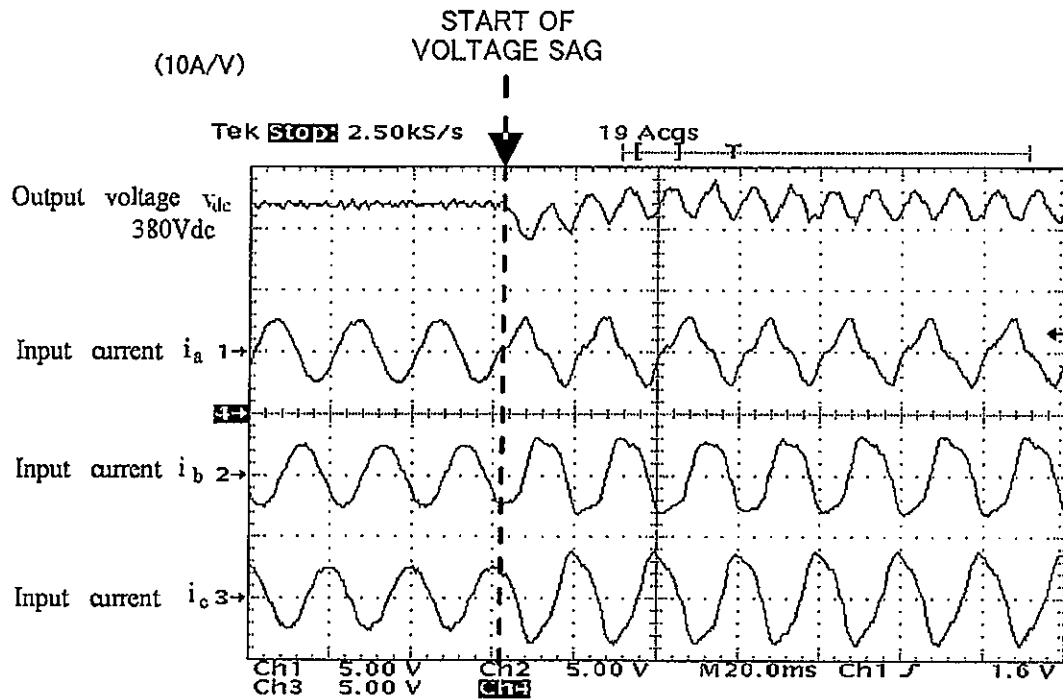


FIG. 11B
VOLTAGE SAG COMPENSATION
IS PERFORMED

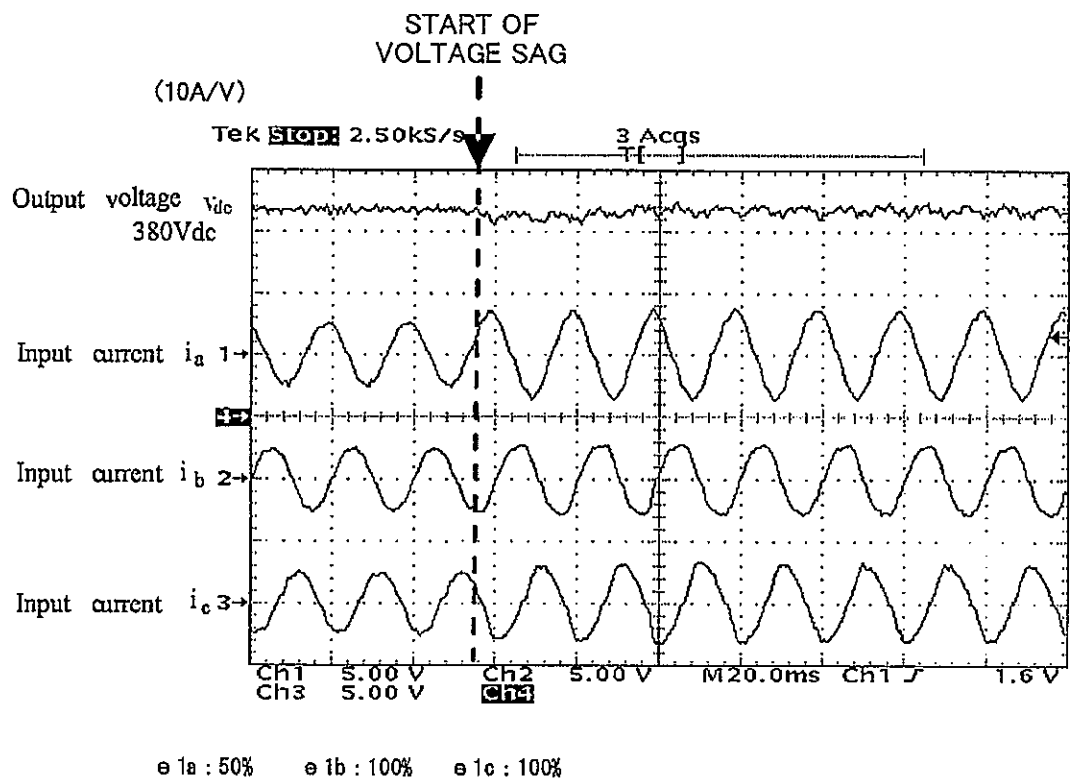


FIG. 12A
COMPENSATION FOR VOLTAGE SAG
IS NOT PERFORMED

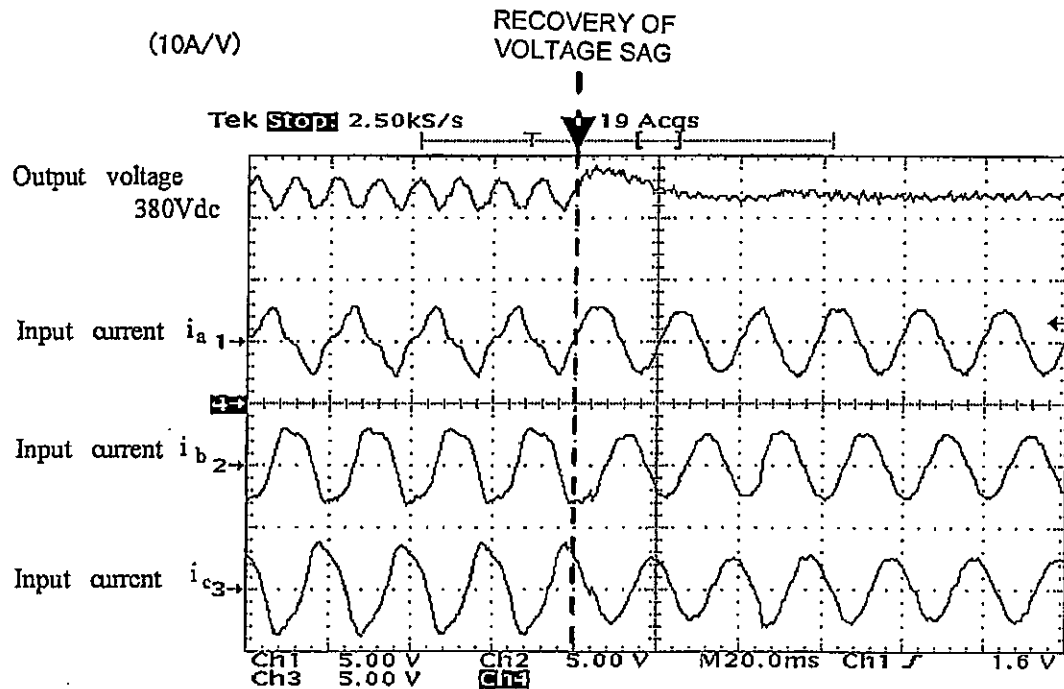


FIG. 12B
VOLTAGE SAG COMPENSATION
IS PERFORMED

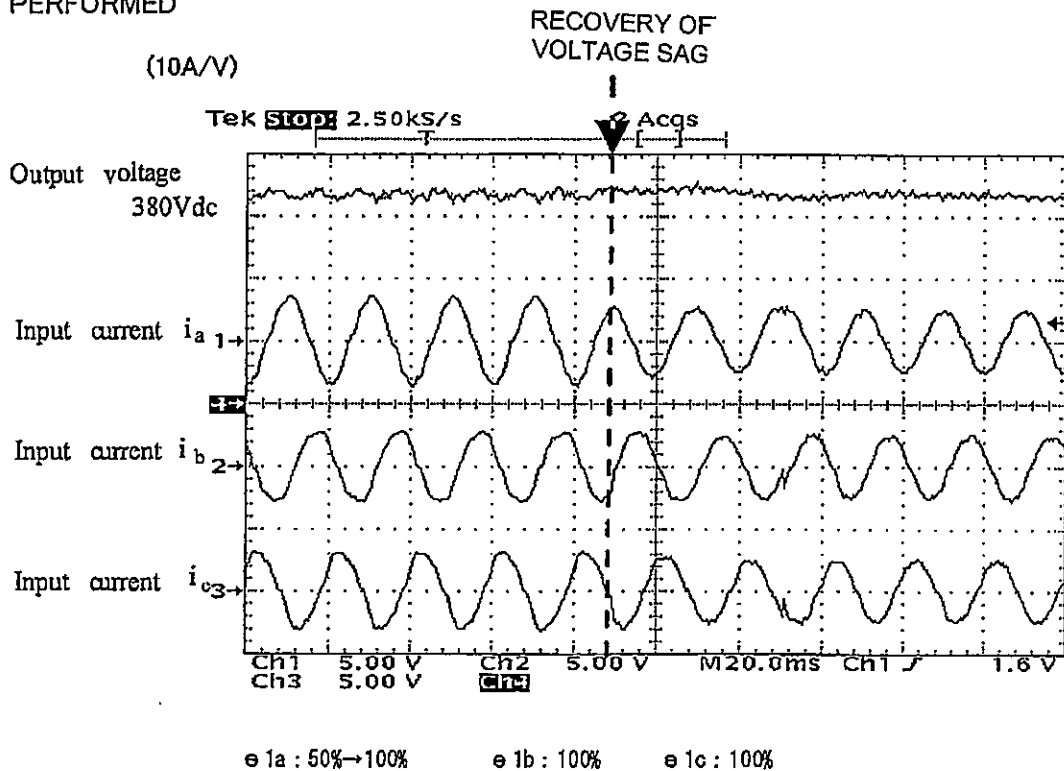


FIG. 13A

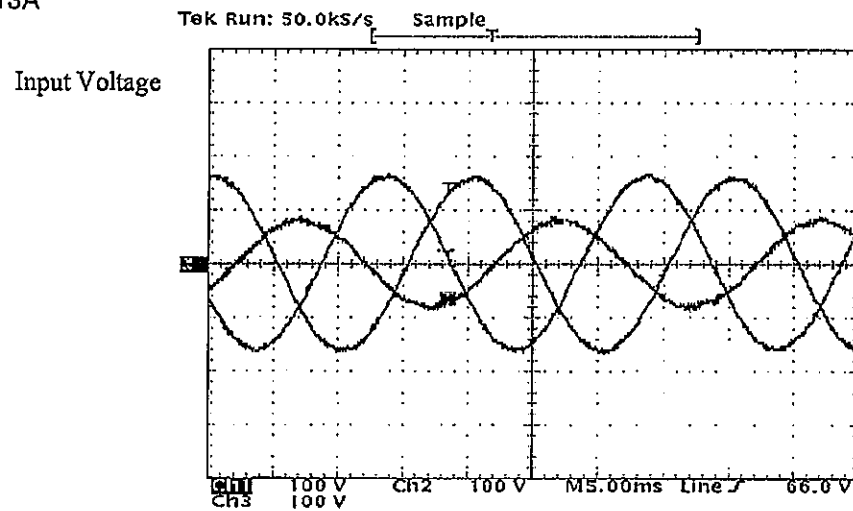


FIG. 13B

Input Current
COMPENSATION FOR
VOLTAGE SAG IS
NOT PERFORMED
(20A/V)

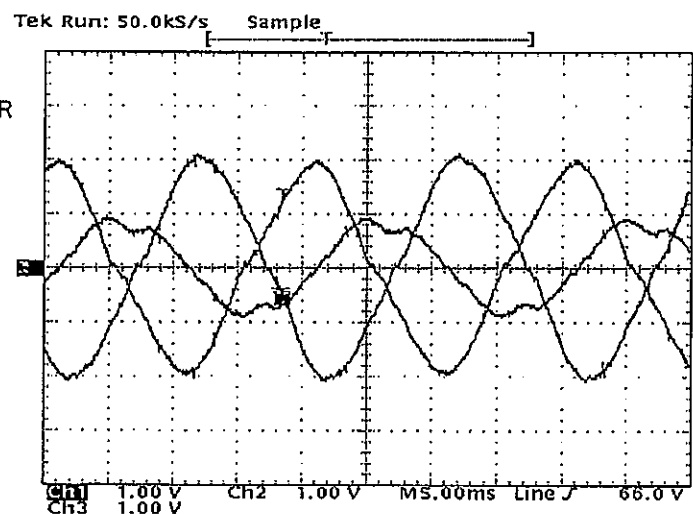
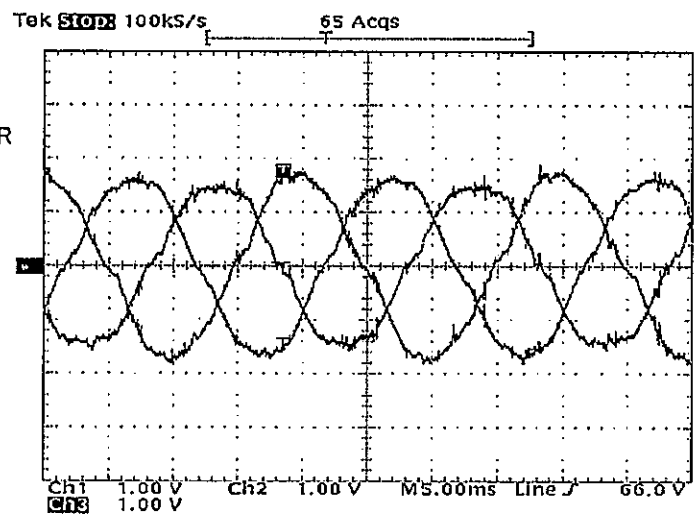


FIG. 13C

Input Current
COMPENSATION FOR
VOLTAGE SAG IS
PERFORMED
(20A/V)



e 1a : 50%

e 1b : 100%

e 1c : 100%

FIG. 14A

Input Voltage

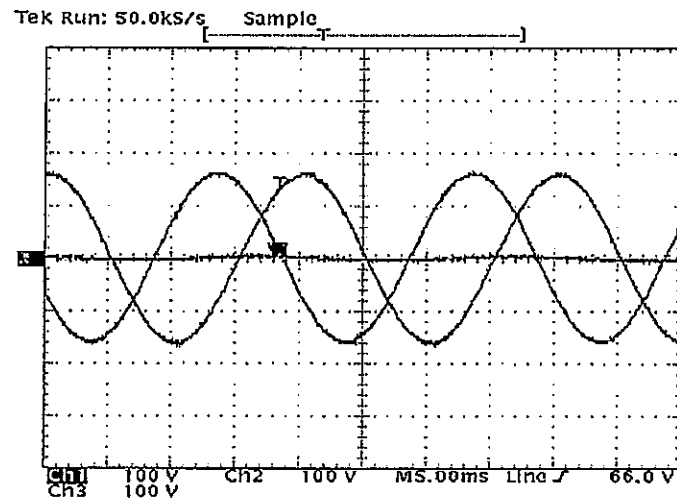


FIG. 14B

Input Current
COMPENSATION FOR
VOLTAGE SAG IS
NOT PERFORMED
(20A/V)

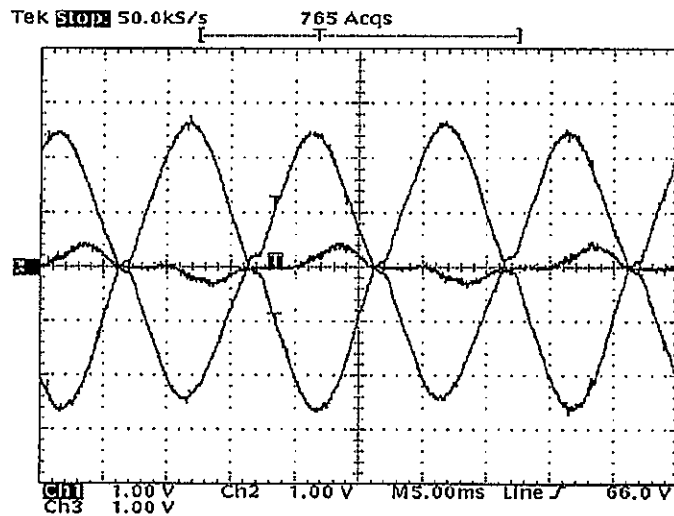
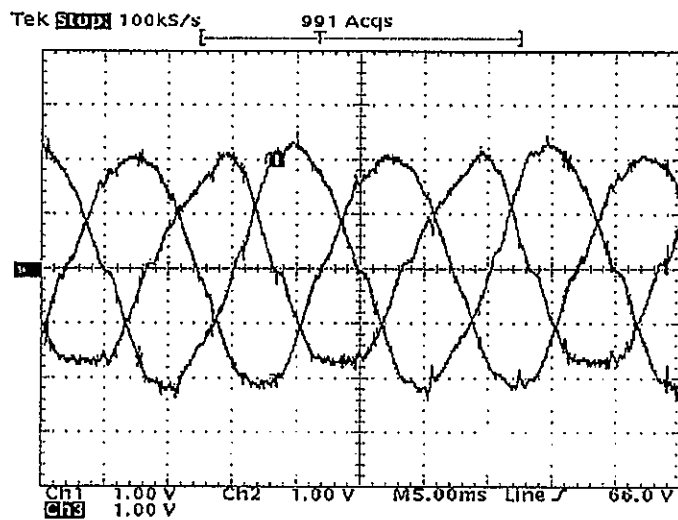


FIG. 14C

Input Current
COMPENSATION FOR
VOLTAGE SAG IS
PERFORMED
(20A/V)



e 1a : 0%

e 1b : 100%

e 1c : 100%

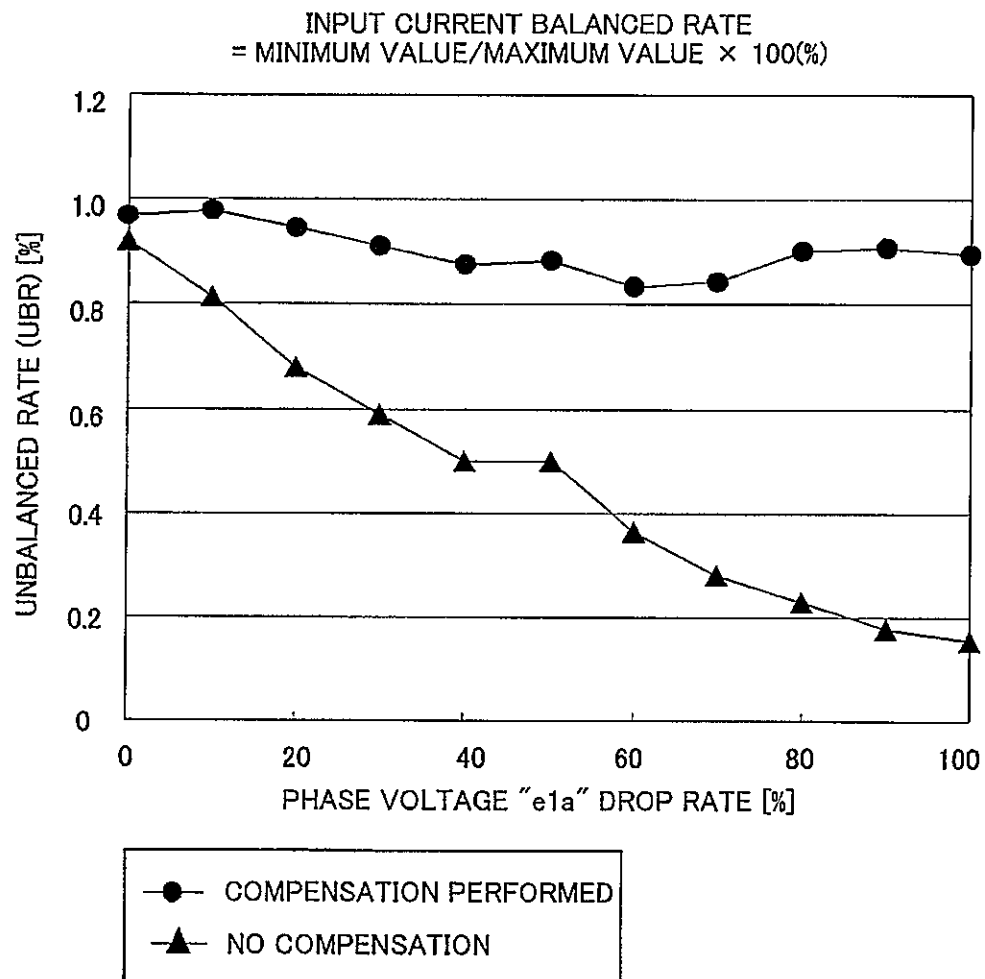


FIG. 15

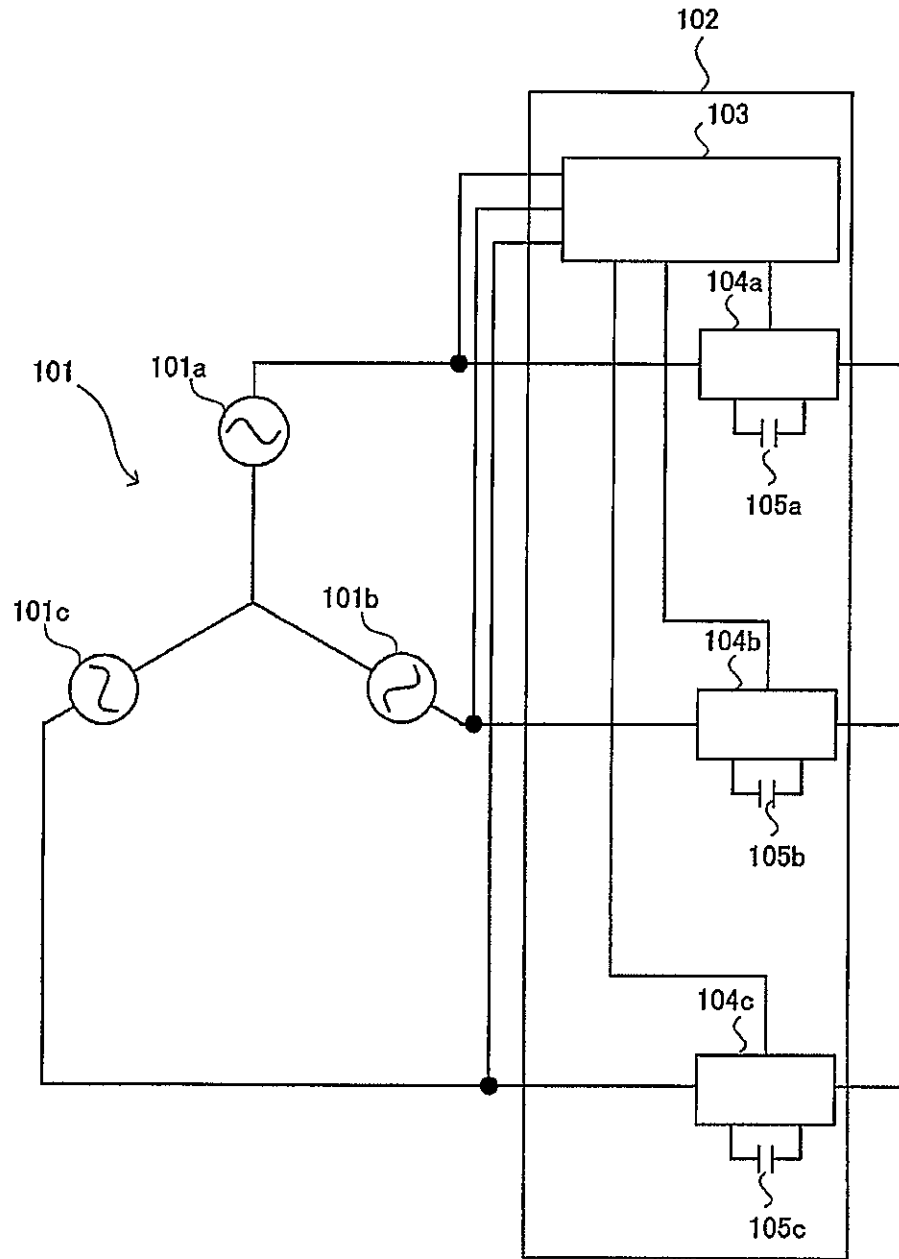
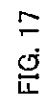


FIG. 16



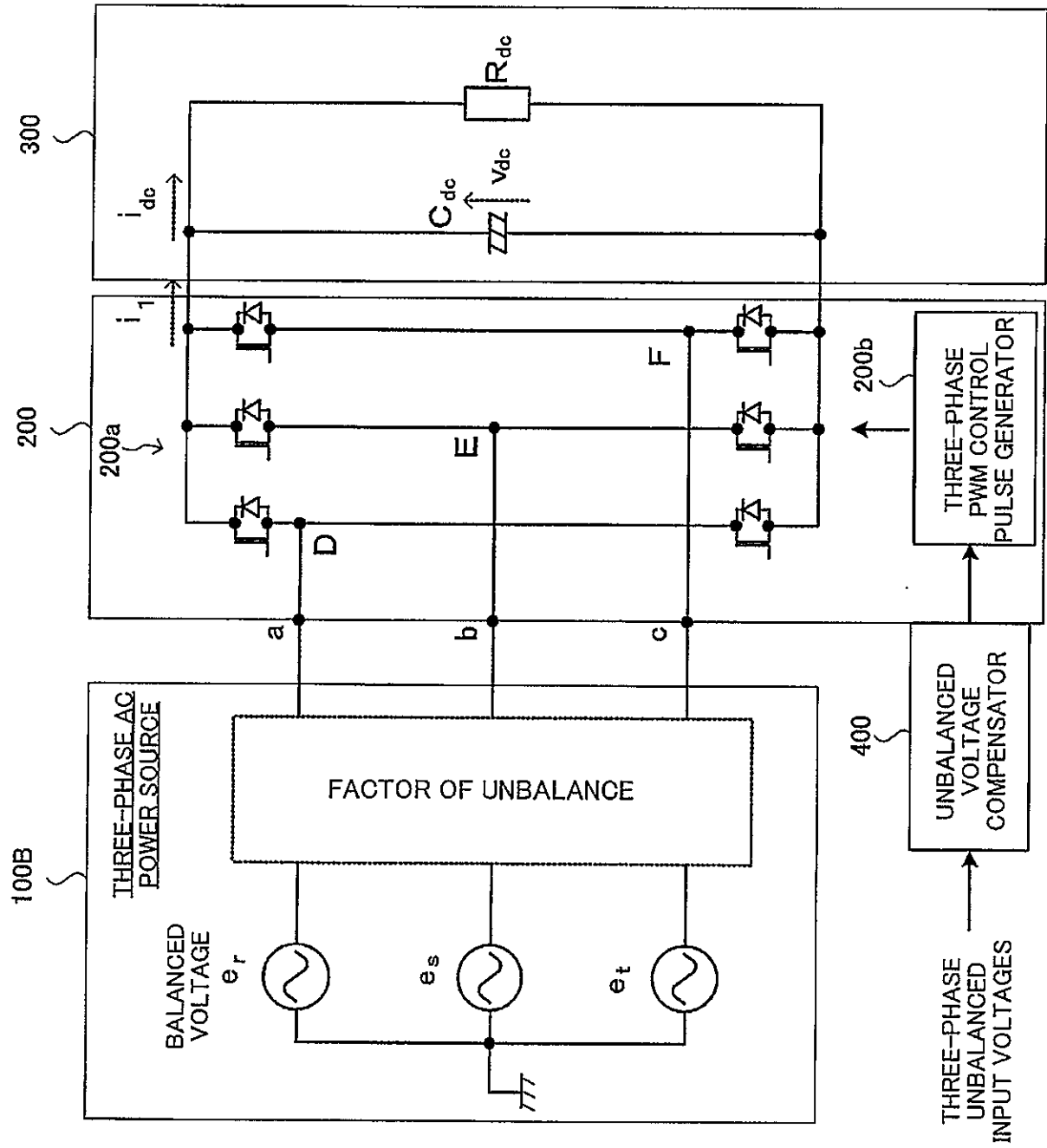


FIG. 18