

A B S T R A C T

An output buffer switching circuit for producing digital output signals comprises a buffer amplifier section (12) for driving a load, a low-impedance transmission line (9) and a power supply section (11) for delivering power to the buffer amplifier section (12). The power supply section (11) comprises a pair of input connections (1,2) for connection to a voltage source and a pair of output connections (3,4) connected to the amplifier section (12), reactance devices for temporary energy storage and switching devices adapted to provide a charge phase in which energy from the voltage source is stored in the reactance devices and a discharge phase in which at least some of the energy stored in the reactance devices is discharged to the output connections (3,4).