

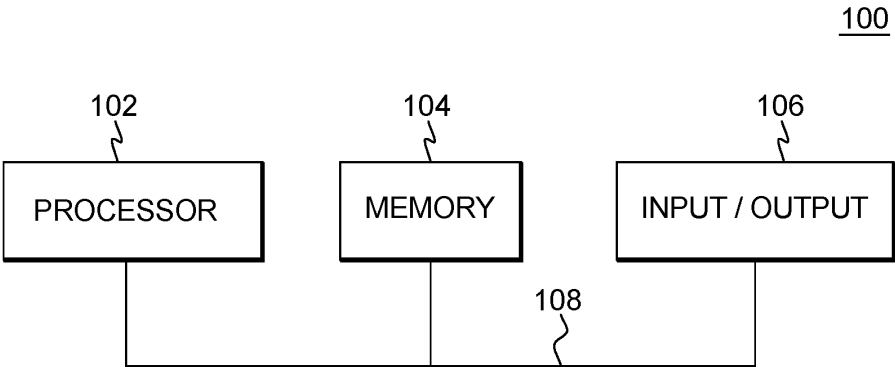


FIG. 1A

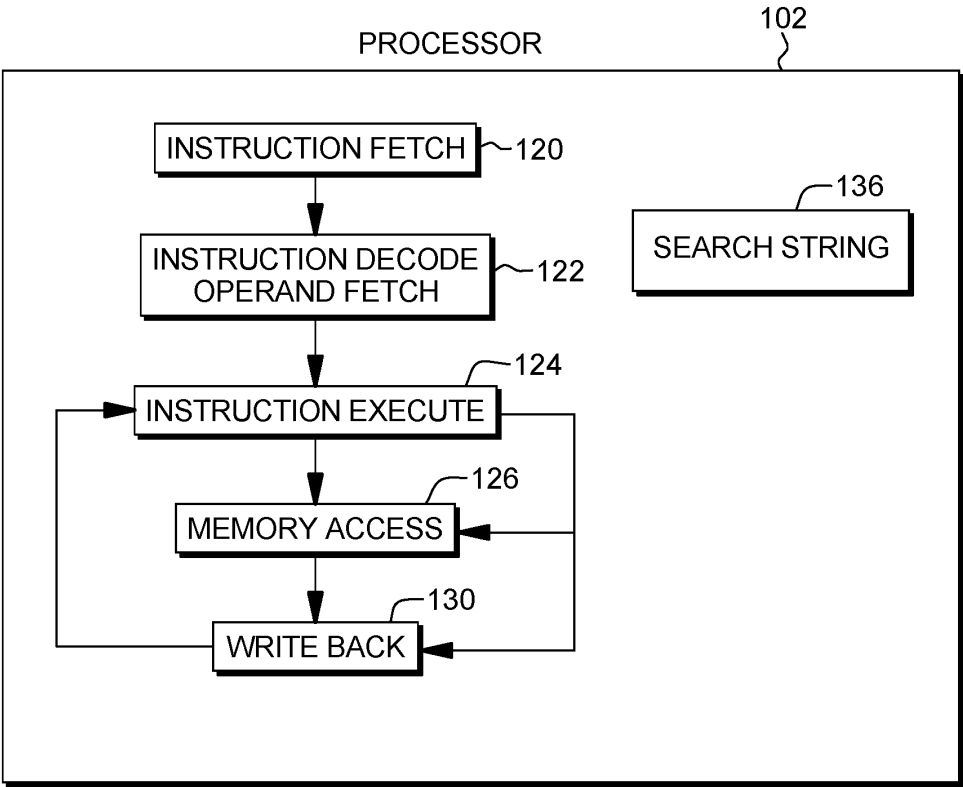


FIG. 1B

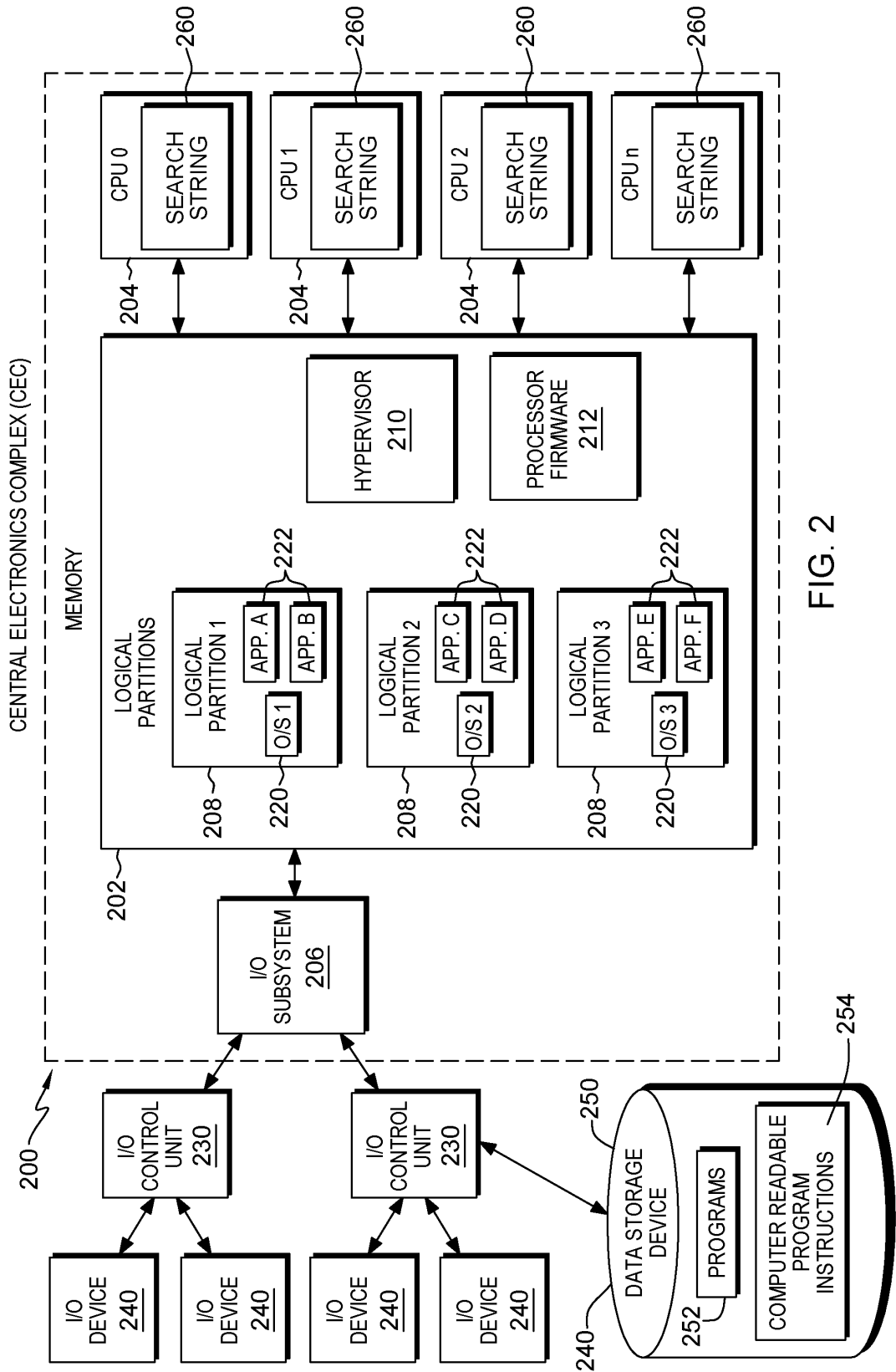


FIG. 2

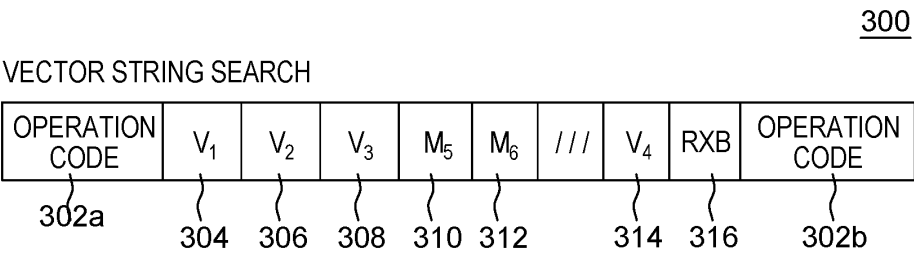


FIG. 3

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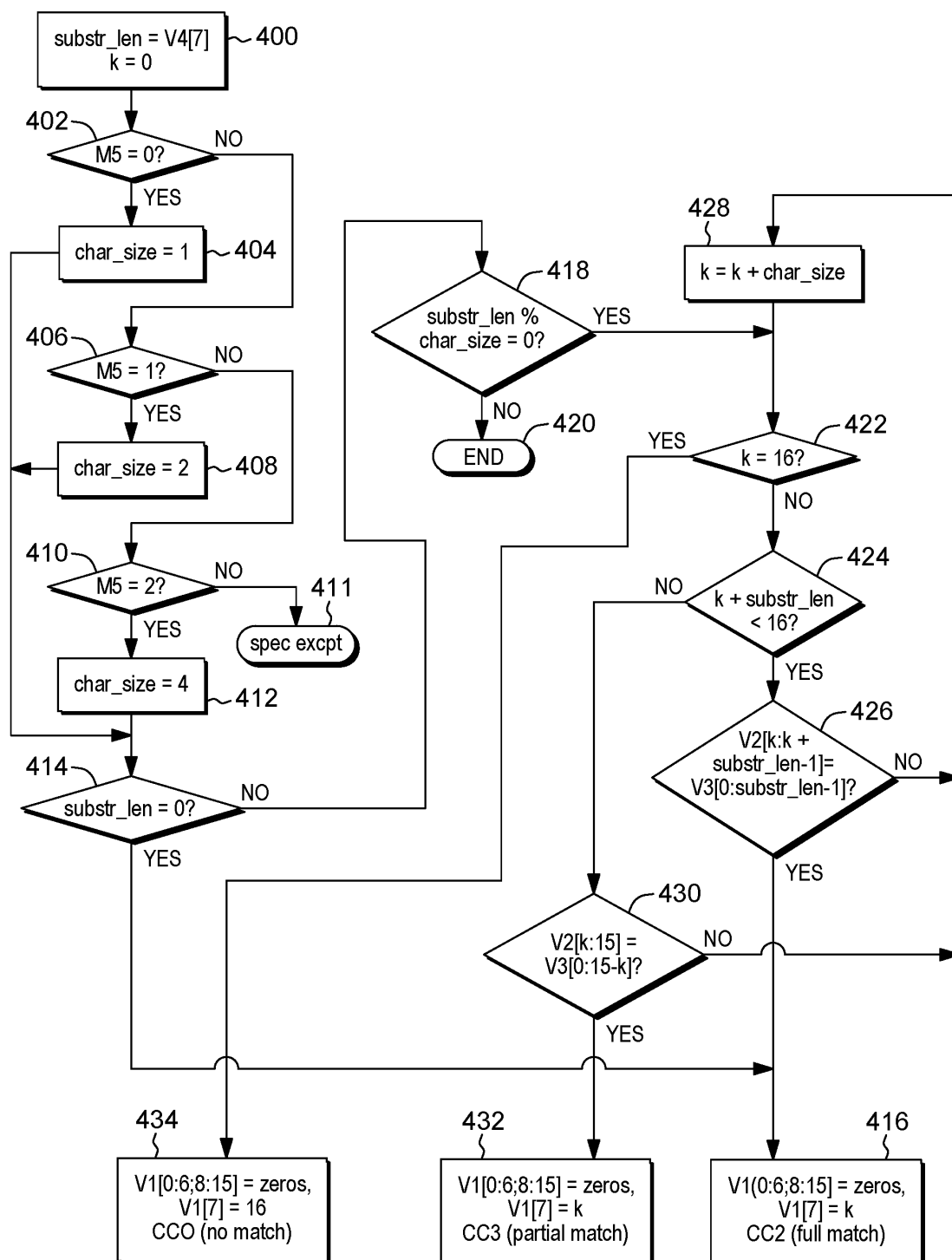


FIG. 4

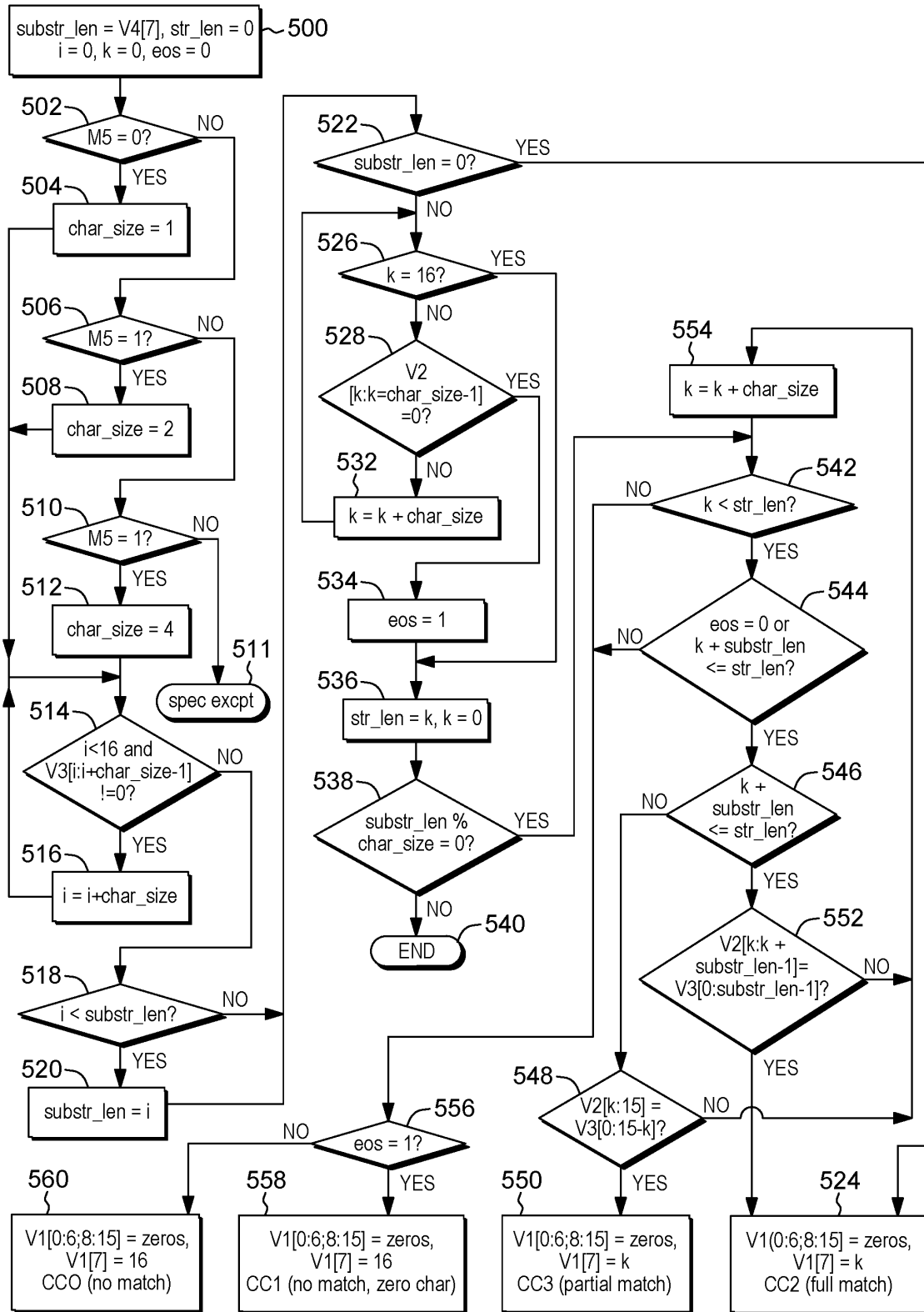


FIG. 5

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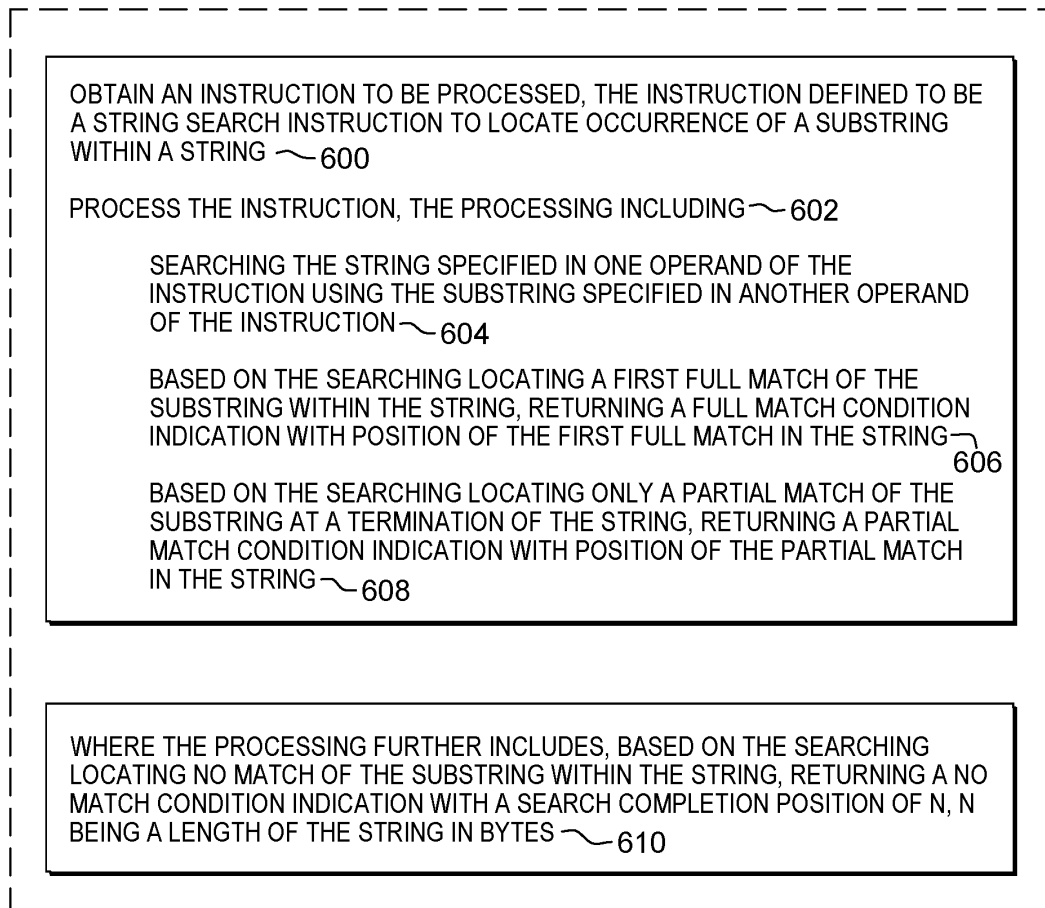


FIG. 6A

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WHERE THE PROCESSING FURTHER INCLUDES DETERMINING A MODE FOR THE SEARCHING, THE SEARCHING INCLUDING A NON-ZERO-TERMINATED SEARCH MODE AND A ZERO-TERMINATED SEARCH MODE, AND THE DETERMINING BEING BASED ON WHETHER A ZERO-SEARCH FLAG IN A FIELD OF THE INSTRUCTION IS SET TO INDICATE THAT THE STRING OR SUBSTRING CAN CONTAIN A ZERO TERMINATION THAT TRUNCATES THE STRING OR SUBSTRING, RESPECTIVELY, FOR THE SEARCHING — 612

WHERE THE PROCESSING FURTHER INCLUDES, BASED ON THE MODE FOR THE SEARCHING BEING THE ZERO-TERMINATED SEARCH MODE, DETERMINING THAT THE STRING INCLUDES THE ZERO TERMINATION, AND BASED THEREON, SETTING A ZERO-TERMINATION FOUND INDICATOR — 614

WHERE THE PROCESSING FURTHER INCLUDES, BASED ON THE SEARCHING REACHING THE ZERO TERMINATION WITHIN THE STRING WITHOUT A MATCH, TERMINATING THE SEARCHING AND RETURNING, WITH REFERENCE TO THE ZERO-TERMINATION FOUND INDICATOR, A NO MATCH WITH ZERO TERMINATION CONDITION INDICATION, WITH A SEARCH COMPLETION POSITION OF N, N BEING A LENGTH OF THE STRING IN BYTES — 616

WHERE THE DETERMINING FURTHER INCLUDES DETERMINING THAT A LENGTH OF THE STRING BEFORE THE ZERO TERMINATION WITHIN THE STRING IS SMALLER THAN THE SUBSTRING LENGTH, AND BASED THEREON, DETERMINING WHETHER A PORTION OF THE SUBSTRING MATCHES THE STRING BEFORE THE ZERO TERMINATION — 618

PROCEEDING WITH THE SEARCHING BASED ON A LENGTH OF THE SUBSTRING BEING A MULTIPLE OF A SIZE OF ELEMENTS WITHIN THE STRING, THE SIZE BEING DETERMINED FROM A FIELD OF THE INSTRUCTION — 620

WHERE THE SEARCHING IS BASED, IN PART, ON THE SIZE OF THE ELEMENTS WITHIN THE STRING, THE SEARCHING INCLUDING ELEMENT-BASED STEPPING THROUGH THE STRING, WHEREIN THE SIZE IS SELECTED FROM THE GROUP CONSISTING OF 1 BYTE, 2 BYTES, OR 4 BYTES — 622

WHERE THE STRING SEARCH INSTRUCTION IS AN ARCHITECTED HARDWARE MACHINE INSTRUCTION OF AN INSTRUCTION SET ARCHITECTURE — 624

WHERE THE ARCHITECTED HARDWARE MACHINE INSTRUCTION IS A VECTOR STRING SEARCH INSTRUCTION — 626

FIG. 6B

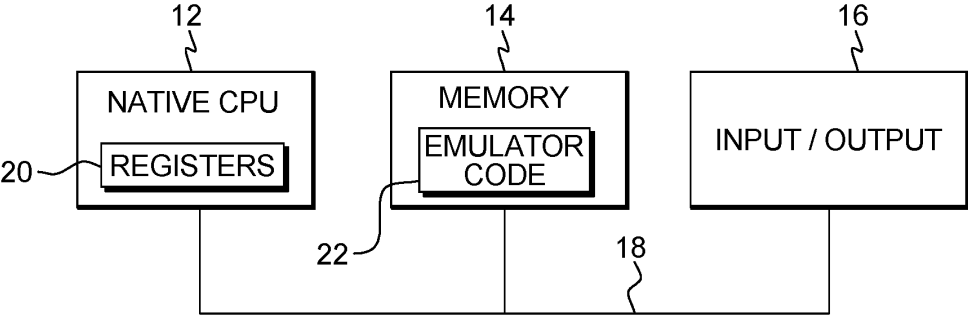


FIG. 7A

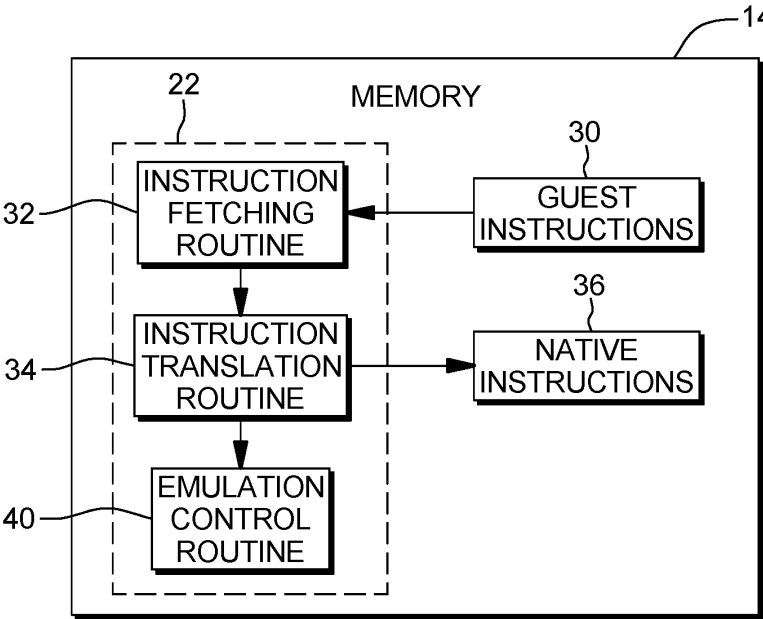


FIG. 7B

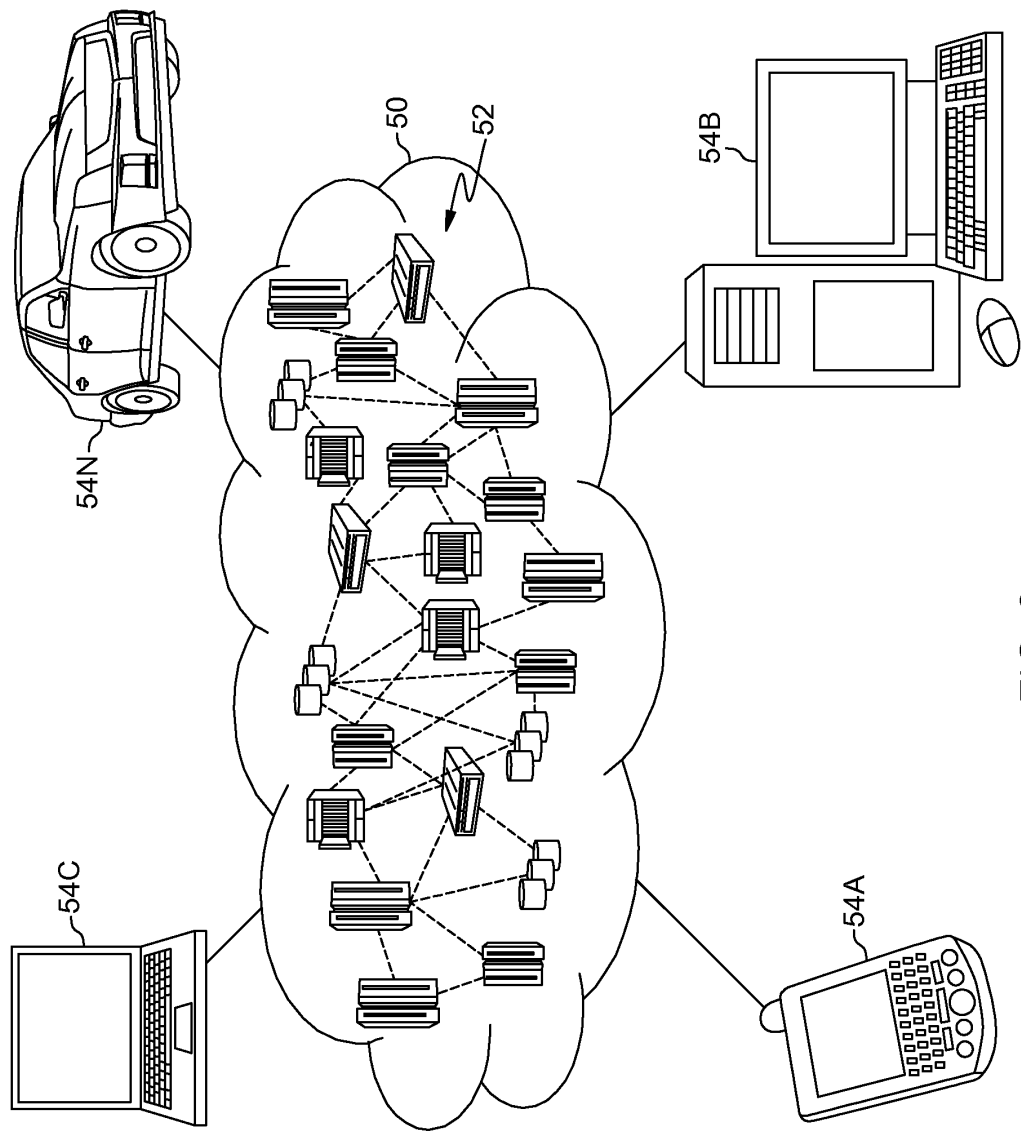


FIG. 8

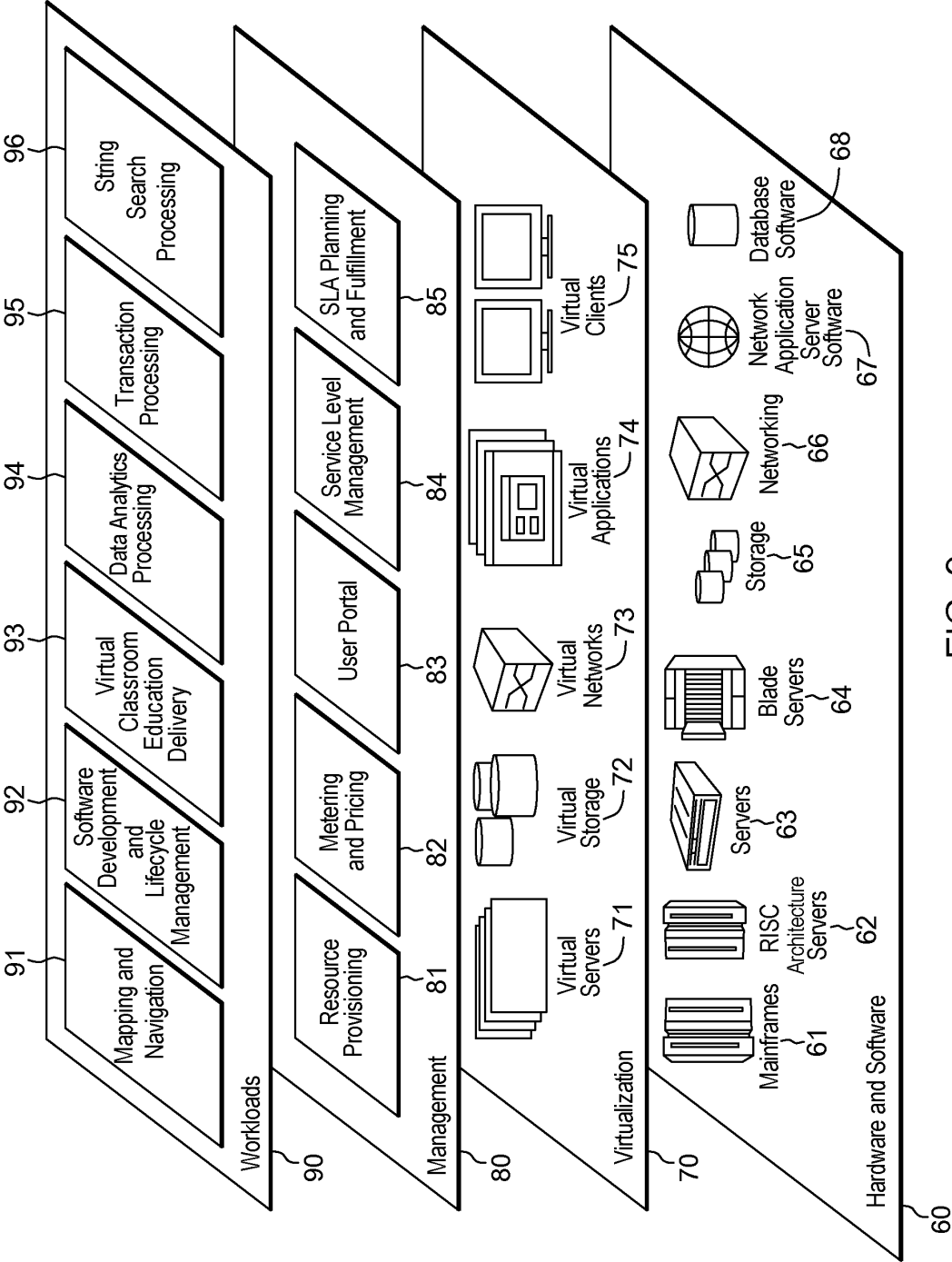


FIG. 9