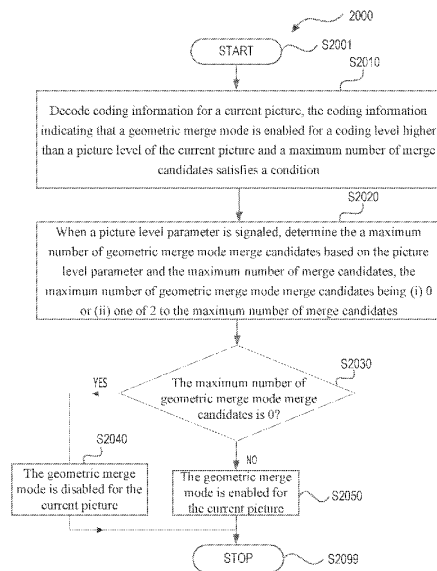




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(54) **Title:** METHOD AND APPARATUS FOR VIDEO CODING**FIG. 20**

(57) **Abstract:** Aspects of the disclosure provide a method and an apparatus including processing circuitry for video decoding. The processing circuitry decodes, from a coded video bitstream, coding information for a current picture. The coding information indicates that a geometric merge mode is enabled for a coding level higher than a picture level of the current picture and a maximum number of merge candidates satisfies a condition. The processing circuitry determines, based on a picture level parameter signaled for the current picture in the coded video bitstream, a maximum number of geometric merge mode merge candidates based on the picture level parameter and the maximum number of merge candidates. The maximum number of geometric merge mode merge candidates can be (i) 0 or (ii) one of 2 to the maximum number of merge candidates. The picture level parameter indicates the maximum number of geometric merge mode merge candidates.

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