

SEMICONDUCTOR STRUCTURE AND MANUFACTURING METHOD THEREFOR, AND MEMORY

ABSTRACT

Provided are a semiconductor structure and a manufacturing method therefor, and a memory, and relate to the technical field of semiconductors, to resolve the technical problem of difficult manufacturing of the semiconductor structure. The semiconductor structure includes a substrate and a conductive structure located above the substrate. The conductive structure includes a plurality of first conductive structures and second conductive structures that are spaced apart from each other and extend in a first direction. Lengths of the first conductive structures and lengths of the second conductive structures vary in steps. The lengths of the plurality of first conductive structures and the lengths of the plurality of second conductive structures vary in steps. The first conductive structures and the second conductive structures form Word Lines (WLs). The WLs are easy to manufacture and lead out, so that other structures are conveniently formed on the WLs, so as to achieve an electrical connection between the WLs and a peripheral circuit.

[Fig. 1]