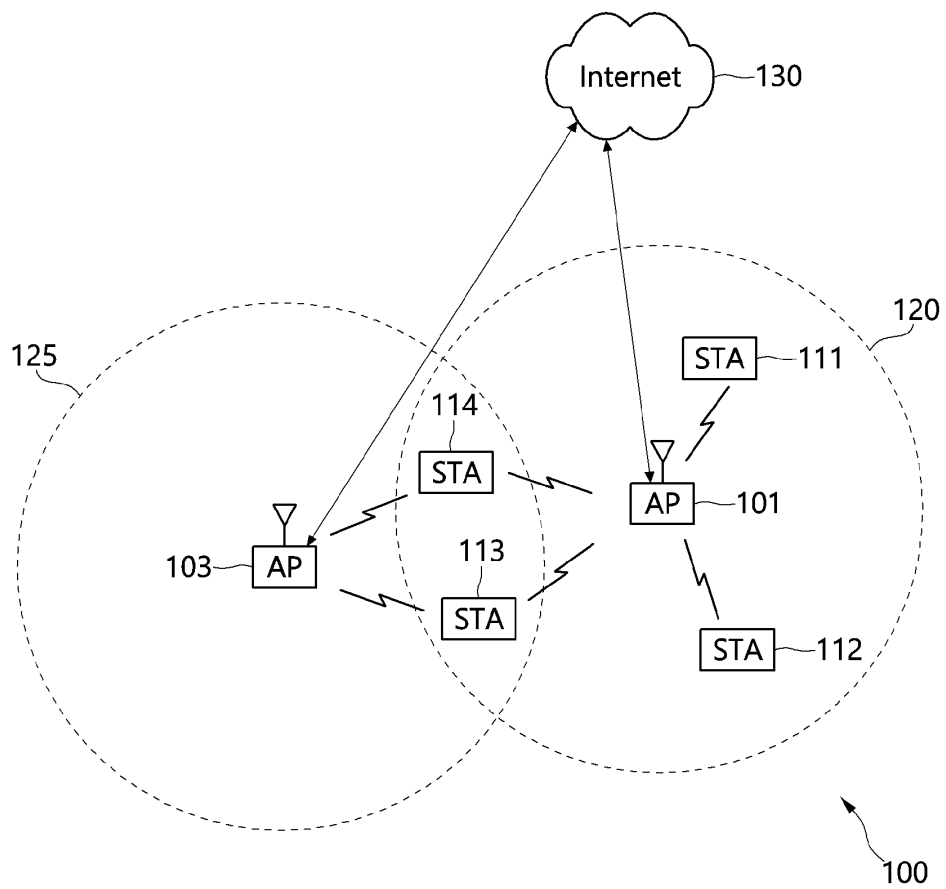
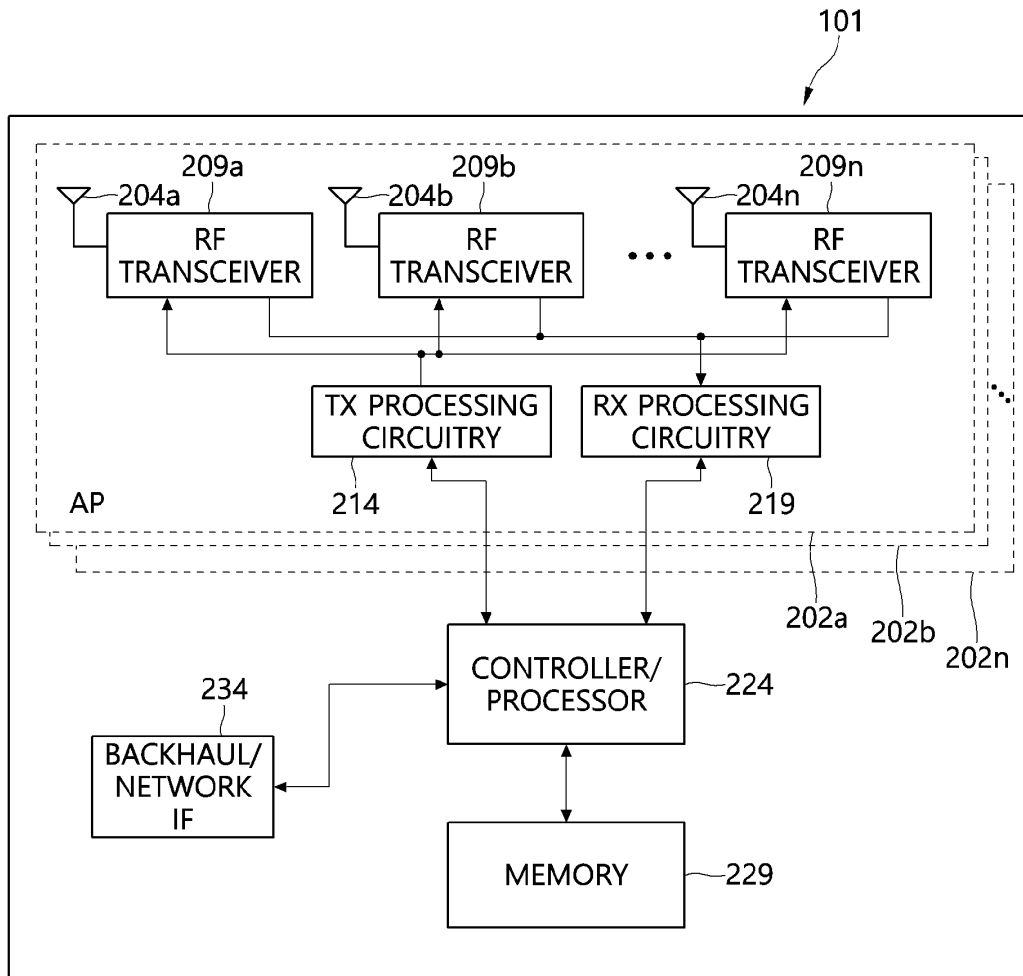


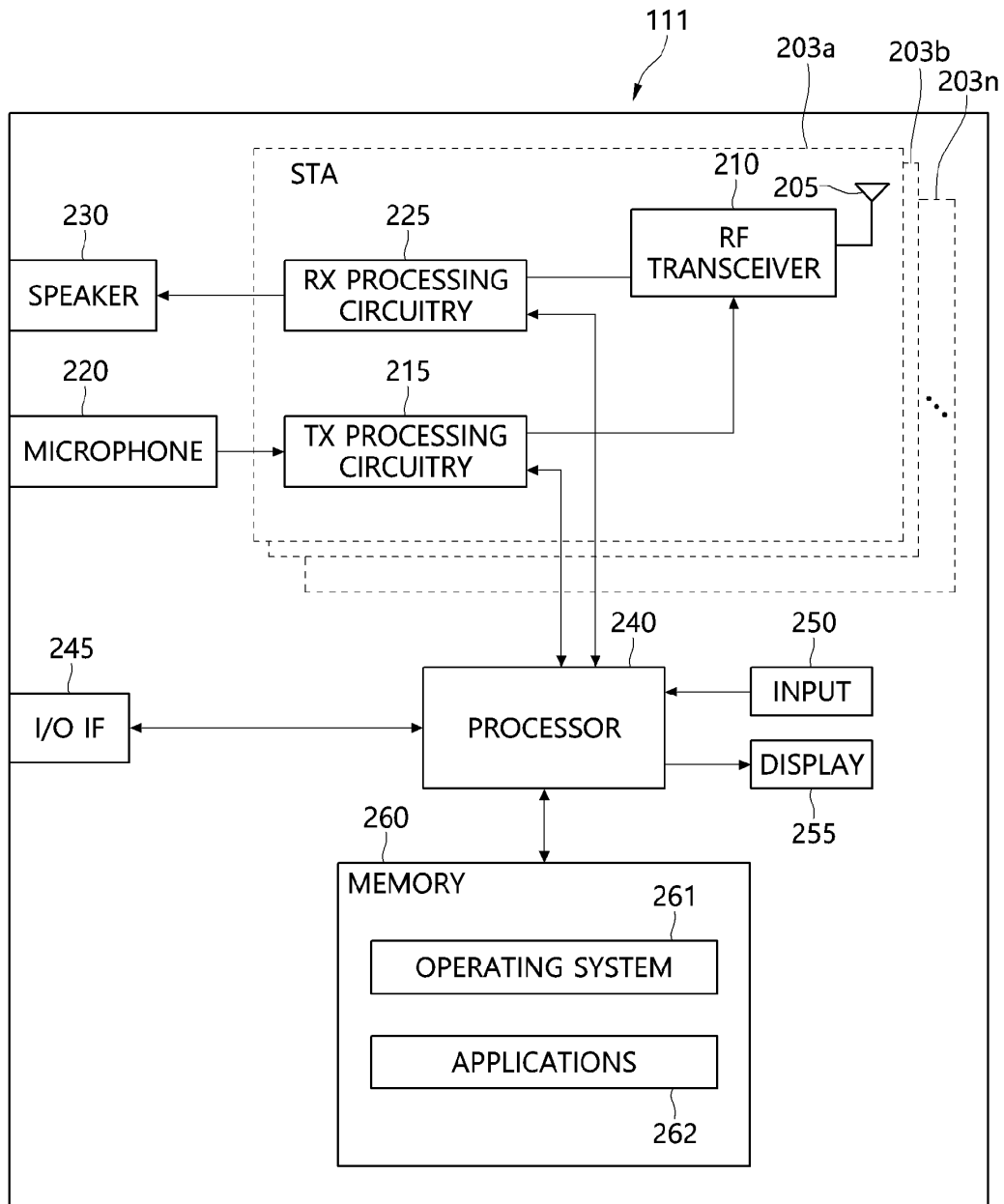
[Fig. 1]



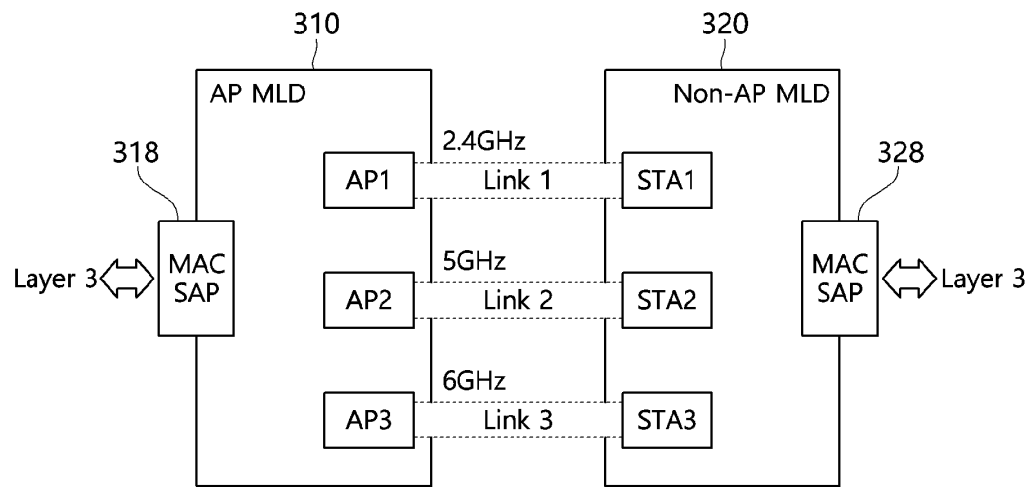
[Fig. 2a]



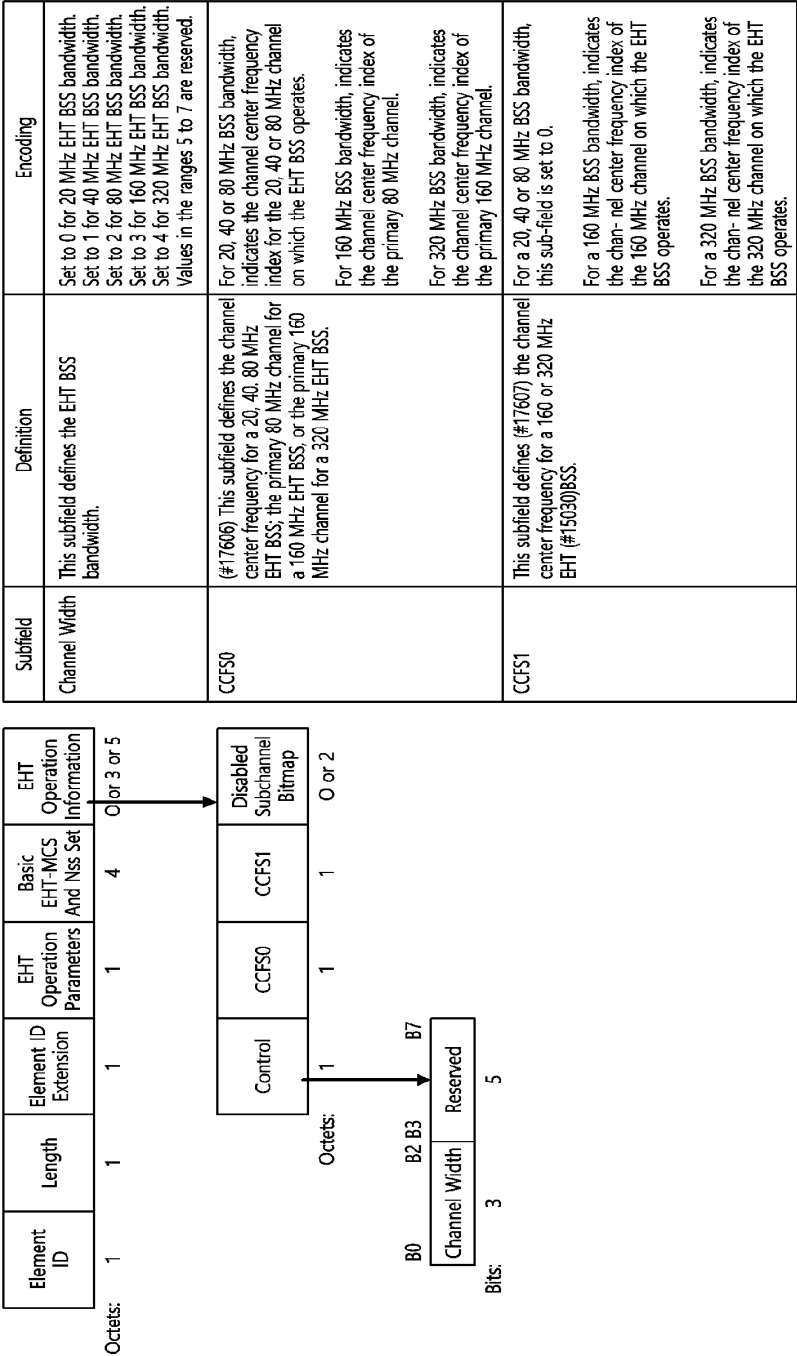
[Fig. 2b]



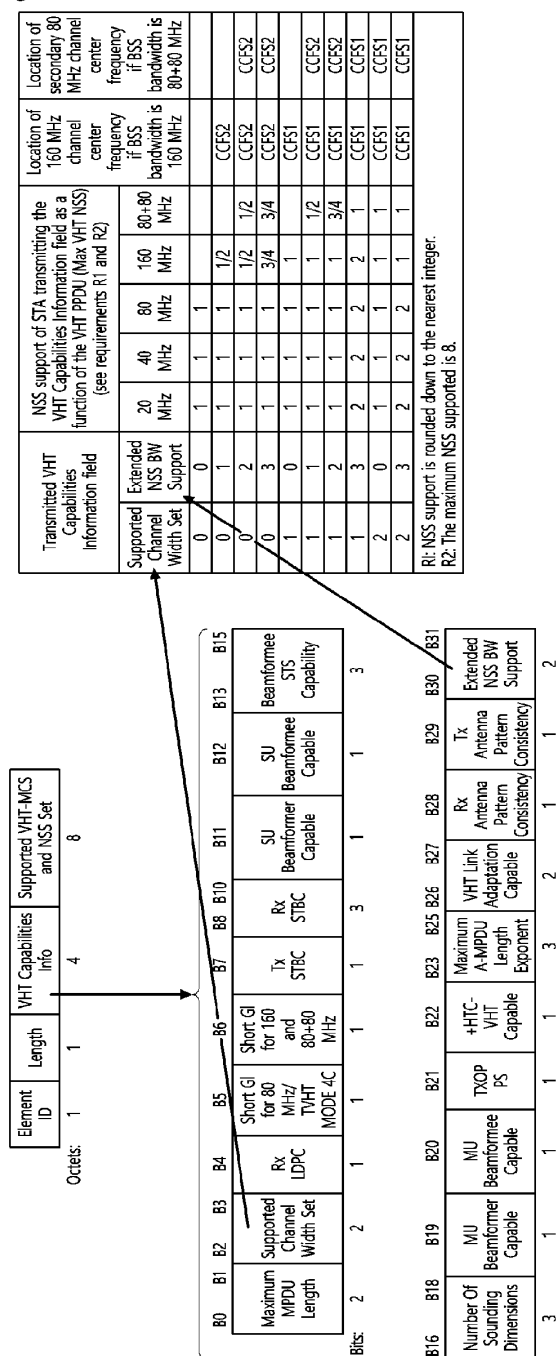
[Fig. 3]



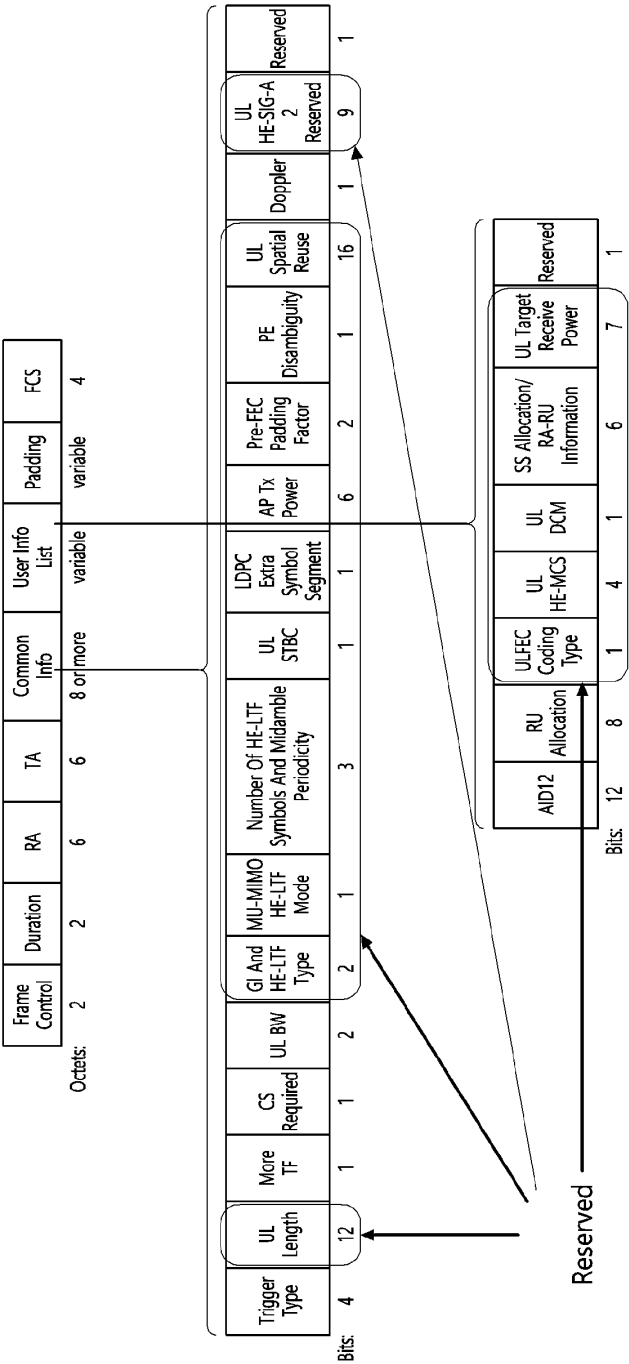
[Fig. 4]



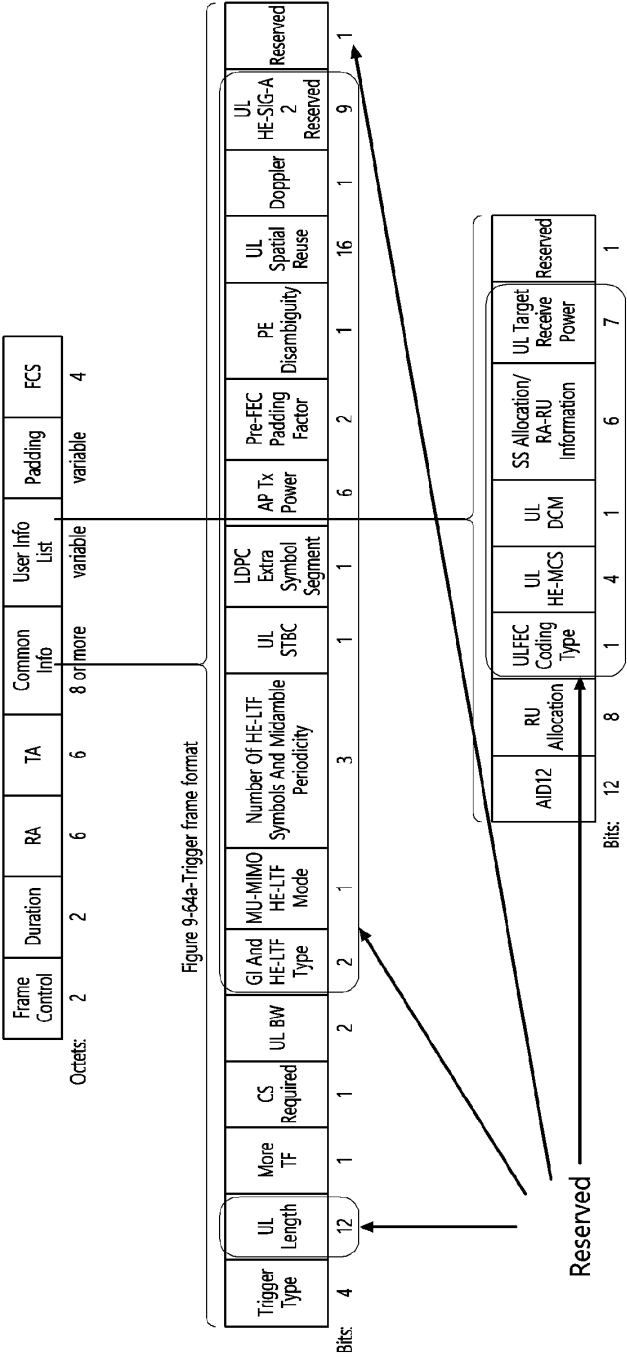
[Fig. 5]



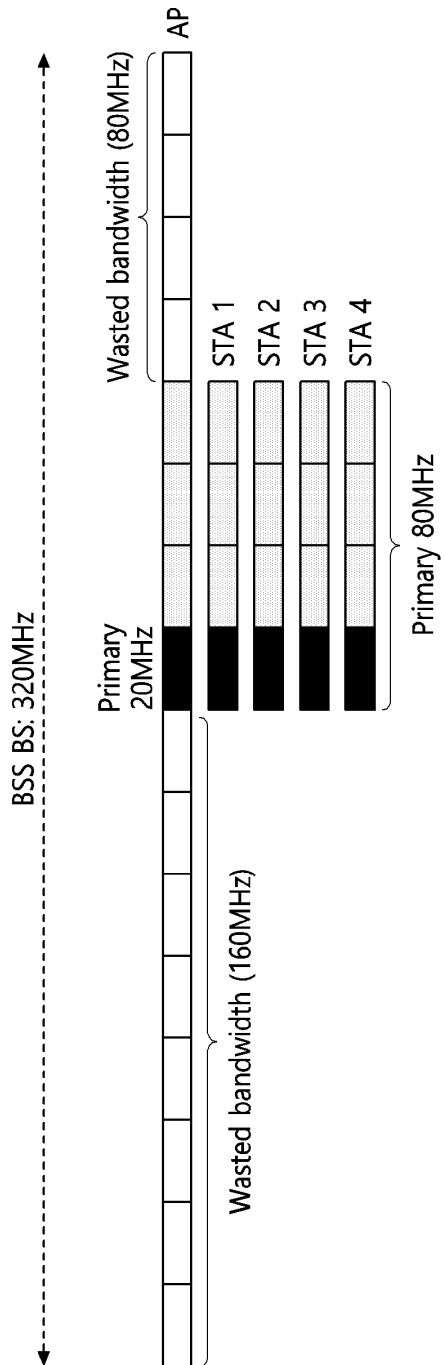
[Fig. 6]



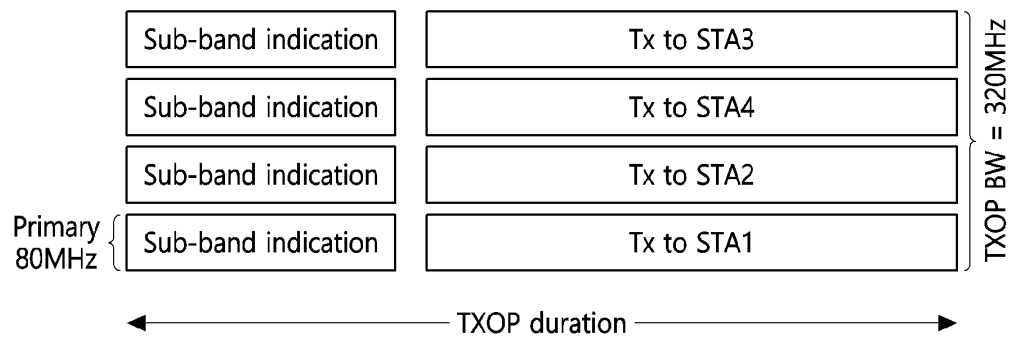
[Fig. 7]



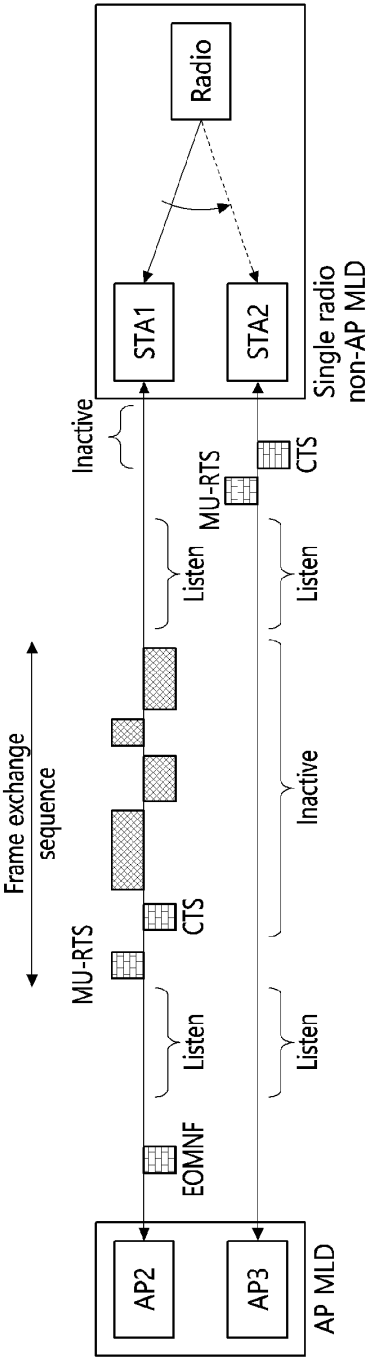
[Fig. 8]



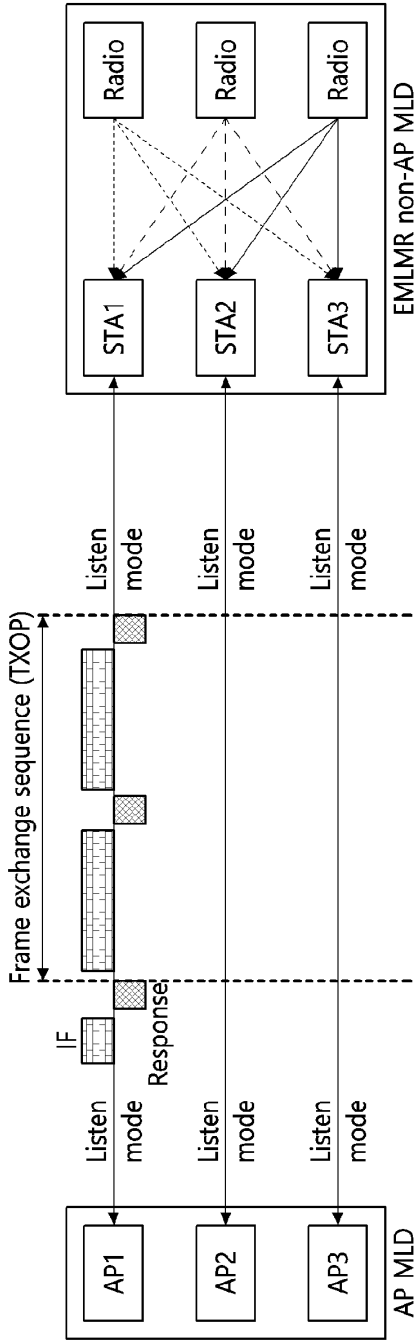
[Fig. 9]



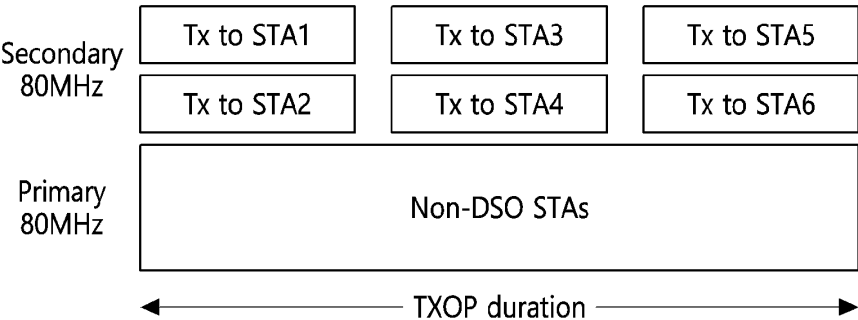
[Fig. 10a]



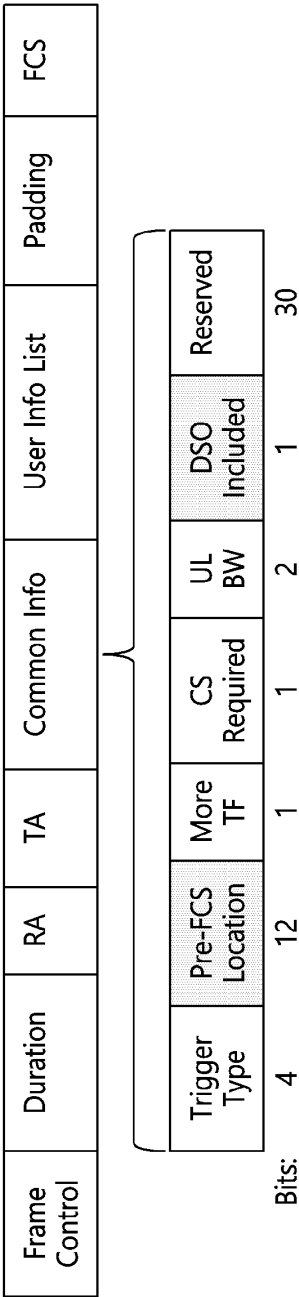
[Fig. 10b]



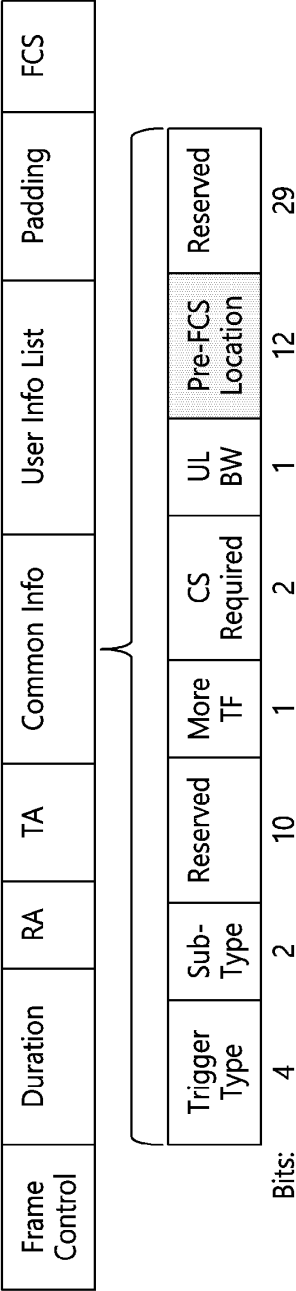
[Fig. 11]



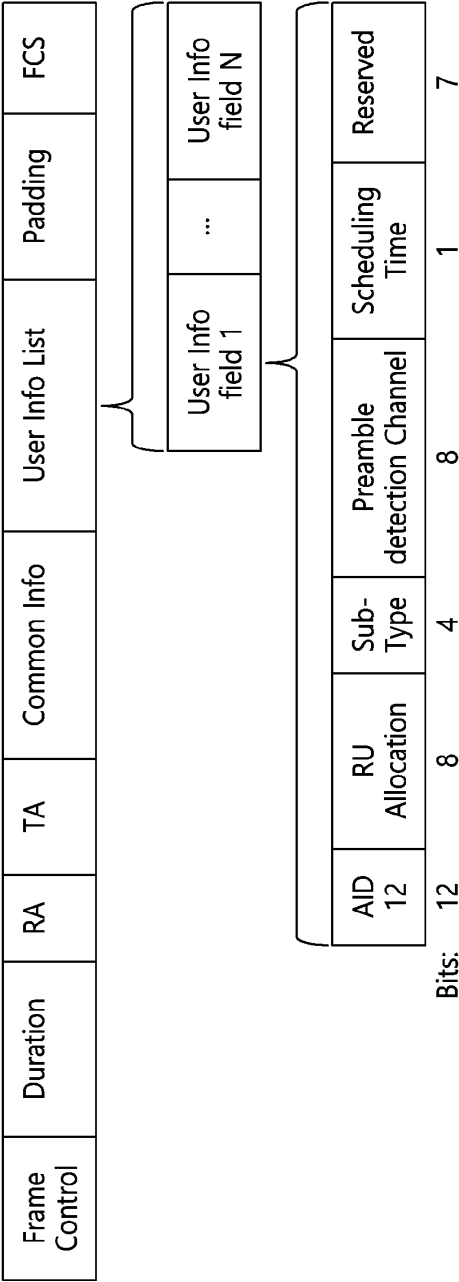
[Fig. 12a]



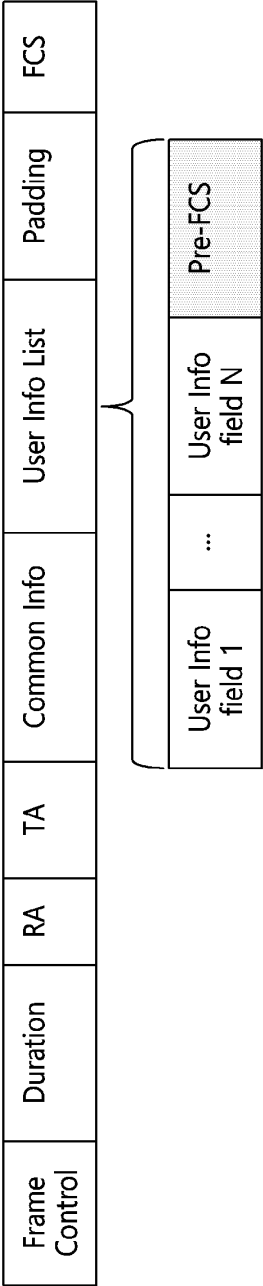
[Fig. 12b]



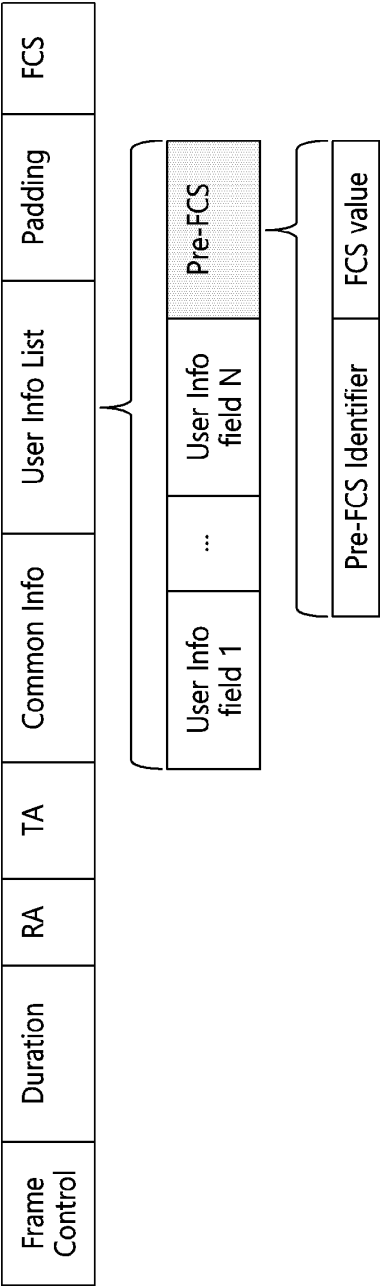
[Fig. 13]



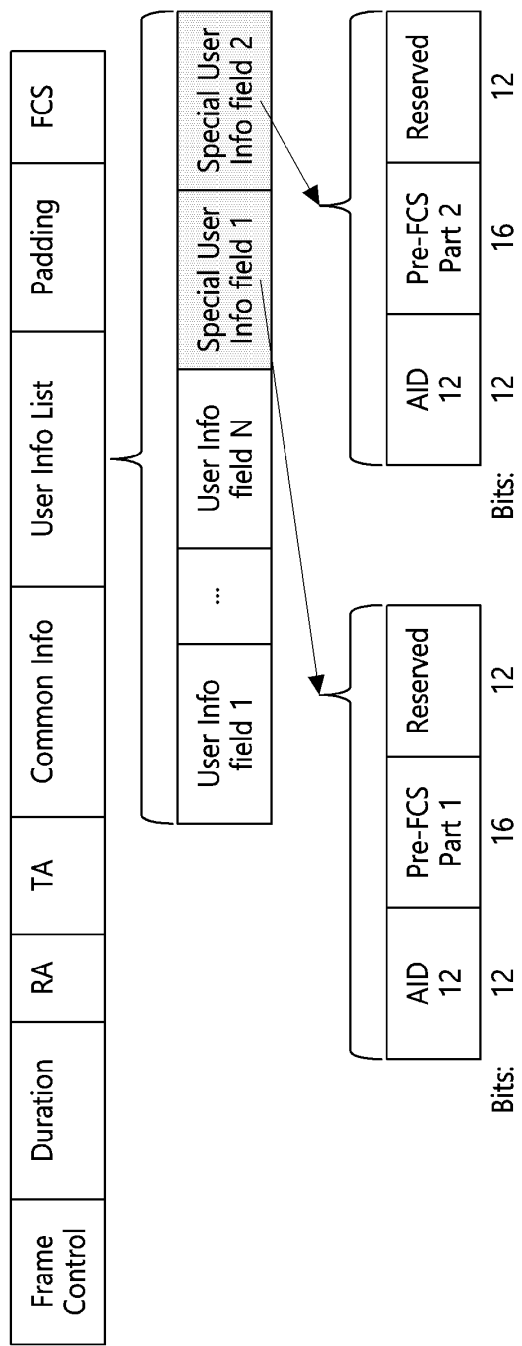
[Fig. 14a]



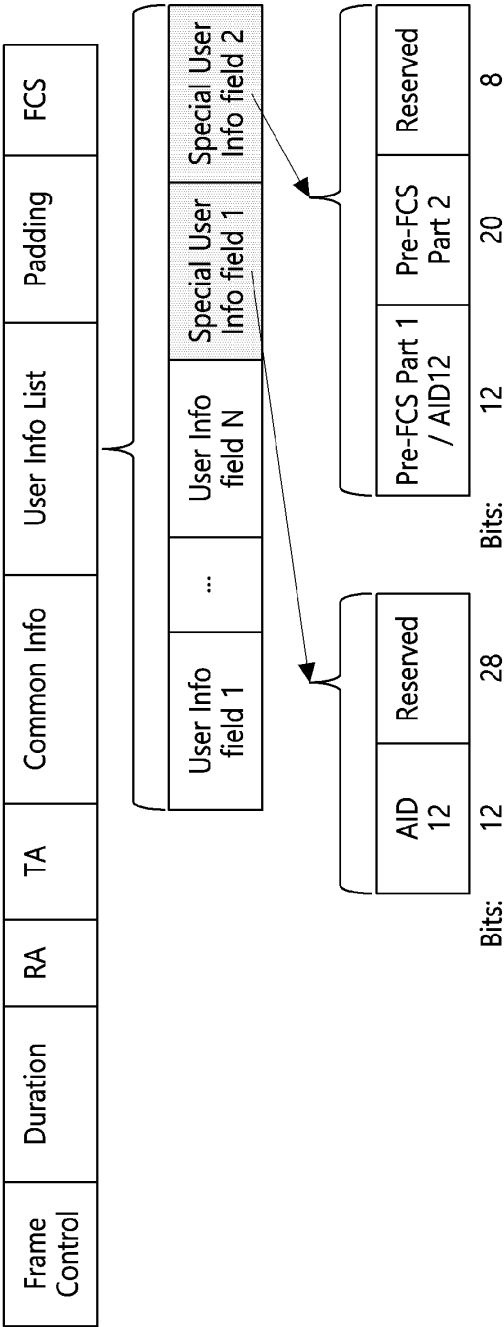
[Fig. 14b]



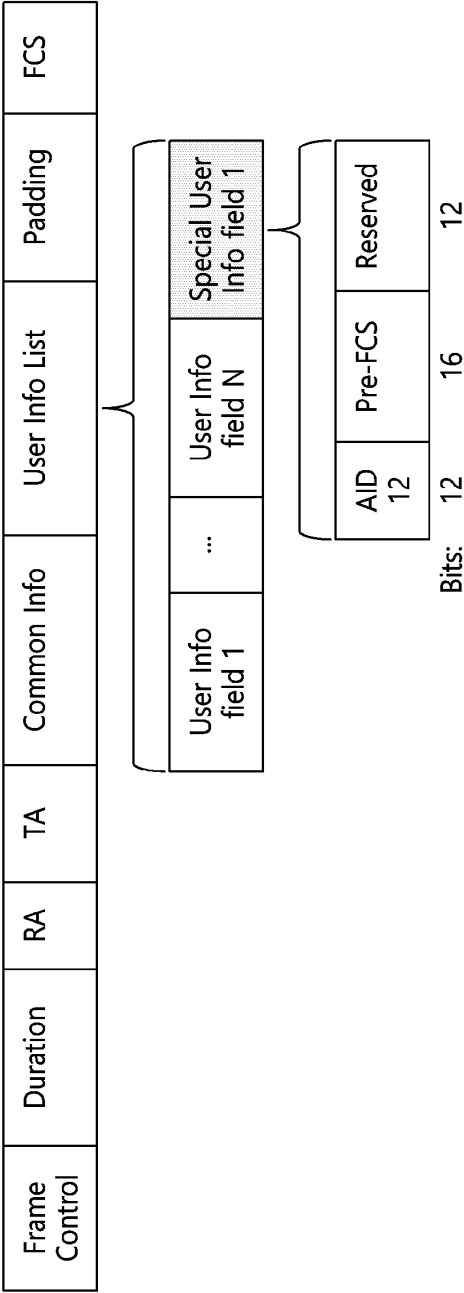
[Fig. 14c]



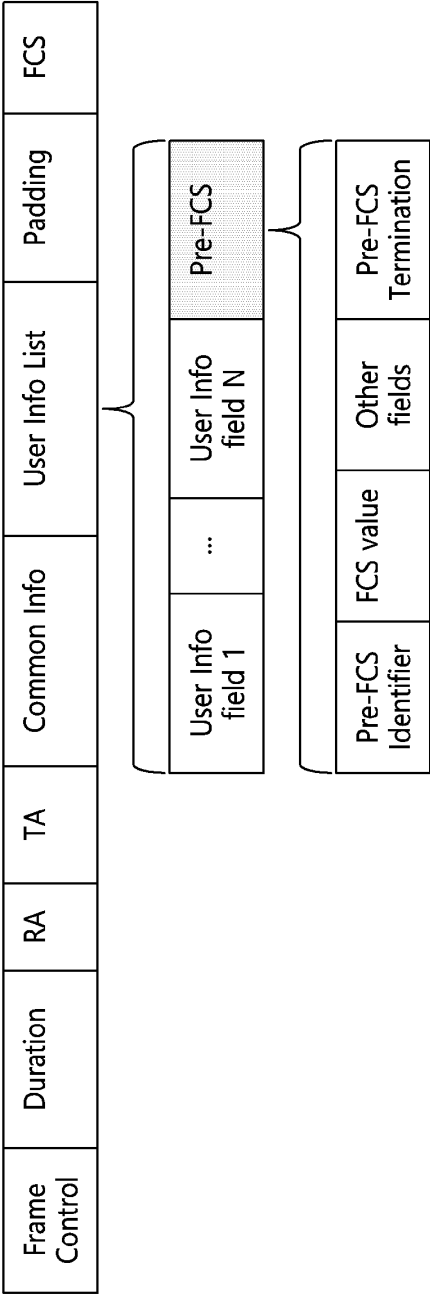
[Fig. 14d]



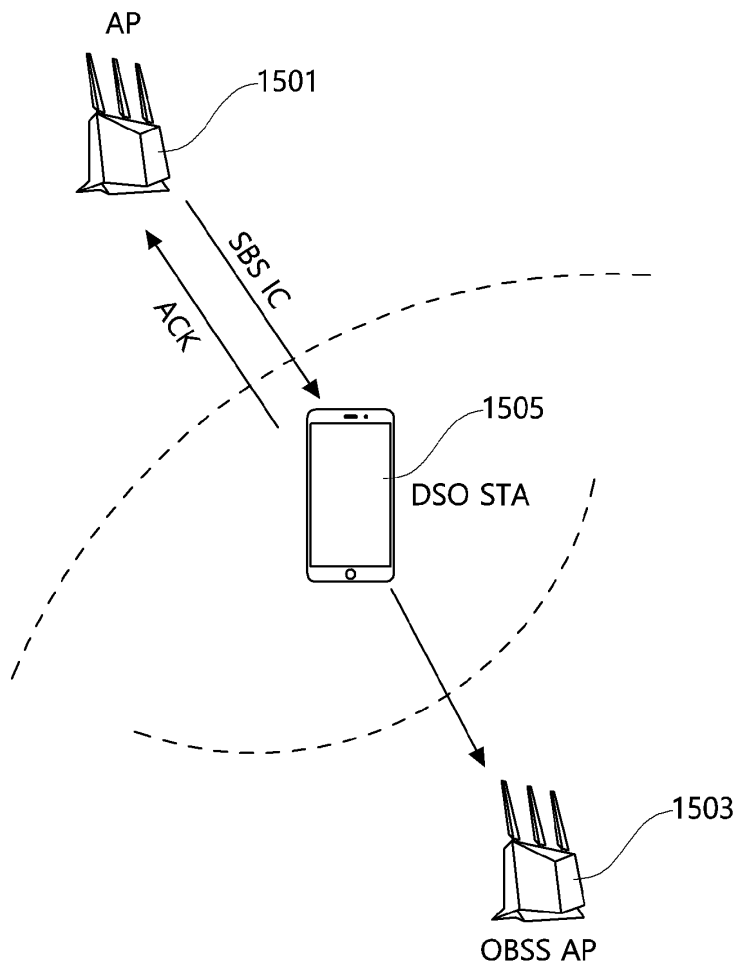
[Fig. 14e]



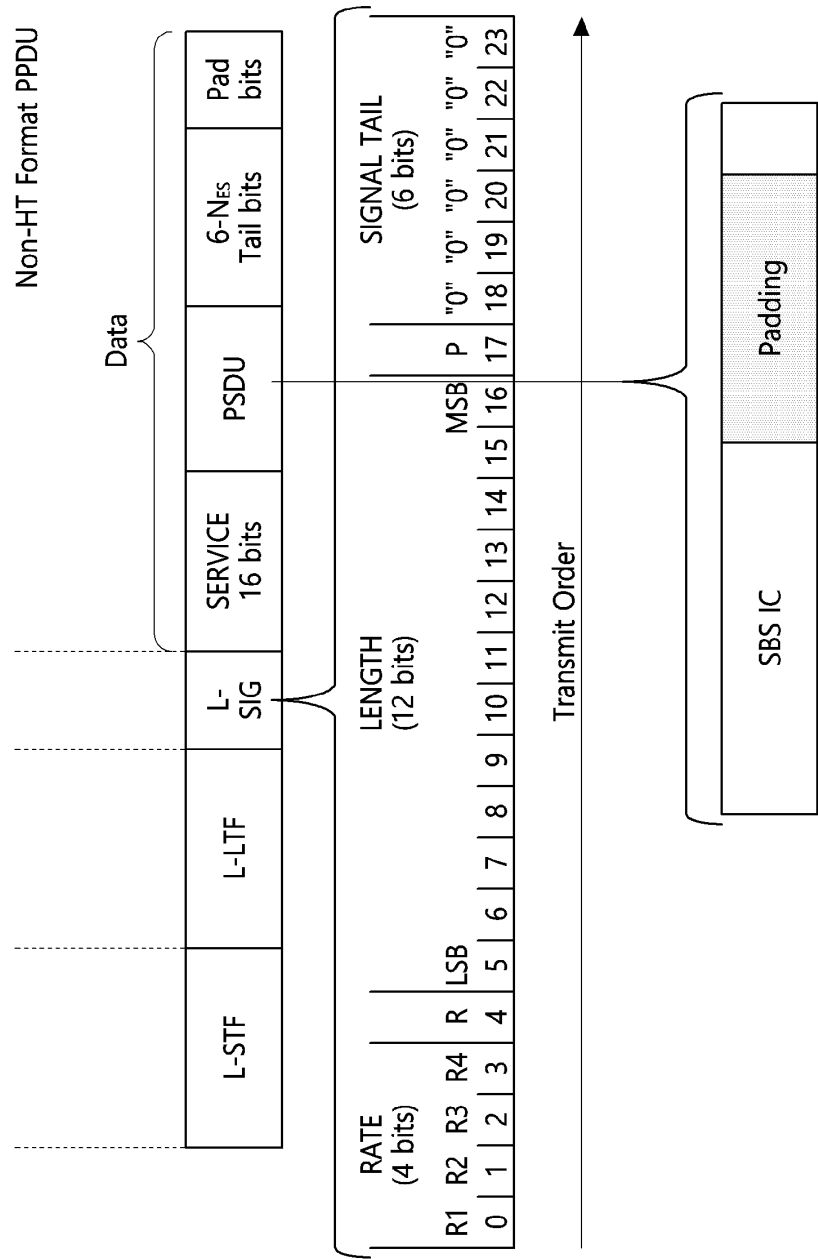
[Fig. 14f]



[Fig. 15]



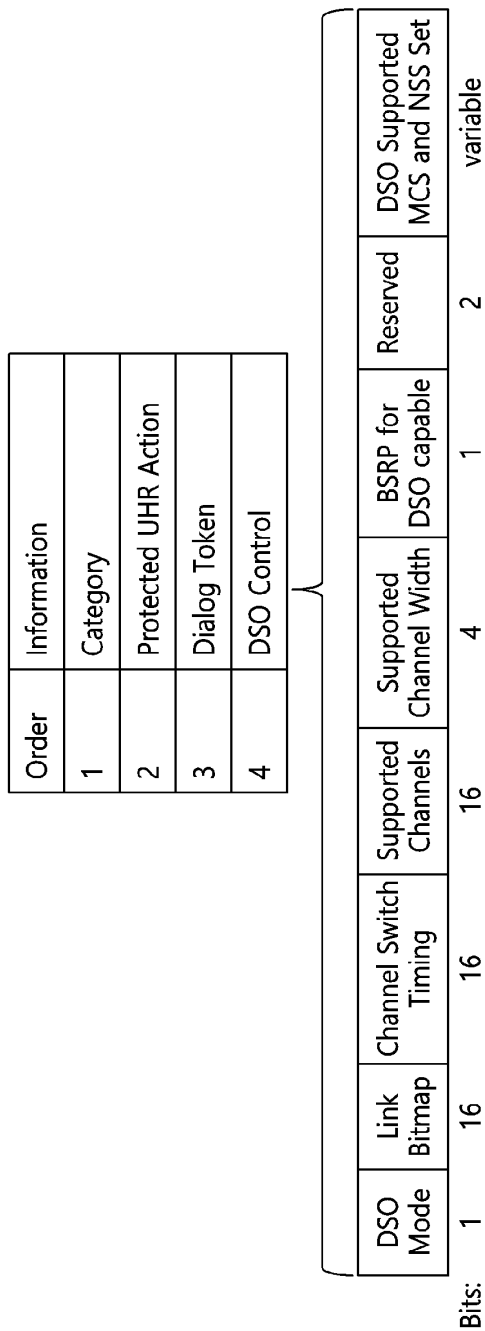
[Fig. 16]



[Fig. 17a]

Element ID	Length	Element ID Extension	UHR MAC Capabilities Information	UHR PHY Capabilities Information
DSO Support		Max. Channel Switch Time	BSRP for DSO Capable	Reserved

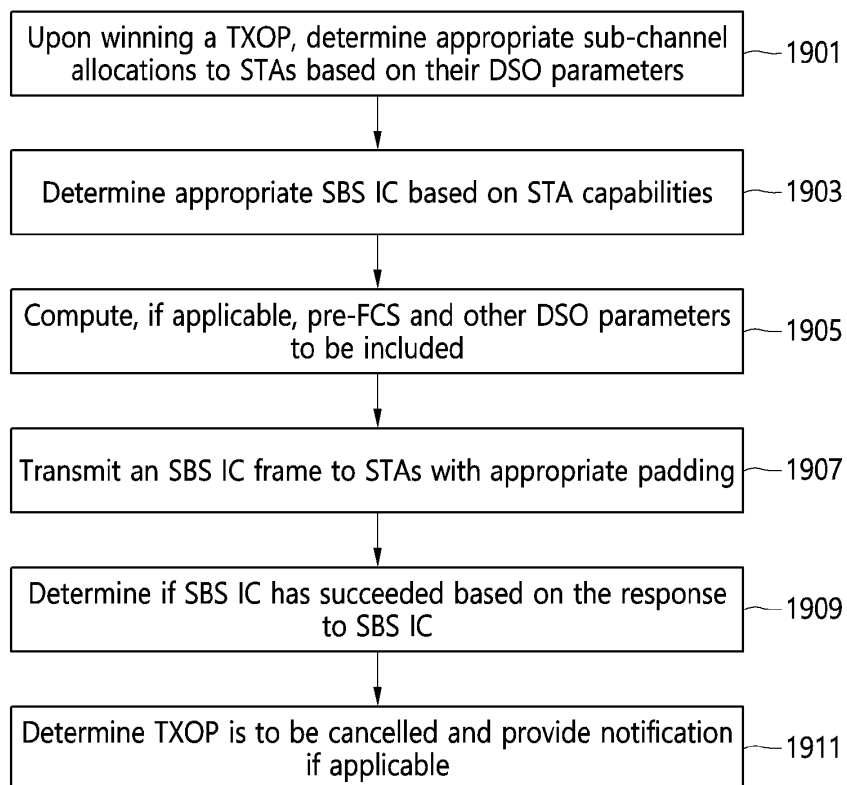
[Fig. 17b]



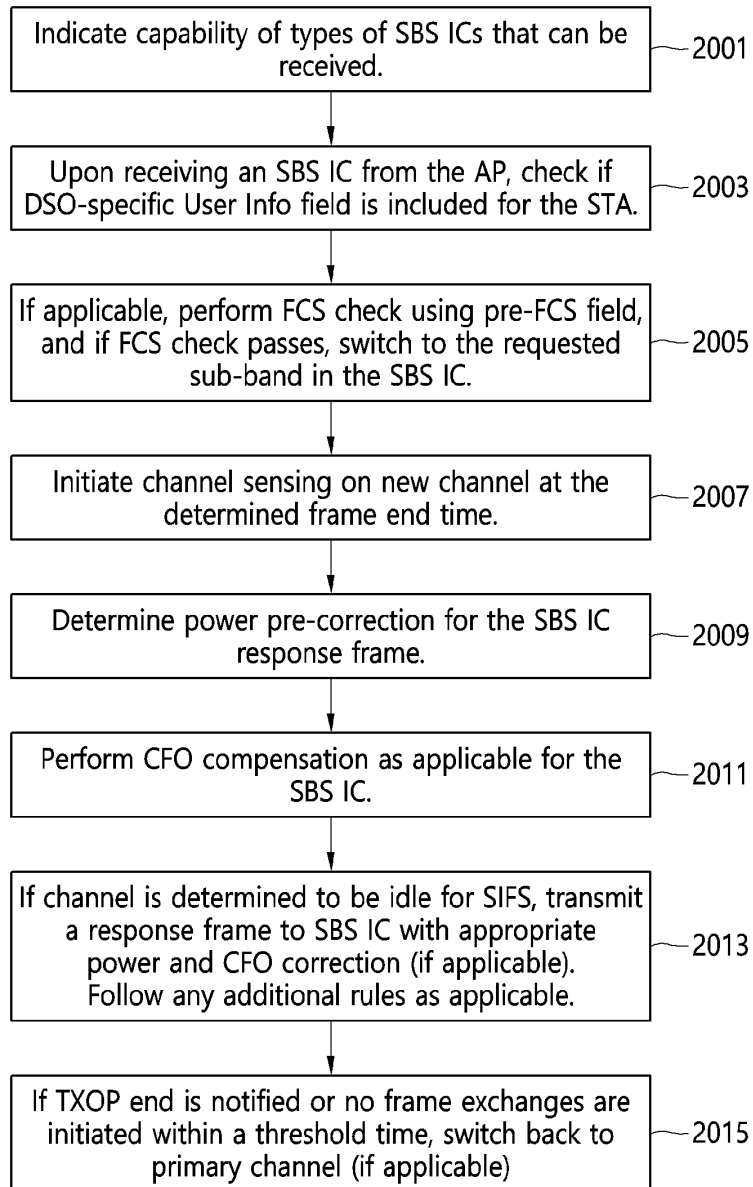
[Fig. 18]

Order	Information
1	Category
2	Protected UHR Action
3	Dialog Token
4	DSO Control
5	EML Control

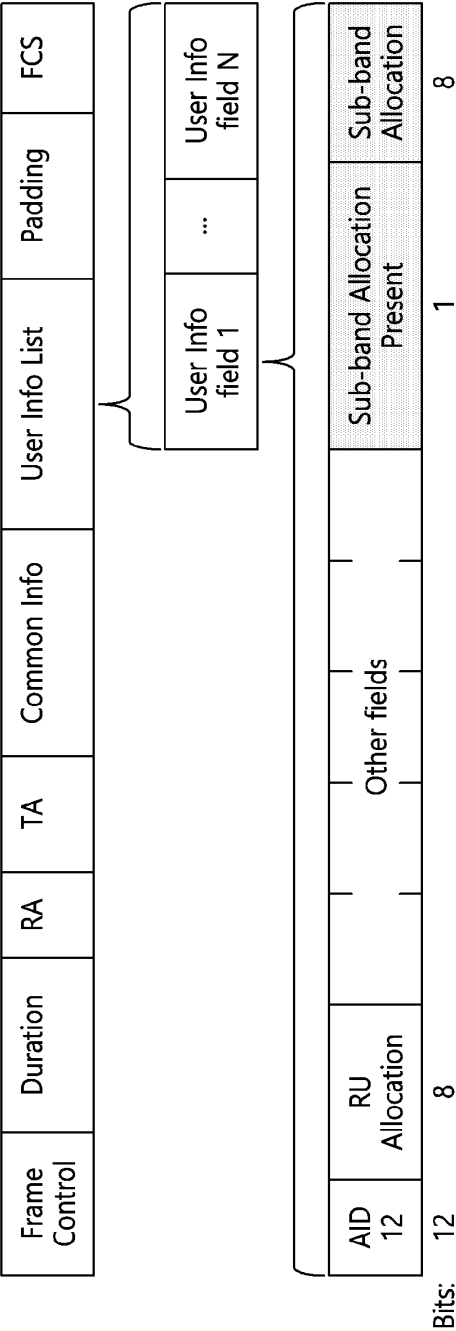
[Fig. 19]

1900

[Fig. 20]

2000

[Fig. 21]



[Fig. 22]

