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ANALOG SIGNAL AMPLIFIER AND AUDIO SIGNAL AMPLIFIER

ABSTRACT

An input limiting circuit 3 limits the maximum signal voltage value of input signal voltage supplied from a signal source 1 through an input terminal 2. A power amplifying circuit 5 converts the signal voltage passing the input limiting circuit 3 into a switching signal having the input signal included in a low frequency component through, for example a PWM modulation, and then, amplifies the output power of the modulated signal using a D class amplification. A signal demodulating circuit 6 selects the switching frequency received from the power amplifying circuit 5, and demodulates it into a signal similar to the signal generated by the original source. The demodulated signal from the signal demodulating circuit 6 is then supplied to a reproducing device 7.

Figure 1



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