ABSTRACT MEANS FOR REACTIVATING A SYSTEM OPERATING IN "SLEEP" MODE

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The present invention concerns a system 10 supplied by an electric power source, and able to operate in "sleep" mode, this system including a system clock 12, oscillation detection means 13 for monitoring the activity of the system clock, and means 16 for reactivating the system when the latter is in "sleep" mode. This system is characterised in that the reactivating means are internal to the system, and in that they are arranged to form, as soon as the system passes into "sleep" mode, an autonomous time base, to reactivate this system at the end of a predetermined time interval and, when the system is not in "sleep" mode, to fulfil a different function to that of time base and reactivation. An advantage of the reactivating means of such a system lies in the fact that they can operate only with a current of the order of 1 nA.

Figure 2