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THERMOELECTRIC COOLING APPARATUS WITH DYNAMIC SWITCHING TO ISOLATE HEAT TRANSPORT MECHANISMS

5 The present invention generally relates to cooling systems. More particularly, the invention is directed to systems attaining high relative efficiency thermoelectric cooling through the application of selectively switched electrical power and selectively switched thermal coupling concepts and configurations.

10 Sub-ambient cooling is conventionally accomplished through gas/liquid vapor compression based refrigeration cycles using Freon type refrigerants to implement the heat transfers. Such refrigeration systems are used extensively for cooling human residences, foods, and vehicles. Sub-ambient cooling is also often used with major electronic systems such as mainframe computers. Though vapor compression cooling can be very efficient, it does require significant moving hardware, including at a minimum, a compressor, a condenser, an evaporator, and related coolant transfer plumbing. As a result of the complexity and associated high cost, vapor compression cooling has not found material acceptance in small cooling applications, for example personal computers.

25 The fact that CMOS logic can operate materially faster as the temperature decreases has been well known for at least ten years. For example, if CMOS logic devices are operated at -50°C , the performance is improved by 50 percent over room ambient temperature operation. Liquid nitrogen operating temperatures, in the range of -196°C , have shown 200 percent performance improvements. Similar benefits have shown to accrue for integrated circuit wiring, where metal wiring resistances decrease by a factor of 2 for integrated circuits operated at -50°C in comparison to room ambient operation. This improvement rivals the recent technological breakthrough of using copper wiring in integrated circuits to reduce interconnect resistance and thereby effectively increase the operating frequencies attainable. Thus, sub-ambient operation of integrated circuit logic devices, such as field effect transistors, as well as the interconnect wiring can materially improve the integrated circuit performance, leaving the question of how to accomplish such cooling in the confines of an ever decreasing size and materially shrinking cost environment.

40 Thermoelectric cooling is one alternative that has found some usage given the compact size of the prevalently used Peltier devices. Peltier device thermoelectric cooling is also very reliable in that the cooling is totally solid state. The key negative aspect of thermoelectric cooling is the inefficiency, wherein a Peltier device cooling system efficiency is commonly only in the range of 20 percent for a relatively nominal

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temperature drop between the cold sink and the ambient. For example to cool at the rate of one watt at a sub-ambient temperature of 0° C the Peltier cooling system must be powered with 5 watts. As the amount of heat to be transferred increases, the total power to be dissipated into the ambient mandates large convection devices and high output power supply circuits. Therefore, Peltier device thermoelectric cooling has not been considered a broadly applicable technology for improving integrated circuit performance.

To understand how the present invention improves thermoelectric cooling efficiency it is necessary to understand why Peltier device thermoelectric cooling is inefficient. A Peltier device is fabricated from semiconductor material such as bismuth telluride or lead telluride. Though new materials are now being evaluated in various universities, they have yet to reach fruition. The commonly used Peltier materials exhibit very high electrical conductivity and relatively low thermal conductivity, in contrast to normal metals which have both high electrical and thermal conductivity. In operation the Peltier devices transport electrons from a cold sink, at temperature T_{cold} , to a hot sink, at temperature T_{hot} , in response to an electric field formed across the Peltier device. However, there are other mechanisms affecting Peltier device efficiency, which mechanisms degrade the net transport of the heat energy from the cold sink to the hot sink.

Fig. 1 schematically depicts a conventional Peltier type thermoelectric element (TE) 1 with DC power supply 2 created the electric field across TE 1 while at a load current 3. The desired heat transfer is from cold sink 4, at temperature T_{cold} , to hot sink 6, at temperature T_{hot} . As indicated in the equation of Fig. 1, the net heat energy transported is composed of three elements, the first representing the Peltier effect (thermoelectric) contribution, the second defining negative Joule heating effects, and the third defining negative conductivity effects. The thermoelectric component is a function of the Seebeck coefficient α , the temperature of operation (T_{cold}) and the current being applied. The Joule heating component reflects that roughly half the Joule heating goes to the cold sink and remainder to the hot sink. Lastly, the negative component attributable to thermal conduction represents the heat flow through the Peltier device, as defined by the thermal conductivity of the Peltier device, from the hot sink to the cold sink. See equation (1).

$$(1) \quad q = \alpha T_{\text{cold}} I - \frac{1}{2} I^2 R - K \Delta T$$

Since the thermoelectric component of the heat transport increases in direct proportion to the current, while the Joule heating increases in proportion to the square of the current, and the thermal conduction is in

direct proportion to the hot sink to cold sink temperature difference, the equation clearly reflects how quickly the Peltier device becomes inefficient.

Equation (2) defines a coefficient of performance for the Peltier device. The coefficient is the ratio of the net heat energy transported at low temperature to the power consumed in the Peltier device. For a typical bismuth telluride material Peltier device, the coefficient of performance is less than 0.3.

$$(2) \quad \eta = \frac{\alpha T_{\text{cold}} I - \frac{1}{2} I^2 R - K \Delta T}{I^2 R + \alpha I \Delta T}$$

Note that the numerator of equation (2) represents the net cooling capability of the Peltier device. The denominator of equation (2) represents the total energy provided by external power source 2. The individual elements of the numerator were described earlier. The first term in the denominator is the total Joule heating, while the second term is the heat energy transport work done by the Peltier device in moving energy from the T_{cold} sink to the T_{hot} sink. Based upon this relationship, the maximum coefficient of performance possible in the configuration of Fig. 1 is given by equation (3).

$$(3) \quad \eta_{\text{max}} = \frac{T_{\text{cold}}}{\Delta T} \cdot \gamma = \frac{T_{\text{hot}}}{T_{\text{cold}}} \cdot \frac{\gamma}{\gamma + 1}$$

The parameter γ can be expressed in terms of the Seebeck coefficient α , electrical conductivity σ and thermal conductivity λ as set forth in equation (4).

$$(4) \quad \gamma = \frac{\gamma^2}{1 + \frac{\gamma^2}{RK}} = \frac{T_{\text{hot}} + T_{\text{cold}}}{2} = 1 + \frac{\alpha^2 \sigma}{\lambda} T$$

$$= 1 + ZT$$

Note that the first factor in equation (3) is the Carnot efficiency, which is the maximum efficiency possible for any heat pump operating between two temperature sinks T_{cold} and T_{hot} . The second factor represents the non-ideal thermoelectric cooling, which can also be characterized by a figure of merit ZT .

Note that $\eta_{\text{max}} \rightarrow (T_{\text{cold}}/\Delta T)$ as $\gamma \rightarrow \infty$.

To date it has been very difficult to develop a thermoelectric material which yields high values of ZT . The prevalent materials for thermoelectric coolers have been bismuth telluride (Bi_2Te_3) and lead tellurium (PbTe). These materials have ZT values of approximately 0.3 at room temperature. Recent work at universities has shown that ZT values approaching 1 may be possible in lead tellurium quantum wells and multilattices. However, even with these materials, the thermoelectric cooling is not competitive with mechanical vapor compression cooling systems.

Another constraint of Peltier device cooling is the limited temperature excursion below ambient attainable. That limitation arises from the fact that temperature span is constrained by efficiency, a parameter which degrades quickly as the temperature differential increases. The maximum temperature differential possible T_{max} is given by equation (5).

$$(5) \quad \Delta T_{\text{max}} = \frac{1}{2} ZT^2_{\text{cold}}$$

For bismuth telluride having a ZT of approximately 0.3, T_{max} is 45°K at 300°K .

Thus, there are a number of very fundamental constraints on efficiency and differential temperature that limit the use of conventional thermoelectric elements for sub-ambient cooling applications.

The present invention overcomes the fundamental constraints of conventional thermoelectric element cooling through the application of dynamic modulation to electrical power and thermally conductive paths connecting the thermoelectric element to the power supply and cold sink, respectively.

In one form, the invention relates to a thermoelectric cooling apparatus as claimed in claim 1.

In a still further form, the invention relates to a method of operating a thermoelectric cooling apparatus as claimed in claim 16.

In a particularized form of the invention, complementing impurity type thermoelectric elements are connected in electrical series and powered by pulses of switched voltage. The thermoelectric elements are thermally coupled to individual electrically isolated hot sinks at one side, and are thermally coupled from a common connection of their respective cold sides to a thermal switch which selectively establishes a thermal path to the cold sink. Selective but synchronized operation of

the electrical switch and the thermal switch provides thermal energy transport, from the cold sink, through the thermal switch, and through the pair of thermoelectric elements to respective hot sinks, at an efficiency exceeding the static mode operation of such thermoelectric elements. The use of transient principles allows the relative isolation of the thermoelectric heat transport mechanism from the thermal conduction and Joule heating mechanisms. The efficient of performance is expected to approach Carnot efficiency.

Embodiments of the invention will now be described with reference to the accompanying drawings, in which:

Fig. 1 schematically depicts a conventional statically operable Peltier device cooling system.

Fig. 2 schematically depicts a single switch, single thermoelectric element, embodiment of the invention.

Fig. 3 schematically depicts a single thermal switch double thermoelectric element embodiment of the invention.

Fig. 4 schematically depicts relative time plots of the electrical power and the thermal energy transports per the embodiment in Fig. 3.

Fig. 5 schematically depicts a closed loop implementation of the single thermal switch configuration in Fig. 3.

Fig. 6 schematically depicts a microelectromechanical systems (MEMS) device.

Fig. 7 depicts by schematic cross-section an array of MEMS devices and Peltier type thermoelectric elements.

Fig. 8 schematically depicts a thermoelectric cooler as may be used to sub-ambient cool integrated circuits and electronic modules.

Fig. 9 schematically depicts the extended use of the invention to a food refrigeration system.

Fig. 10 schematically depicts potential applications and benefits of the invention as applied to various human residences and transportation media.

Fig. 11 schematically depicts the application of a small thermoelectric cooler to locally cool a selected part of an integrated circuit chip.

5 The conceptual groundwork for the present invention involves a separate of the dependency between thermal conductivity and electrical conductivity, a dependency which has heretofore limited the temperature differential and efficiency of conventional thermoelectric element heat transfers. Mathematically, the objective is to effectively isolate the
10 elements contributing to net heat transfer relationship specified in Fig. 1 through the use of thermoelectrical switches to dynamically maximize thermoelectric heat transfer while minimizing Joule heating and conduction heat transfer. Thermoelectric element transient effects are used to
15 increase efficiency by synchronizing the pulsed voltage applied across the thermoelectric element and the switched thermal conductivity coupling between the cold side of the thermal electric element and the cold sink. In a preferred implementation, the switching of thermal conductivity is accomplished using microelectromechanical systems (MEMS) thermal switches, wherein arrays of multiple miniature thermoelectric elements and related
20 MEMS thermal conductivity switches are used to increase heat transfer capacity.

Fig. 2 schematically depicts a minimum element configuration of the invention. Thermoelectric element 1 is continuously coupled to hot sink 6
25 through a thermal path having a heat transfer q . The opposite end of thermoelectric element 1, from the perspective of voltage application and responsive heat transfers, is thermally coupled through thermal switch 7 to cold sink 4. As embodied in Fig. 2, switch 7 also conducts electrical
30 current, allowing the application of voltage 2 across thermoelectric element 1 when switch 7 is closed. At the start of a cycle thermoelectric element 1 is at T_{hot} by virtue of the thermal coupling to hot sink 6. Upon pulsed closure of switch 7, thermoelectric element 1 quickly establishes a relative temperature differential between hot end 8 and cold
35 end 9, the temperature differential allowing heat transfer from cold sink 4 through thermal switch 7. With time, however, Joule heating effects within thermoelectric element 1 elevate the average temperature of thermoelectric element 1 so that the net heat transfer through
40 thermoelectric element 1 begins to decrease. At this point, switch 7 is opened, disconnecting both the electrical power and the thermal coupling. The residual thermal energy in thermoelectric element 1 at the time of switch disablement elevates the temperature sufficiently to provide an exponentially decaying heat transfer between thermoelectric element 1 and
45 hot sink 6. When the temperature of thermoelectric element 1 has decayed to that approaching hot sink 6, the cycle is repeated. The transient character of the operation is tied to the fact that thermoelectric heat

transfer occurs immediately upon the receipt of a relative voltage whereas Joule heating and subsequent thermoelectric element conduction loss are delayed effects. Thus the invention relies on the different time scales and time constants of electrical and thermal conduction.

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The basis concept for improving efficiency, as described with reference to Fig. 2, exhibits some less pronounced but still significant inefficiency contributors. The most pronounced ones are the Joule heating in switch 7 when the switch is closed, the thermal conductance losses through switch 7 when the switch is in an open state, and the heat loss due to heat capacity of thermoelectric element 1.

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A detailed analysis of the transients provides that the heat loss due to the heat capacity of the thermoelectric element is approximately equal to the Fourier conductance term. Hence the expression for the coefficient of performance earlier set forth as equation 2, is more fully described by equation (6).

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$$(6) \quad \eta_1 \approx \frac{\alpha IT_{\text{cold}} - I^2 R_s - K_s \Delta T - K \Delta T}{\alpha I \Delta T + I^2 (R + R_s)}$$

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In equation (6), the terms R_s and K_s are the ON electrical resistance and the OFF thermal conductance of the switch. The ON electrical resistance R_s of the switch can commonly be made small at the expense of increasing the OFF thermal conductance K_s .

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One approach to improving the coefficient of performance is shown by the embodiment in Fig. 3, where the electrical switch is placed at the hot sink through the judicious placement and connection of n-type and p-type thermoelectric elements. Thereby, the heating associated with the electrical switch is eliminated as a contributor to the coefficient of performance. Mathematically this allows the coefficient of performance to be rewritten as set forth in equation (7).

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$$(7) \quad \eta_2 \approx \frac{\alpha IT_{\text{cold}} - K \Delta T}{I^2 R + \alpha I \Delta T}$$

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The effect is that the maximum coefficient of performance is slightly higher, typically 20%, than that associated with the single switch implementation in Fig. 2, as mathematically described by equation (3). See equation (8).

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$$(8) \quad \eta_{2\text{max}} = \frac{T_{\text{cold}}}{\Delta T} \frac{\gamma - 1}{\gamma + 1}$$

Although the enhancement in coefficient of performance is not dramatic, the difference is particularly significant for spot cooling applications. In this regard, note that the net cooling power of the thermoelectric cooler as represented by the numerator of equation (9) indicates that the maximum temperature is effectively unbounded.

$$(9) \quad \Delta T = \frac{\alpha T_{\text{cold}}}{K} I$$

Therefore, the maximum temperature differential can be increased significantly by increasing the current and in that context making thermoelectric cooling practical for small sensors and specialized circuits on silicon die. Such localized or spot cooling applications are particularly useful in voltage controlled oscillators, phase detectors, mixers, low noise amplifiers, lasers, photodiodes, and various material type optoelectric circuits. In theory at least, spot cryogenic temperature cooling may be possible in limited applications. The use of multiple impurity type thermoelectric elements and a separate electrical switch provides significant potential as to efficiency and temperature range.

The embodiment in Fig. 3 introduces a number of interrelated refinements. First, multiple thermoelectric elements are used. Secondly, the timing of the electric power as applied to the thermoelectric elements is separated from the timing of the thermal switch coupling the cold end of the thermoelectric elements to the cold sink. Lastly, the switch connecting the cold end of the thermoelectric elements to the cold sink is solely a thermal switch, eliminating any electrical conduction requirements and the Joule losses associate with a current flow through the switch. The embodiment in Fig. 3 utilizes two thermoelectric elements, n impurity type thermoelectric element 11 and p impurity type thermoelectric element 12. This configuration allows the shared use of single voltage source 13, as enabled through electrical switch 14, while having cold ends 16 and 17 of respective thermoelectric elements 11 and 12 thermally coupled through thermal switch 18 to cold sink 4. Hot ends 19 and 21 of respective thermoelectric elements 11 and 12 are thermally and electrically connected to respective hot sinks 22 and 23, which hot sinks are electrically separate to effectuate the use of shared voltage source 13.

Though the operation of the two thermoelectric element embodiment in Fig. 3 is analogous to that of the single thermoelectric element embodiment in Fig. 2, the isolation of the thermal switch and the electrical switch provide greater flexibility in defining the respective duty cycles and switching synchronizations. Though electrical switch 14

and thermal switch 18 will both operate with very short duty cycles, and exhibit relatively synchronous operation one to another, the timing of the closure and opening cycles are likely to differ depending on the transient characteristics of the thermoelectric elements and conductive path couplings to the hot and cold sinks. For example, improved thermal coupling would suggest that electrical switch 14 close first, thermal switch 18 close momentarily thereafter, electrical switch 14 open somewhat later and thermal switch 18 open somewhat after the opening of electrical switch 14. The underlying objective of the switching operations being to maximize the efficiency of the heat transfer from cold sink 4 to hot sinks 23 and 24.

Fig. 4 schematically depicts by plots illustrative voltage and thermal energy transport waveforms associated with the operation of the embodiment in Fig. 3. The first plot shows the pulsed nature of the voltage applied across the thermoelectric elements. The second plot illustrates the thermal transient and associated decay of the thermal energy dissipated into the hot sink. The last plot illustrates the thermal energy absorption from the cold sink through the thermal switch. The plots in Fig. 4 are schematic in that they are intended to illustrate general concepts rather than to portray particularized time related magnitudes.

Fig. 5 schematically illustrates an extension of the preferred embodiment in Fig. 3, wherein the enablements of electrical switch 14 and thermal switch 18 are performed in response to inputs from temperature sensor 24. Temperature sensor 24 provides an input to synchronizing control 26 to operate switches 14 and 18 in response to the actual temperatures at the hot, cold, or both ends of the thermoelectric elements. Though the synchronism and duty cycle characteristics of switches 14 and 18 remain relatively similar to those in for the embodiment in Fig. 3, the use of sensed temperature optimizes efficiency by using actual rather than estimated thermal characteristics to operate switches 14 and 18. The implementation in Fig. 5 allows adjustment in the switch timing to compensate for such effects as greater hot sink temperatures or lower cold sink temperatures within the context of the same cooling apparatus.

Fig. 6 schematically illustrates the structure of a representative microelectromechanical systems (MEMS) thermal switch of the type particularly suited to the present invention. Since MEMS technology is still in its infancy, the switch depicted in Fig. 6 merely illustrates one of many potential thermal switch configurations suitable to provide a selective thermal coupling between the thermoelectric element and the cold sink. The thermal switch shown in Fig. 6 is fabricated using conventional

integrated circuit techniques so as to form on a surface of silicon chip 27 an array of nickel magnets 28 amenable to a slight displacement by movement at thin flexible membranes 29. Introduction of an electrical current into spiral coil 29 produces a force adequate to translate the magnetic array in a direction perpendicular to the plane of the silicon chip. The MEMS switch in Fig. 6 should have a relatively low thermal conductivity when opened yet a relatively high thermal conductivity when closed by actuation. If the MEMS device in Fig. 6 is to accomplish both electrical and thermal switching, refinements are likely to be necessary to lower the "on" resistance of the switch.

Fig. 7 illustrates the use of an array of MEMS devices to selectively establish thermal connections between the Peltier type thermoelectric devices and a cold sink. Peltier devices 32 and 33 are electrically interconnected by copper conductor 34 to replicate the functions associated with the depiction in Fig. 3. The spacing between copper layer 34 and magnetic arrays 28 of MEMS switches 36 and 37 is expected to be in the nominal range of half a micron. This dimension is expected to allow a nominal size electrical coil 31 (Fig. 6) to initiate actuation of the switch structures. Since switch cycling is expected to occur in the order of seconds, the reliability associated with kilohertz frequency switching of MEMS devices should not be a problem.

The MEMS type thermal switch described with reference to the illustrations in Figs. 6 and 7 is merely one of many potential thermal switch configurations. For example, it is fully contemplated that electrostatic forces generated in capacitive switch structures could be used to accomplish similar objectives. The underlying goal for all the switches is to maximize the thermal conductivity extremes for switch positions, such that when the switch is closed the thermal path between the thermoelectric element and the cold sink has a maximum thermal conductance while for the open switch the thermal conductance is the minimum attainable.

The depiction in Fig. 7 portrays that the thermoelectric cooling system of the present invention is preferably composed of plural thermoelectric elements and MEMS switches configured in arrays. The multiplicity of thermoelectric elements and switches ensures that the transient characteristics which underlie the present invention can be achieved within the dimensions of the thermoelectric element and switch materials. Stated otherwise, it is expected that the isolation of the thermoelectric heat transfer from the Joule heating and conduction components is most effectively accomplished with relatively small thermal capacity thermoelectric elements, commonly Peltier devices, and correspondingly small MEMS type thermal switches.

Fig. 8 schematically depicts one application for the thermoelectric cooler of the present invention. In this case, the cooler is situated between a heat sink dissipating power into an air ambient and a cold sink having electronic modules and integrated circuits attached thereto.

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Fig. 9 schematically illustrates the use of the thermoelectric cooler in an extended array form to efficiently and cleanly operate a food refrigerator. The high efficiency and lack of major moving parts characterizing the present invention facilitates the migration of thermoelectric cooling from highly selective and limited applications, such as small portable coolers, to major appliances in substantially every home.

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Still further applications are schematically depicted in Fig. 10, as the concepts underlying the present invention are further refined and extended in size to encompass major heat transfer applications encompassing residential and office cooling, food transportation systems, and personal vehicle cooling.

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Fig. 11 schematically illustrates an application somewhat at the other end of the spectrum, where micro size thermoelectric coolers are selectively bonded to parts of an integrated circuit chip for purposes of selectively cooling such selected regions to control integrated circuit parameters.

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The present invention has very broad applicability in part because it is not constrained to specific thermoelectric materials or electronic configurations. The invention utilizes the thermal dynamics of pulse operated thermoelectric elements in combination with miniature thermal switches to isolate heat transfer characteristics and attain higher cooling efficiency.

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It will be seen that while the embodiments of the invention describe the thermoelectric element connected continually to the hot sink, it is within the scope of the invention for the thermoelectric element to be connected to the hot sink with a switch of similar structure to the thermal switch 18.

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CLAIMS

1. A thermoelectric cooling apparatus, comprising:

a first thermal sink (4) of a first nominal temperature;

a second thermal sink (22,23) of a second nominal temperature, the second temperature being relatively greater than the first temperature;

a thermoelectric element (11,12);

second means for connecting the thermoelectric element to the second thermal sink; and

means (V) for enabling the thermoelectric element; characterised by

first means (26) for selectively switching a thermal coupling (18) of the thermoelectric element to the first thermal sink.

2. The apparatus as recited in claim 1 wherein said second means comprises a continuous thermal coupling between said thermoelectric element and the second thermal sink.

3. The apparatus as recited in claim 1 wherein said enabling means comprising means (14) for selectively switching an electrical voltage across the thermoelectric element.

4. The apparatus recited in claim 3, wherein the second thermal sink is comprised of first (22) and second (23) electrically separate sections.

5. The apparatus recited in claim 4, wherein the first and second electrically separate sections are coupled to a power supply (V) through the means (14) for selectively switching an electrical voltage.

6. The apparatus recited in claims 3 to 5, wherein the duty cycle of the means (14) for selectively switching an electrical voltage is similar to the duty cycle of the means (26) for selectively switching a thermal coupling.

7. The apparatus recited in claims 1 to 6, wherein the means for selectively switching a thermal coupling is a microelectromechanical system (MEMS) device.

8. The apparatus as claimed in claim 2 further comprising:

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a third thermal sink (23) of the second nominal temperature, the third thermal sink being electrically separate from the second thermal (22) sink;

5 a second thermoelectric element (12) thermally coupled to the third thermal sink;

10 wherein said means (26) for selectively switching a thermal coupling (18) comprises means for selectively switching a thermal coupling of the first mentioned and second thermoelectric elements to the first thermal sink (4); and

said enabling means comprises means (14) for selectively switching an electrical voltage (V) across the first mentioned and second thermoelectric elements.

20 9. The apparatus recited in claims 3 to 6 or 8, wherein the means for selectively switching a thermal coupling and the means for selectively switching an electrical voltage are adapted to operate in functional synchronism.

10. The apparatus recited in claim 8, wherein the first and second thermoelectric elements are Peltier devices.

25 11. The apparatus recited in claim 10, wherein the first and second thermoelectric elements are of opposite impurity type.

30 12. The apparatus recited in claim 2 wherein the apparatus is operable in an ambient, said first thermal sink means is adapted to absorb thermal energy at a temperature below the ambient; said second thermal sink means is adapted to dissipate thermal energy at a temperature above the ambient; said thermoelectric element is adapted to transport thermal energy therebetween; said means for selectively switching is adapted to switch the thermal conductance of the coupling between the first
35 thermoelectric element and the second thermal sink; and said means for enabling is adapted to enable the thermoelectric element in relative functional synchronism with the means for selectively switching the thermal conductance.

40 13. The apparatus recited in claim 12, wherein the duty cycle of the means for enabling is small relative to the duty cycle of the means for selectively switching.

45 14. The apparatus recited in claim 4, further comprising a second thermoelectric element, the first thermoelectric element being coupled to the first section of the second thermal sink means and the second

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thermoelectric element being coupled to the second section of the second thermoelectric element, and the second thermoelectric element also coupled to the means for selectively switching the thermal coupling.

5 15. The apparatus recited in claim 14, wherein the means for enabling is adapted to selectively switch a power supply connected across the first and second sections of the first thermal sink.

10 16. The apparatus recited in claim 12, wherein the dissipating of the thermal energy is into the ambient, and the absorbing of thermal energy is from one of a group of a food refrigeration system or a vehicle occupant cooling system or an electronic integrated circuit device.

17. A method of operating a thermoelectric cooling apparatus having a first thermal sink operable at a first nominal temperature, a second thermal sink operable at a second nominal temperature relatively greater than the first nominal temperature, and a thermoelectric element coupled to the second thermal sink, comprising the steps of:

20 transmitting thermal energy from the thermoelectric element to the second thermal sink through a coupling; and

enabling the thermoelectric element; characterised by

25 selectively switching the transmission of thermal energy between the thermoelectric element and the first thermal sink.