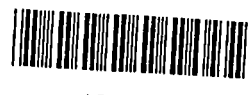


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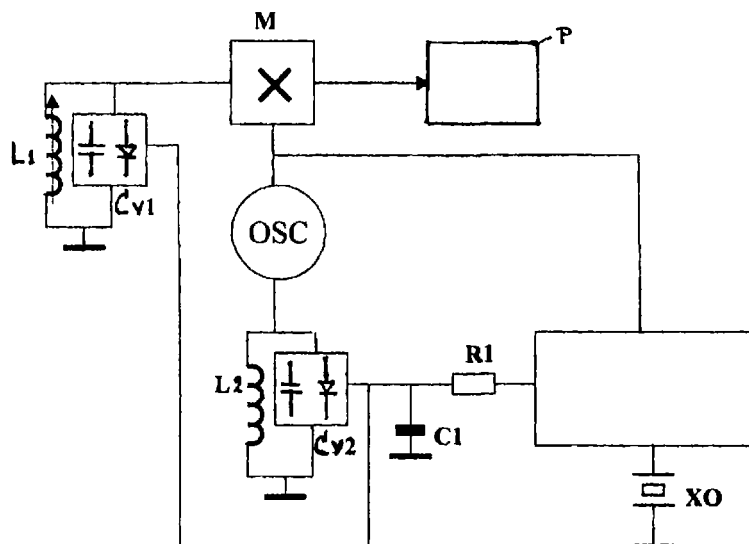
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54) Title: RECEIVER COMPRISING A DIGITALLY CONTROLLED CAPACITOR BANK



(57) Abstract: Receiver comprising an RF input filter including a digitally controlled capacitor bank with n capacitors being controlled by a tuning control signal for varying the tuning frequency of the RF input filter within a tuning range. For an improvement of the receiver in price/performance ratio the n capacitors of the digitally controlled capacitor bank are monolithically integrated, whereas the bandwidth of the tunable RF input filter is being adjusted to the maximum relative spread of said capacitors. A continuous tuning control signal is being supplied through an analogue to digital (AD) converter to said capacitor bank and to a first input of a differential stage, an output of the AD converter being coupled through a digital to analogue (DA) converter to a second input of a differential stage, an output of the differential stage being coupled to a control terminal of a variable capacitance diode being arranged in parallel to the digitally controlled capacitor bank.

WO 02/09280 A1

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