

FIG. 1A

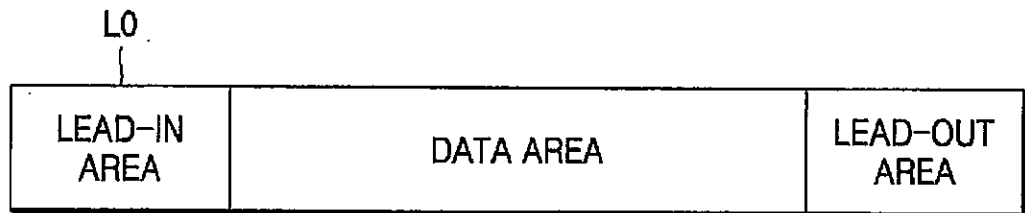


FIG. 1B

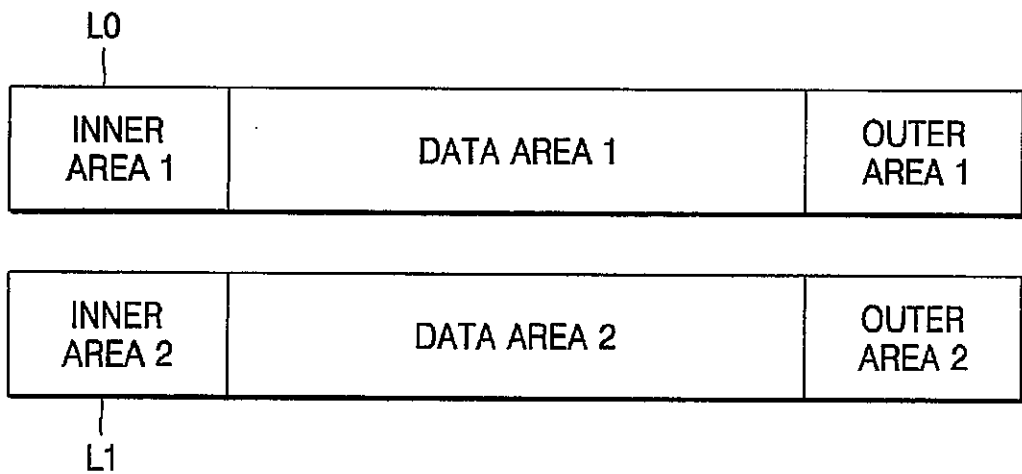


FIG. 2

LEAD-IN AREA	...
	DMA2
	Recording Condition Test Area
	AIA
	TDMA1
	DMA1
	...
DATA AREA	Spare Area1
	User Data Area
	TDMA2
	Spare Area2
LEAD-OUT AREA	...
	DMA3
	...
	DMA4
	...

FIG. 3A

INNER AREA1	...
	DMA2
	Recording Condition Test Area
	A/A
	TDMA1
	DMA1
DATA AREA1	...
	Spare Area1
	User Data Area
	TDMA3
	Spare Area2
	...
OUTER AREA1	DMA3
	...
	DMA4
	...

FIG. 3B

INNER AREA2	...
	Recording Condition Test Area
	...
	DMA8
	TDMA2
	DMA7
DATA AREA2	...
	Spare Area4
	TDMA5
	User Data Area
	TDMA4
	Spare Area3
OUTER AREA2	...
	DMA6
	...
	DMA5
	...

FIG. 4A

INNER AREA1	...
	DMA2
	Recording Condition Test Area
	A/A
	TDMA1
	DMA1
DATA AREA1	...
	Spare Area1
	User Data Area
	Spare Area2
OUTER AREA1	...
	DMA3
	...
	DMA4
	...

FIG. 4B

INNER AREA2	...
	Recording Condition Test Area
	...
	DMA8
	TDMA2
	DMA7
DATA AREA2	...
	Spare Area4
	TDMA5
	User Data Area
OUTER AREA2	Spare Area3
	...
	DMA6
	...
	DMA5

FIG. 5

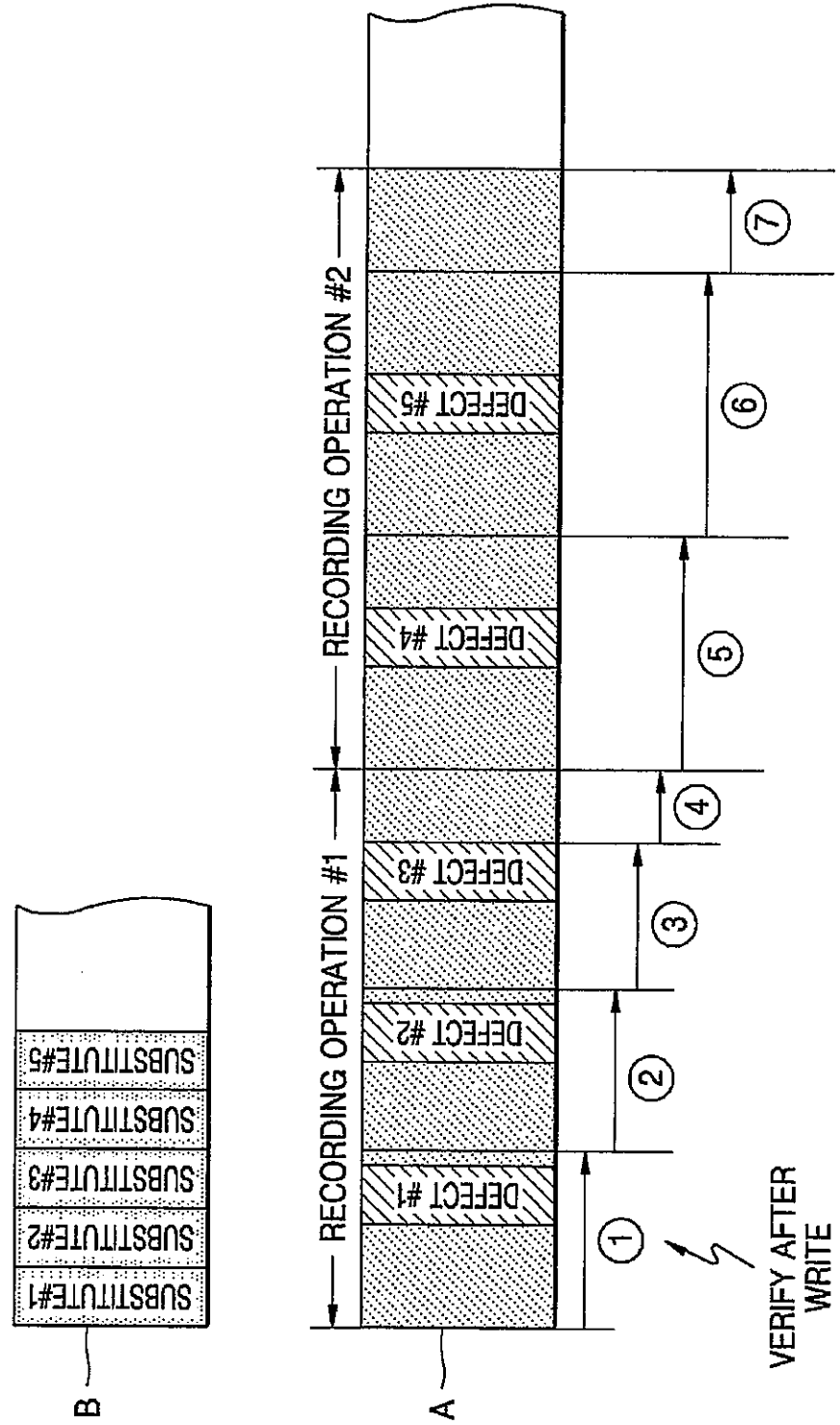


FIG. 6

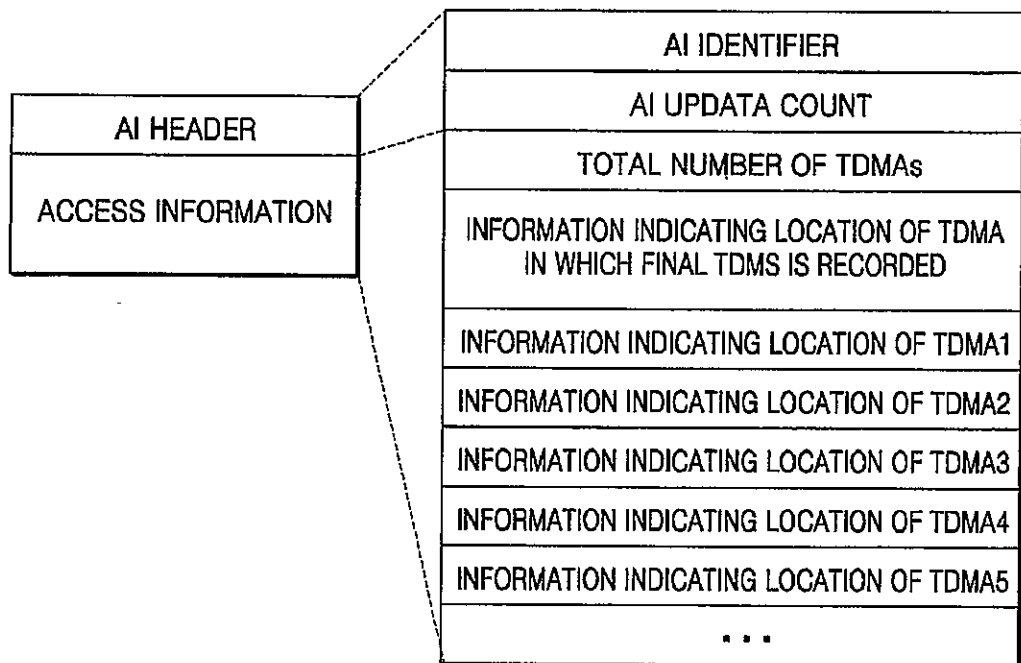


FIG. 7

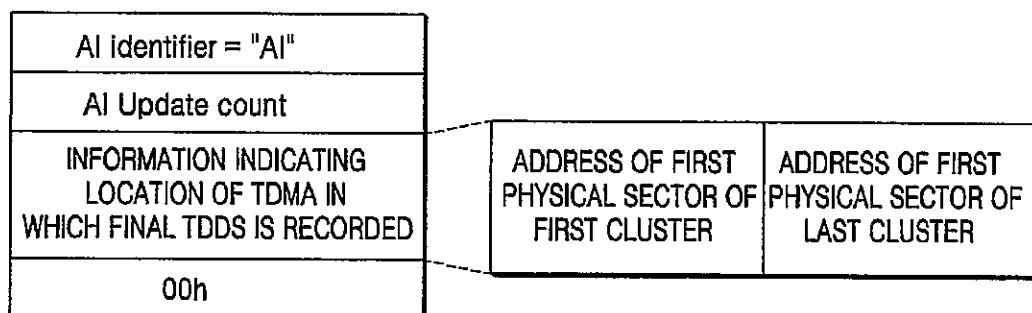


FIG. 8

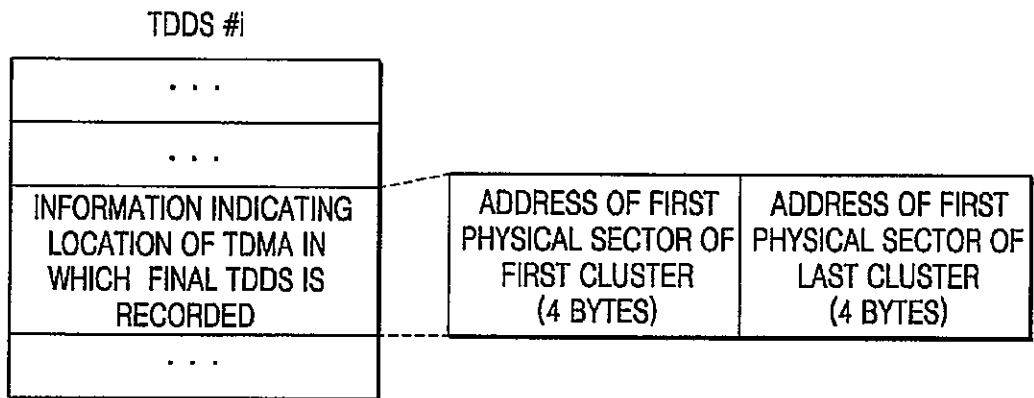


FIG. 9

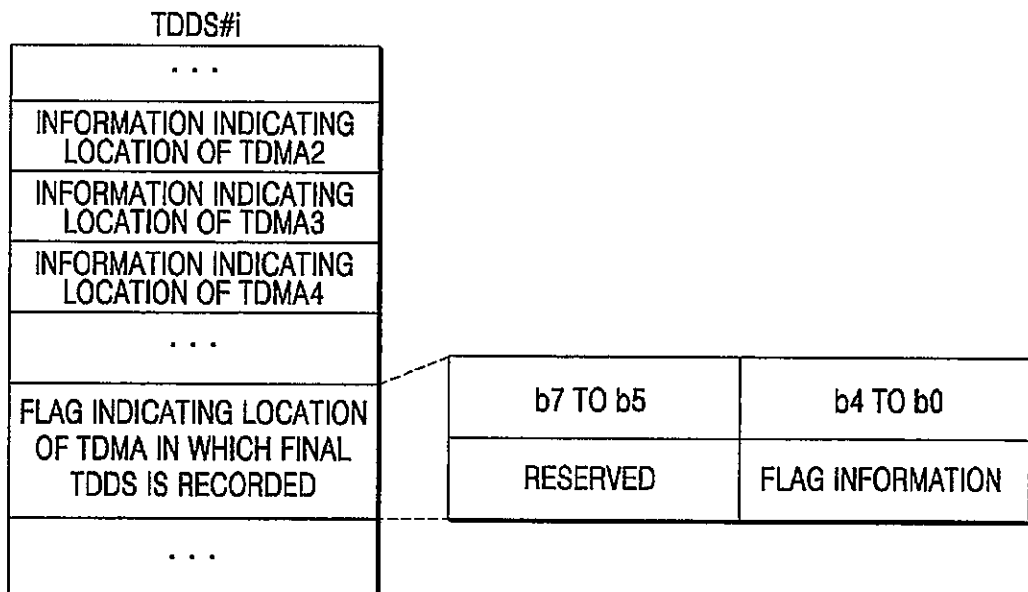


FIG. 10

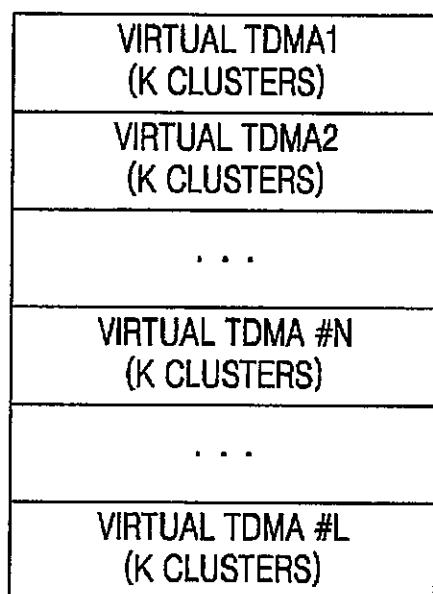


FIG. 11

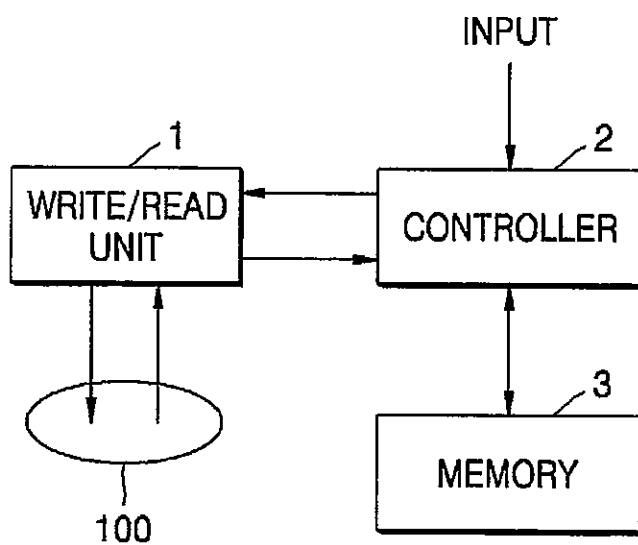


FIG. 12

