

FIG. 1

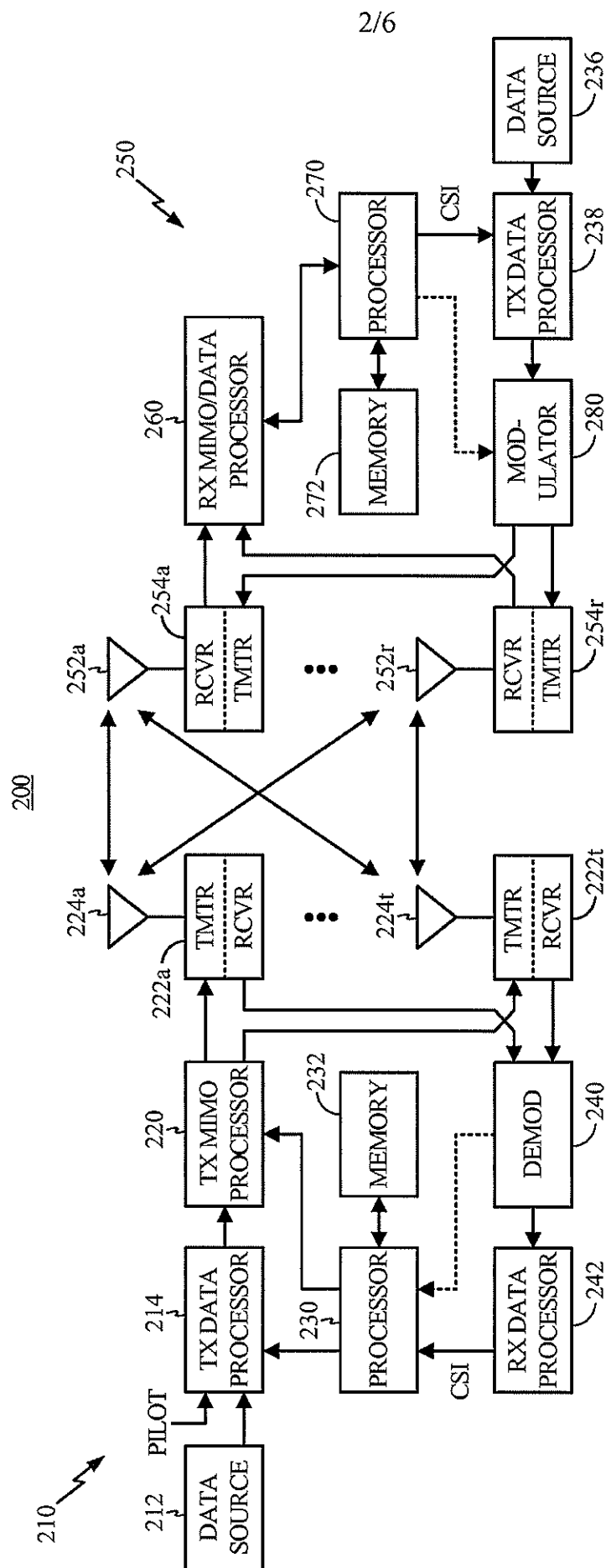


FIG. 2

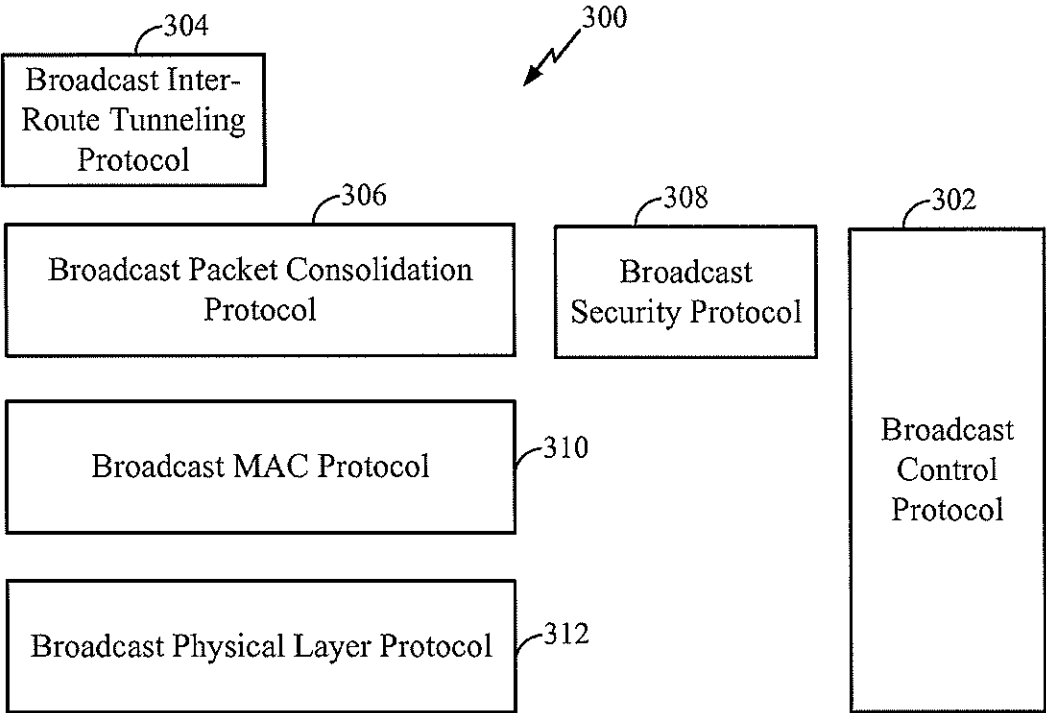
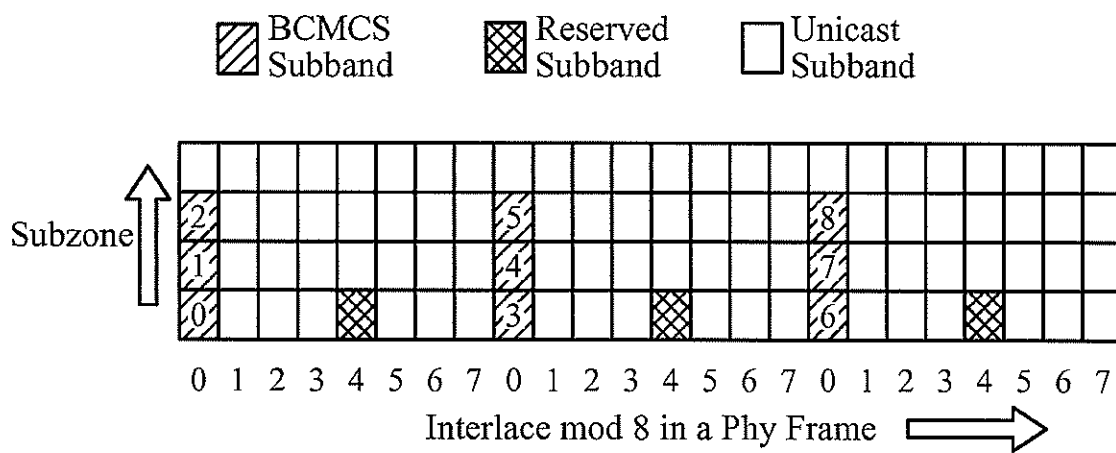


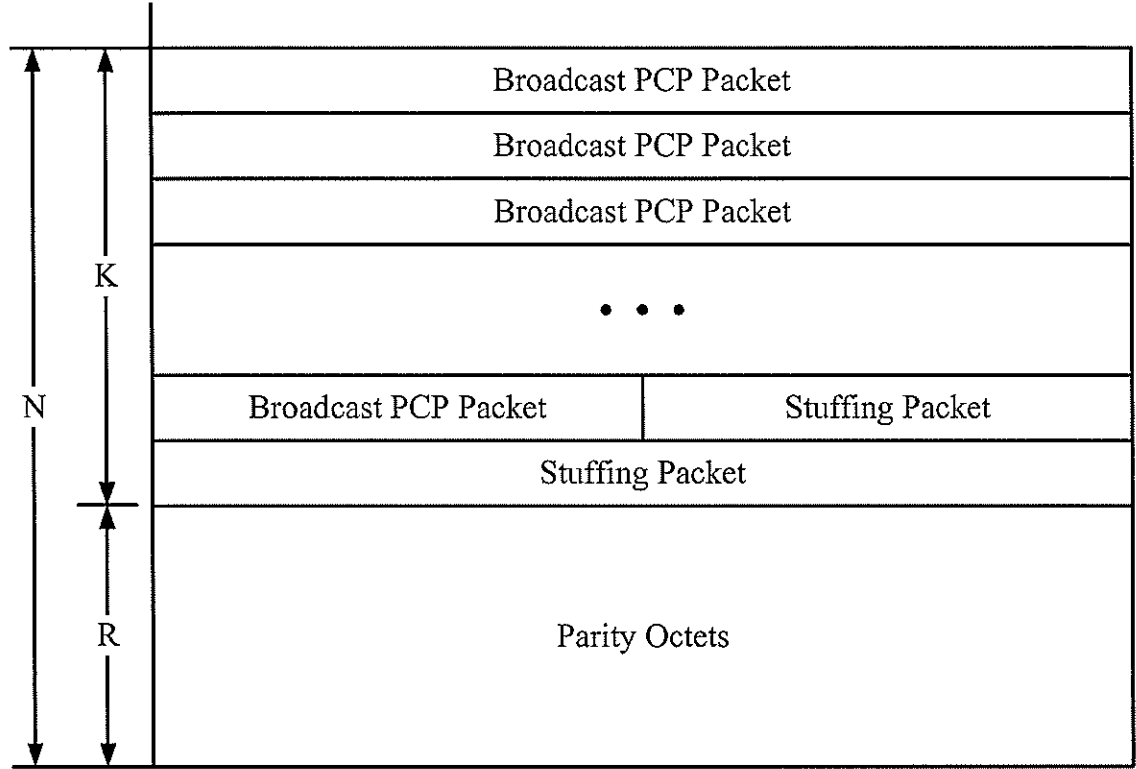
FIG. 3

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Indexing BCMCS Subbands

FIG. 4



Error Control Block Structure

FIG. 5

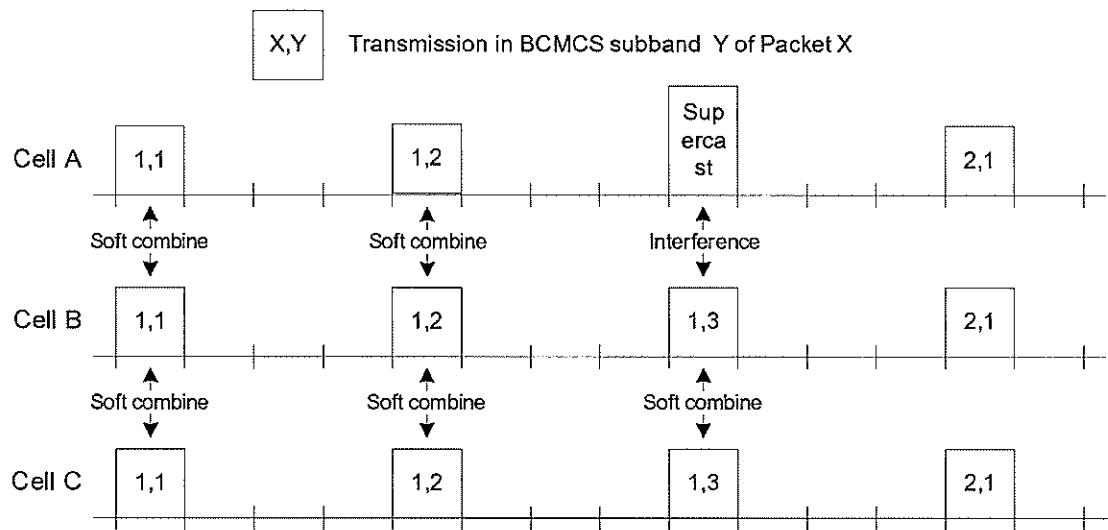


FIG. 6