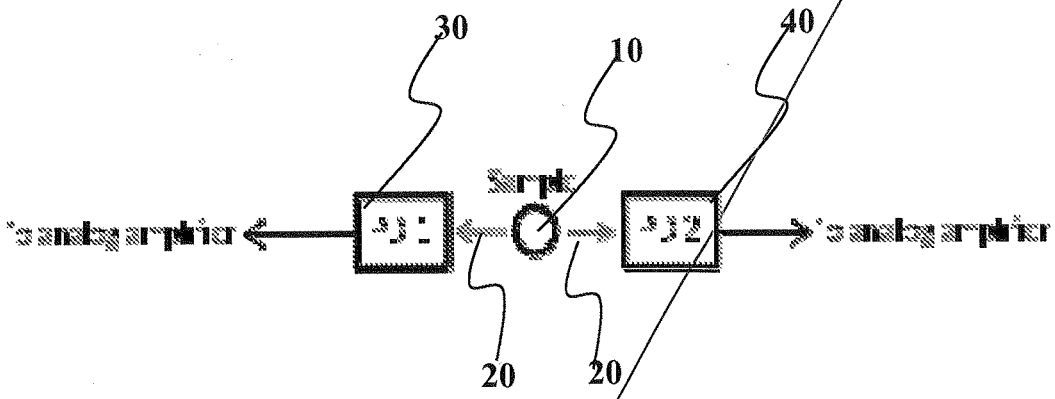


FIG. 1

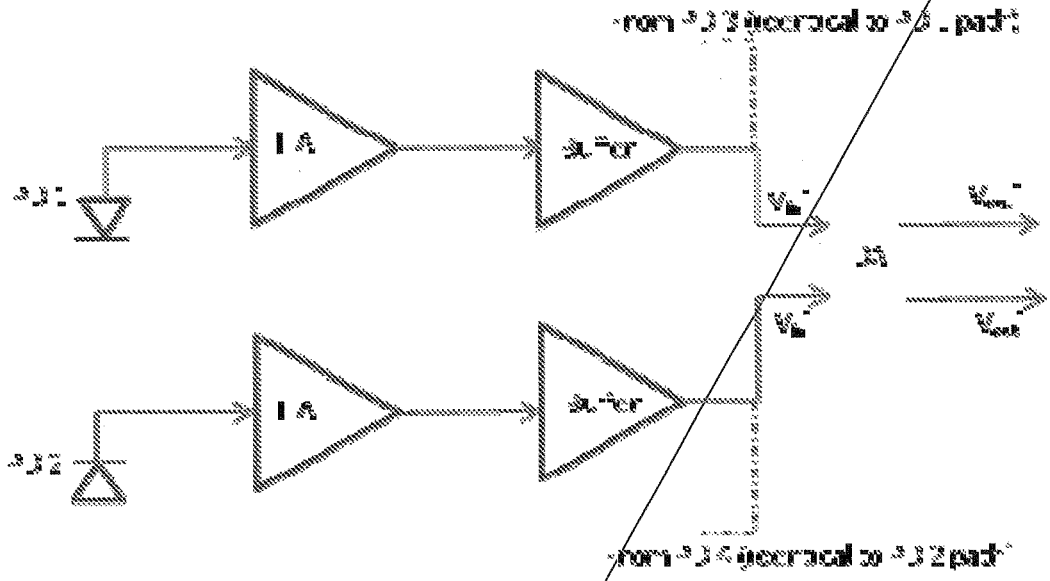


FIG. 2

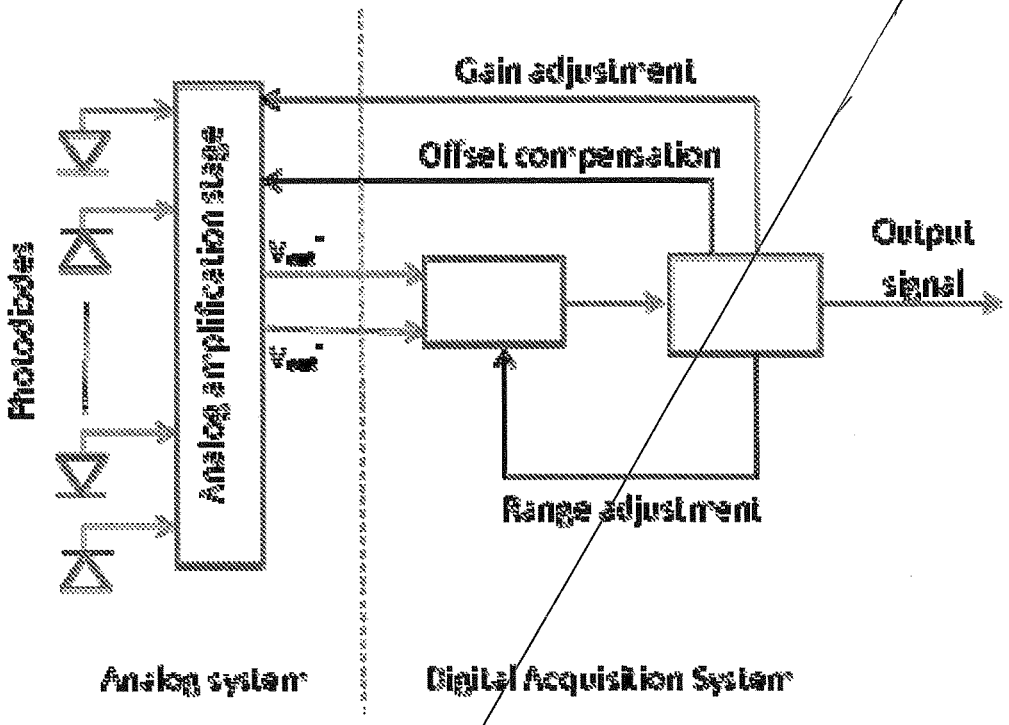

Gain adjustment
Offset compensation
Photodiodes
Analog amplification stage
Output signal
Range adjustment
Analog system
Digital Acquisition System

FIG. 3

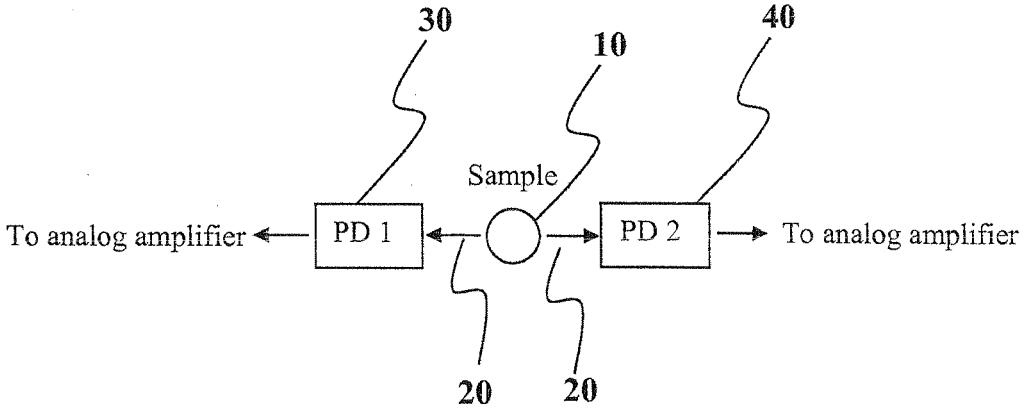

30
10
40
Sample
To analog amplifier
PD 1
PD 2
To analog amplifier
20
20

FIG. 1

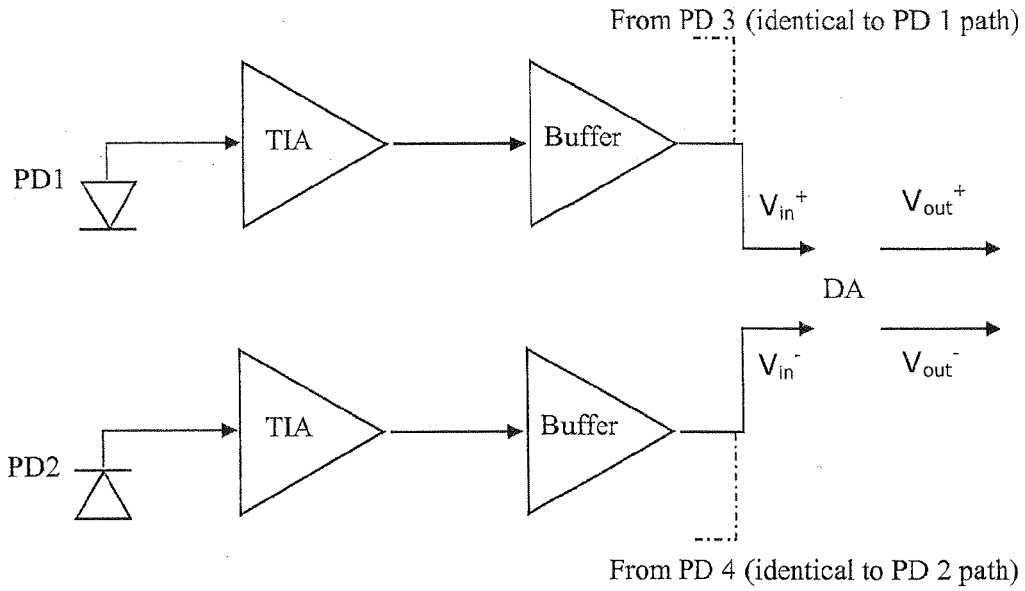

From PD 3 (identical to PD 1 path)
PD1
TIA
Buffer
V_{in}^+
V_{out}^+
DA
V_{in}^-
V_{out}^-
PD2
TIA
Buffer
From PD 4 (identical to PD 2 path)

FIG. 2

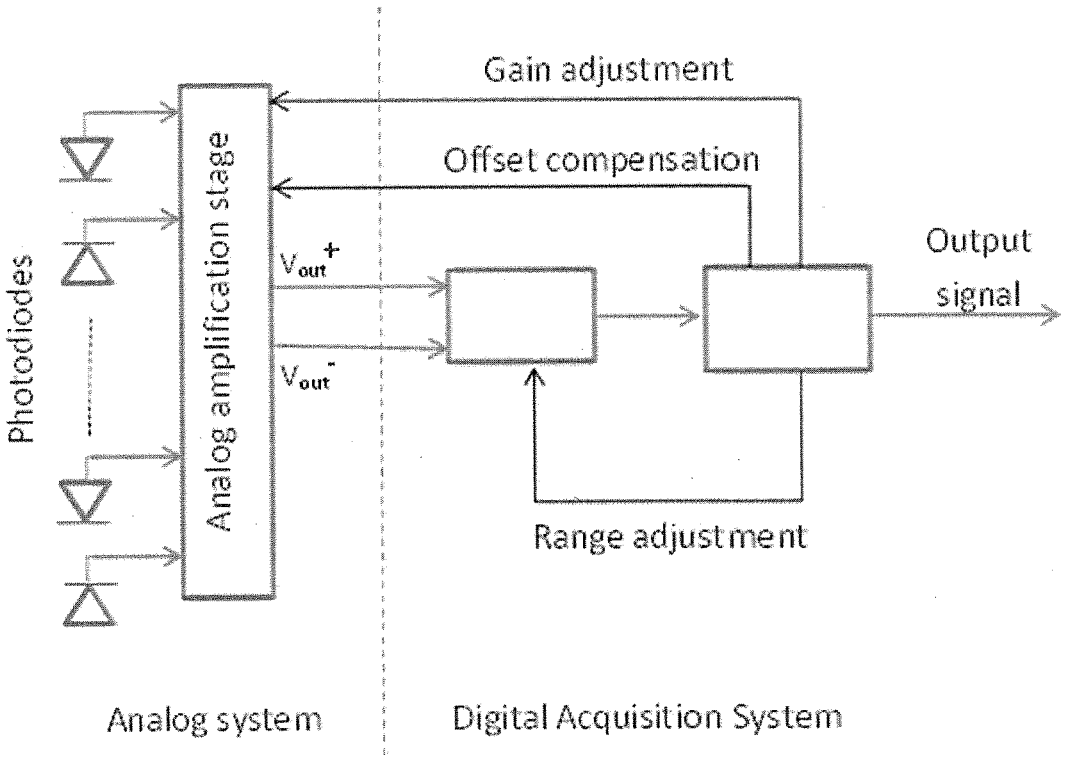

Gain adjustment
Offset compensation
Photodiodes
Analog amplification stage
V_{out}^+
V_{out}^-
Output signal
Range adjustment
Analog system
Digital Acquisition System

FIG. 3