

ABSTRACT OF THE INVENTION

A video display apparatus has a controllable, synchronized switch (Q2) to control current conduction in an inductance (T1). Responsive to a switching signal (Vd), a modulator (L1, C9, C10, D3, D4) switches the conductive states of the controllable switch (Q2). A signal generator (200) is coupled to a source (W1) of retrace pulses (Rt1), for generating at an output, a horizontal rate sawtooth signal (Hsaw). The sawtooth signal (Hsaw) comprising a first ramp of duration within the first half of the retrace pulse and a second ramp of duration equal to the trace interval. A source of a modulation signal (E/W correction) is coupled to a means (U1) for generating the switching signal (Vg). Responsive to the modulation and sawtooth signals the means (U1) generates the switching signal (Vg) having a first polarity which inhibits conduction in the switch (Q2) and a second polarity which enables conduction in the switch (Q2). The first polarity of switching signal commences within the first half of the retrace interval and the second polarity commences within the remaining time of the horizontal interval.

FIGURE 2

This Sheet Has Been Replaced

