

[ABSTRACT]

An information processing system includes a control unit, a peripheral device including a first register, a second register, and an initialization flag circuit, in which the initialization flag circuit is configured to output a high level signal when the information
5 processing system is started and is configured to output a low level signal in a case where data indicating a low level is written into the second register, the low level signal being the same level as the low level, and in which the control unit is configured to write initial setting data for initializing the peripheral device into the first register in a case where the control unit determines that an output signal from the initialization flag circuit is a high
10 level and is configured to write data indicating a low level into the second register, the low level indicating that writing of the initial setting data is completed.