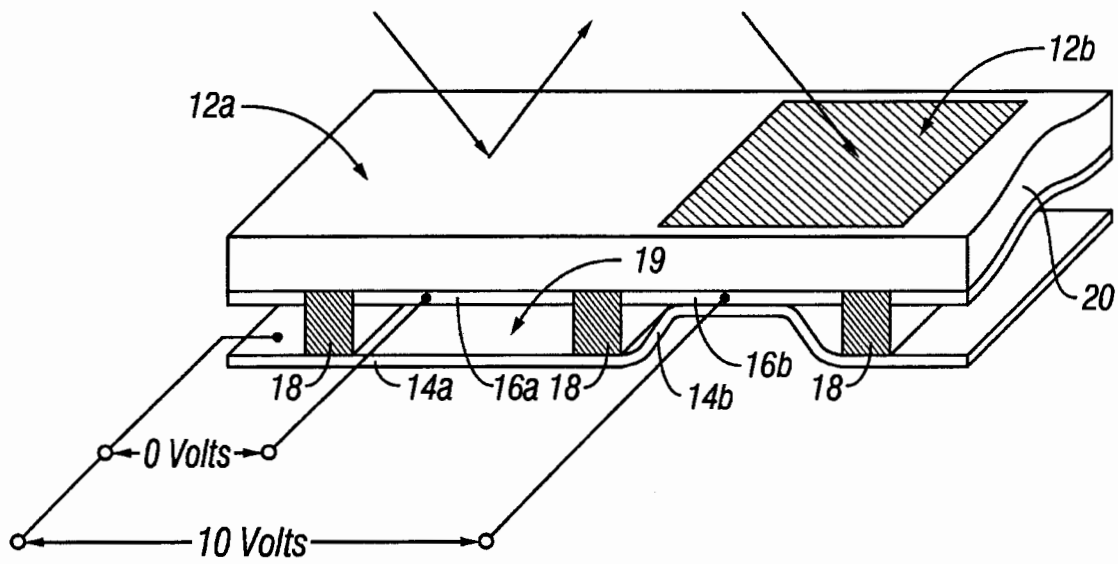


FIG. 1

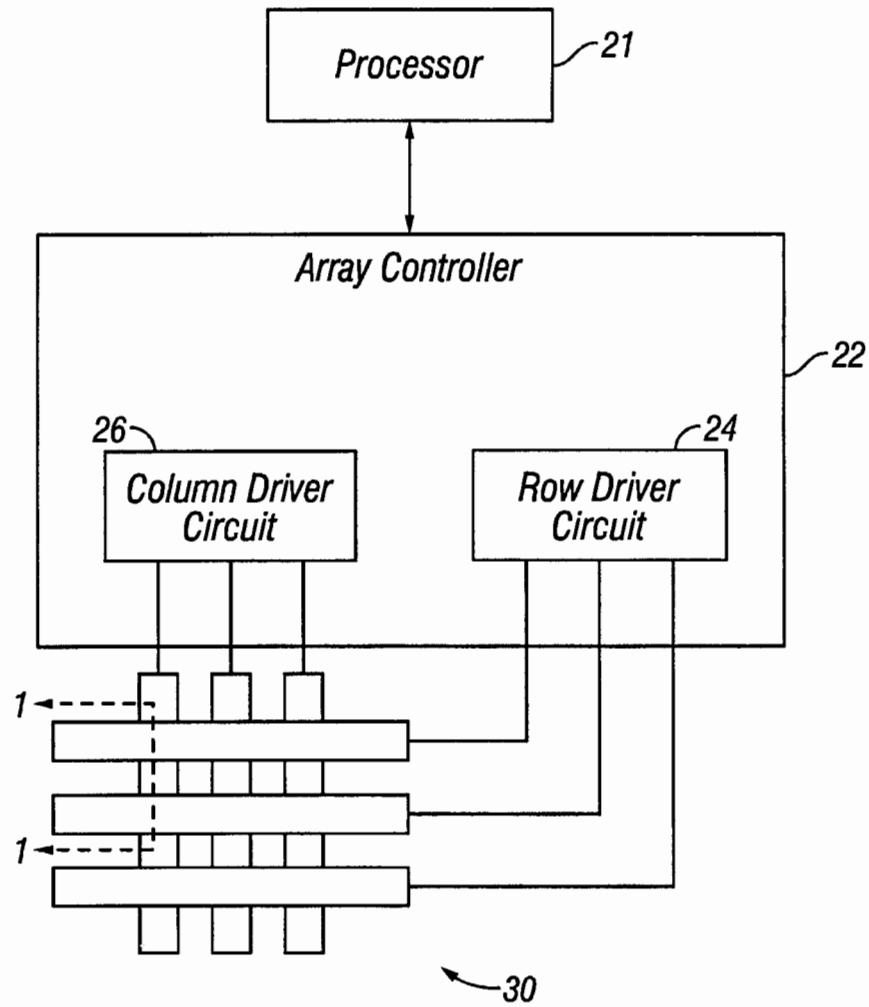

Processor
21
Array Controller
22
26
Column Driver Circuit
24
Row Driver Circuit
1
1
30

FIG. 2

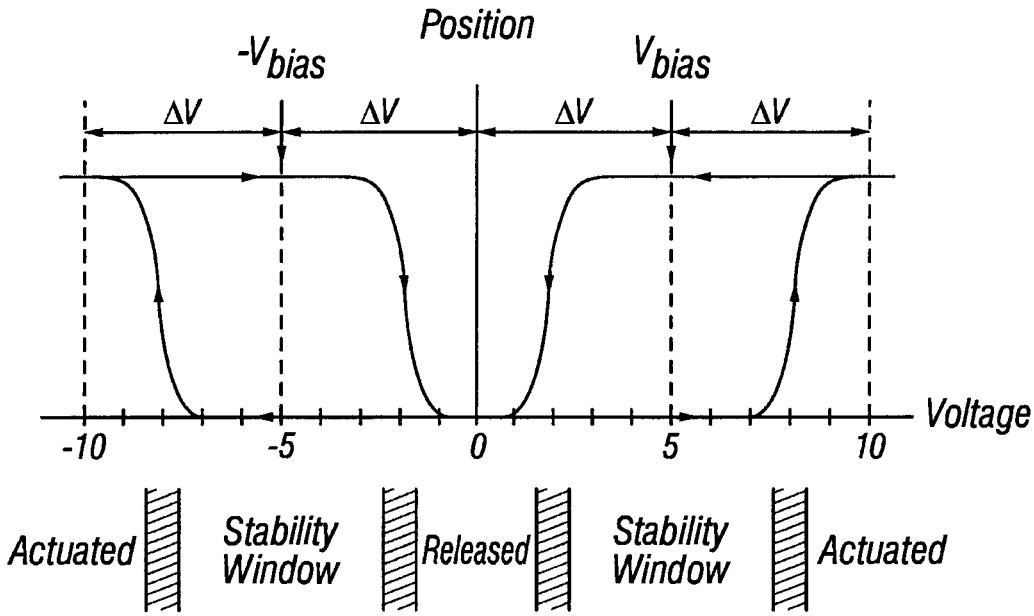


FIG. 3

Row Output Signals	Column Output Signals: $+V_{bias}$	Column Output Signals: $-V_{bias}$
0	Stable	Stable
$+\Delta V$	Release	Actuate
$-\Delta V$	Actuate	Release

FIG. 4

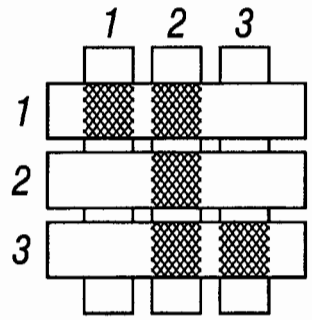

1
2
3
1
2
3

FIG. 5A

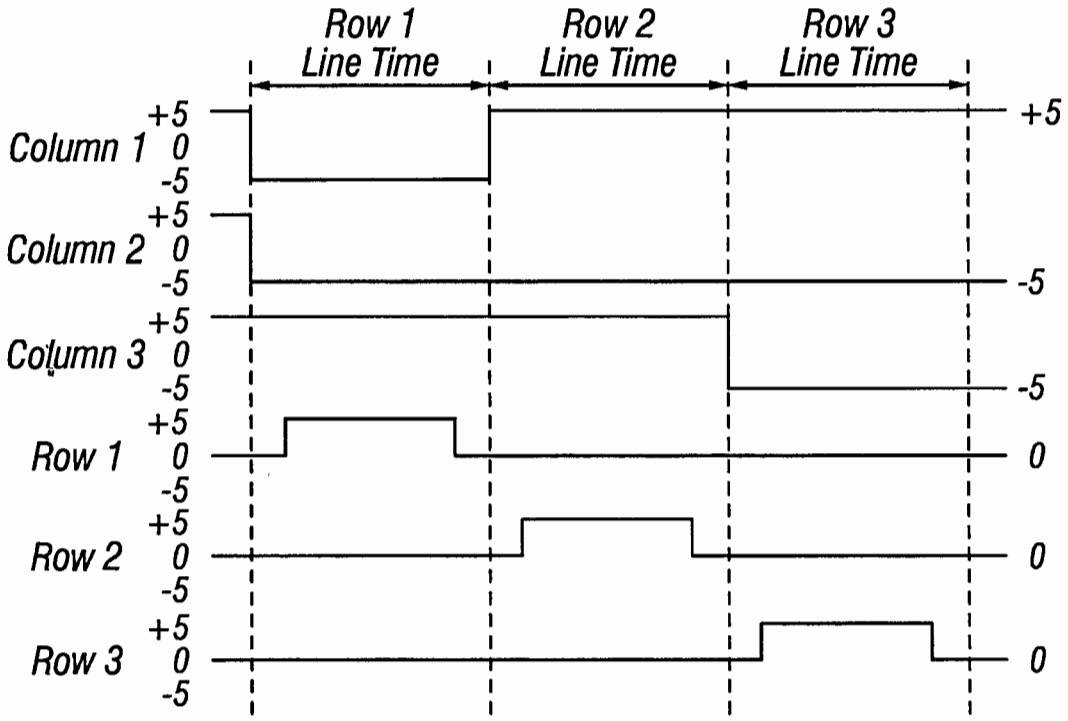

Row 1
Line Time
Row 2
Line Time
Row 3
Line Time
Column 1
Column 2
Column 3
Row 1
Row 2
Row 3
+5
0
-5
+5
-5
-5
0
0
0

FIG. 5B

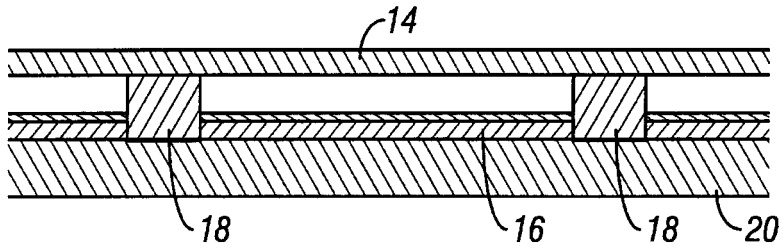


FIG. 6A

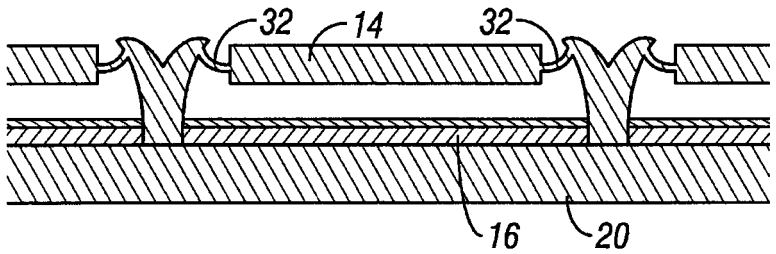


FIG. 6B

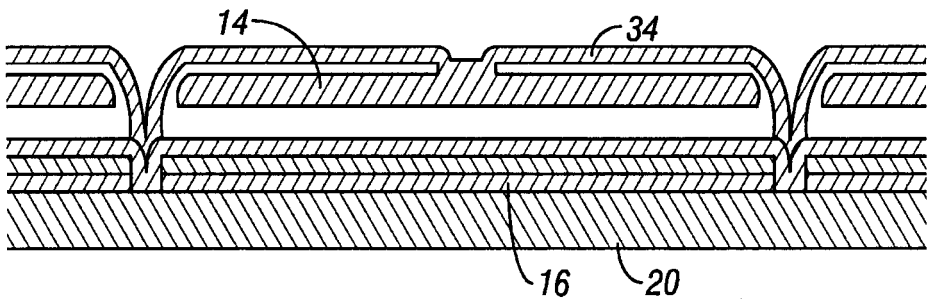


FIG. 6C

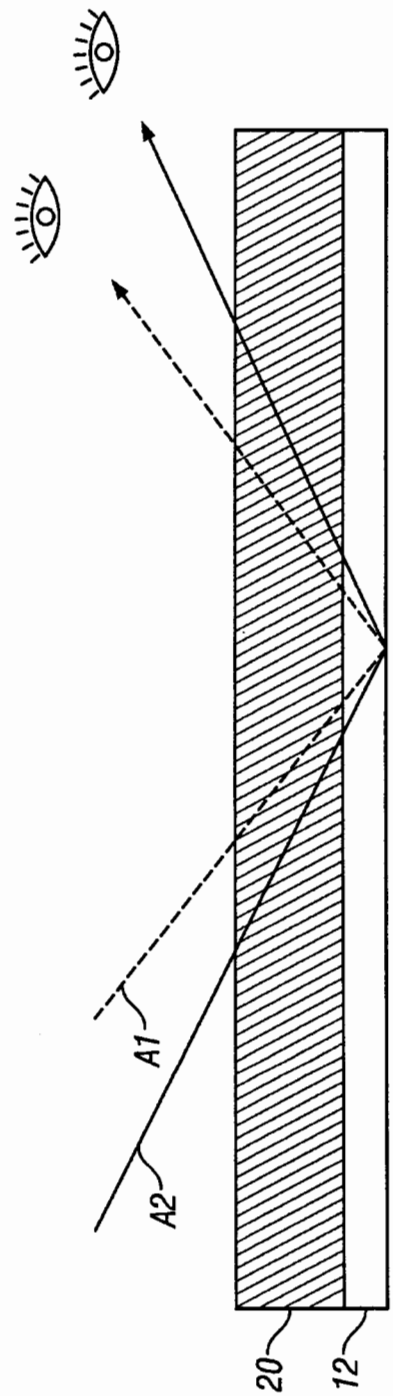


FIG. 7

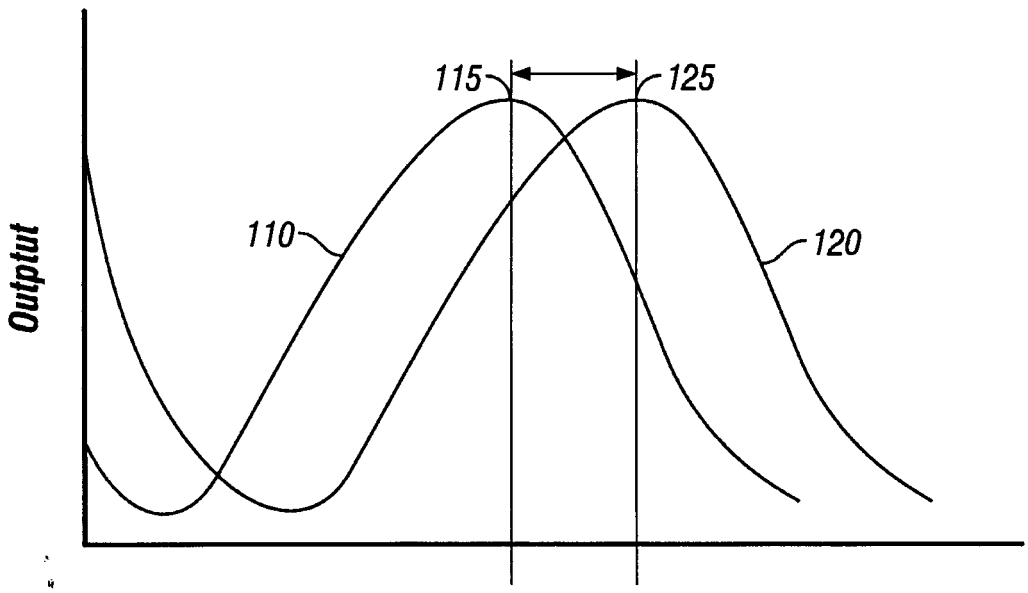


FIG. 8

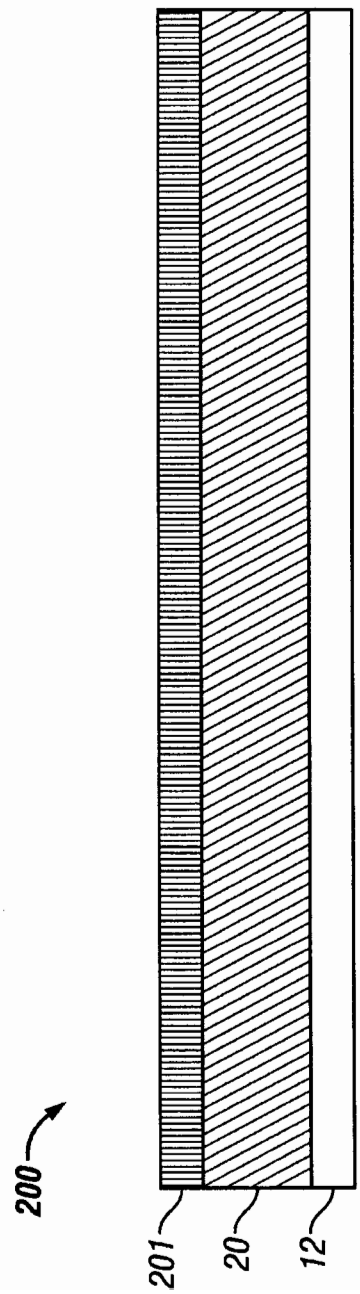


FIG. 9

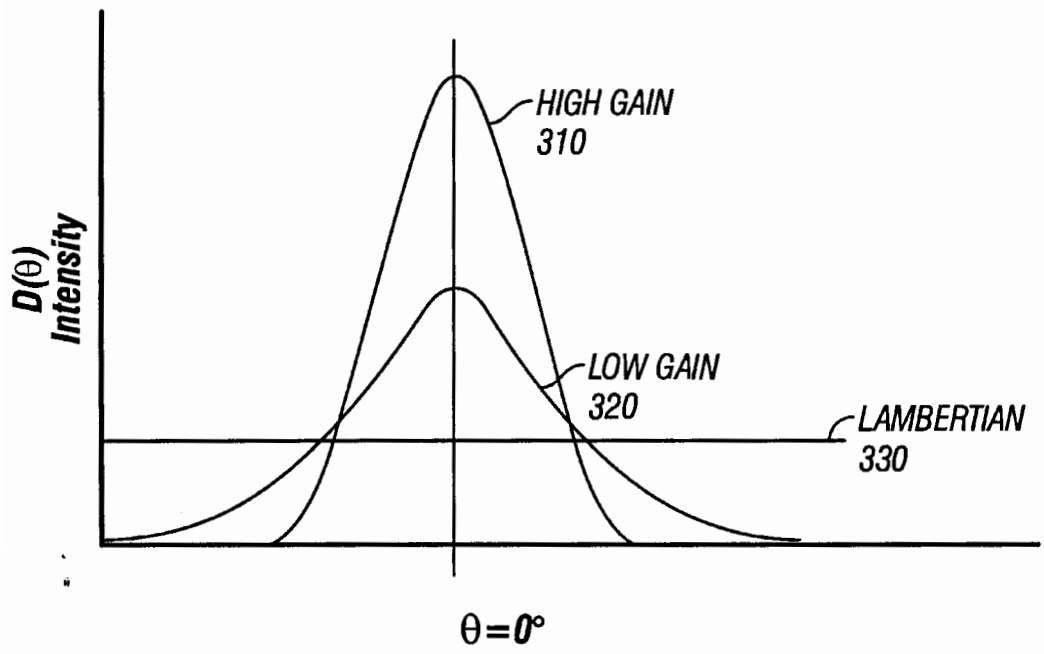


FIG. 10

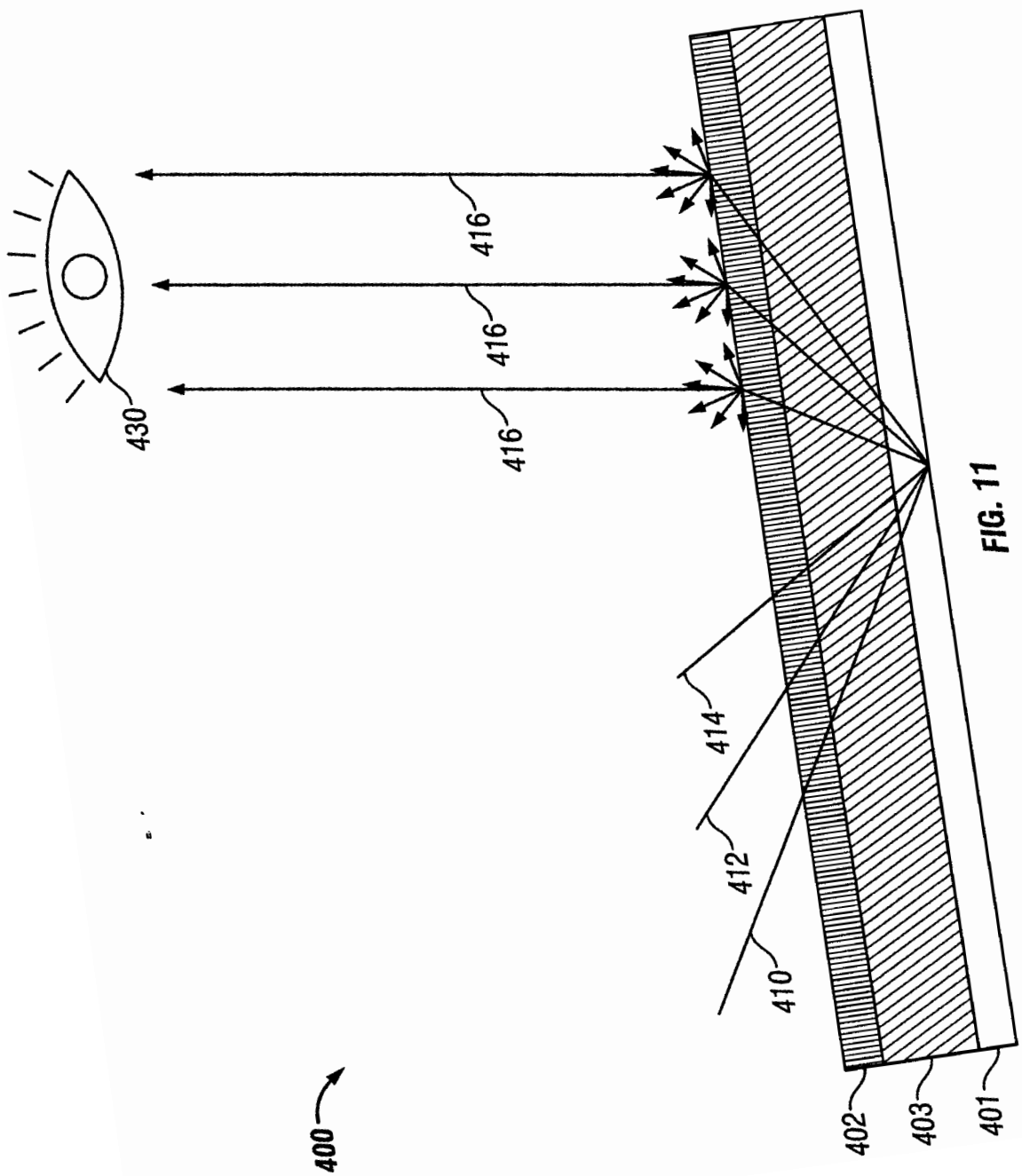


FIG. 11

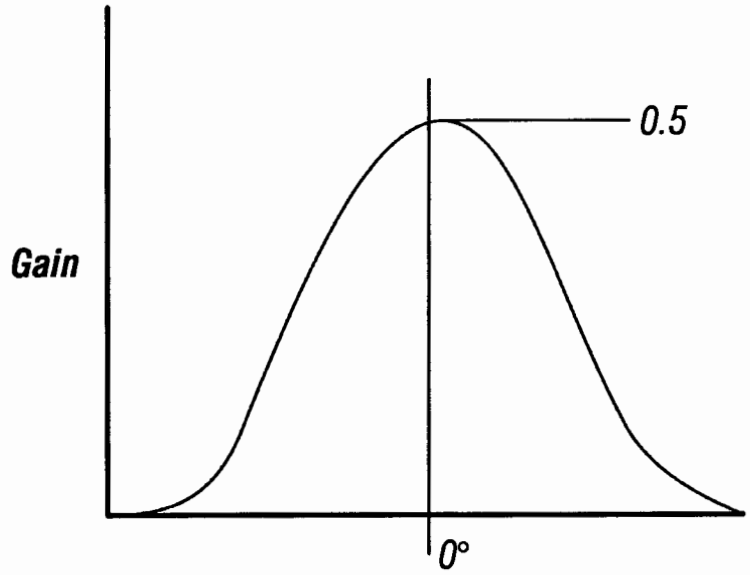


FIG. 12A

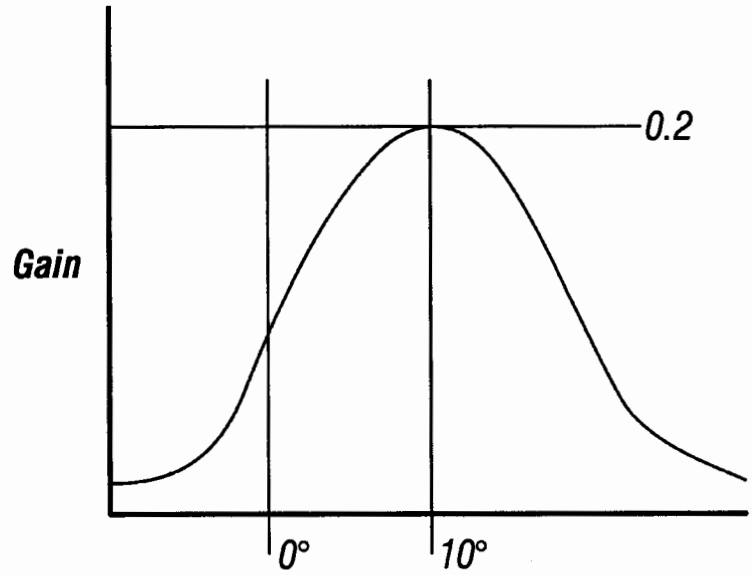


FIG. 12B

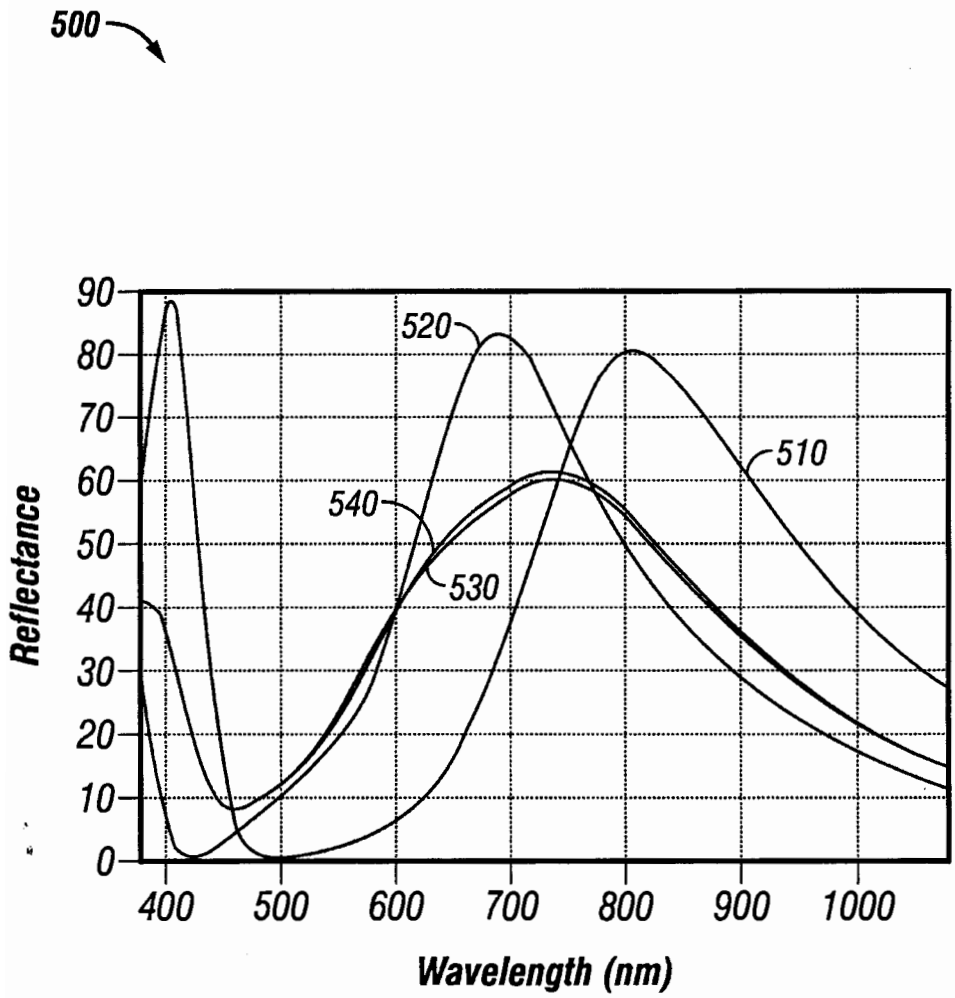

500
90
80
70
60
50
40
30
20
10
0
Reflectance
400
500
600
700
800
900
1000
Wavelength (nm)
520
510
540
530

FIG. 13

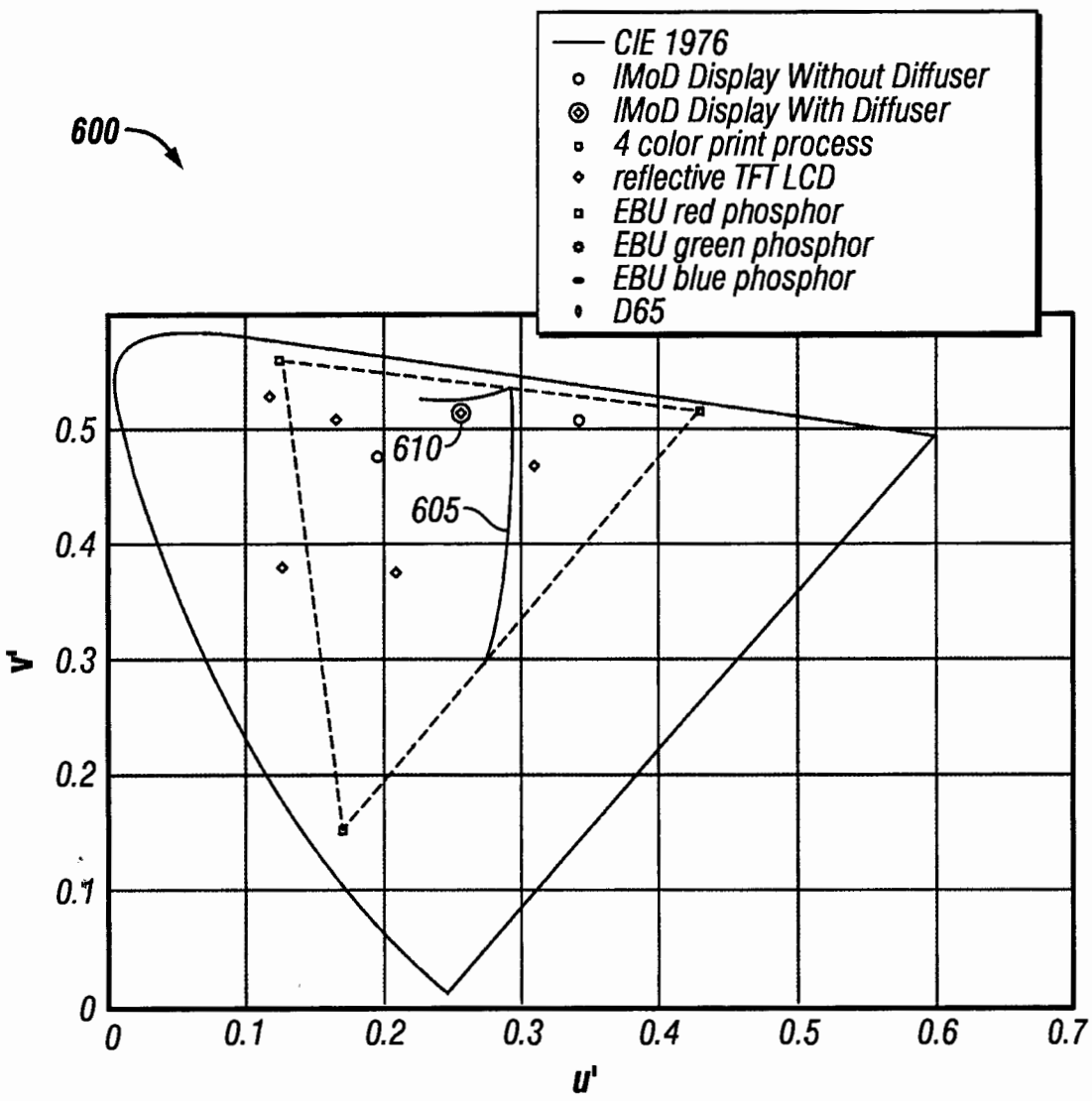

600
CIE 1976
IMoD Display Without Diffuser
IMoD Display With Diffuser
4 color print process
reflective TFT LCD
EBU red phosphor
EBU green phosphor
EBU blue phosphor
D65
610
605
v'
u'
0
0.1
0.2
0.3
0.4
0.5
0.6
0.7

FIG. 14

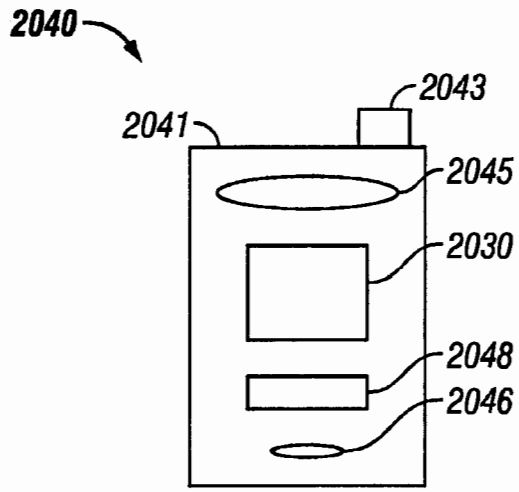


FIG. 15A

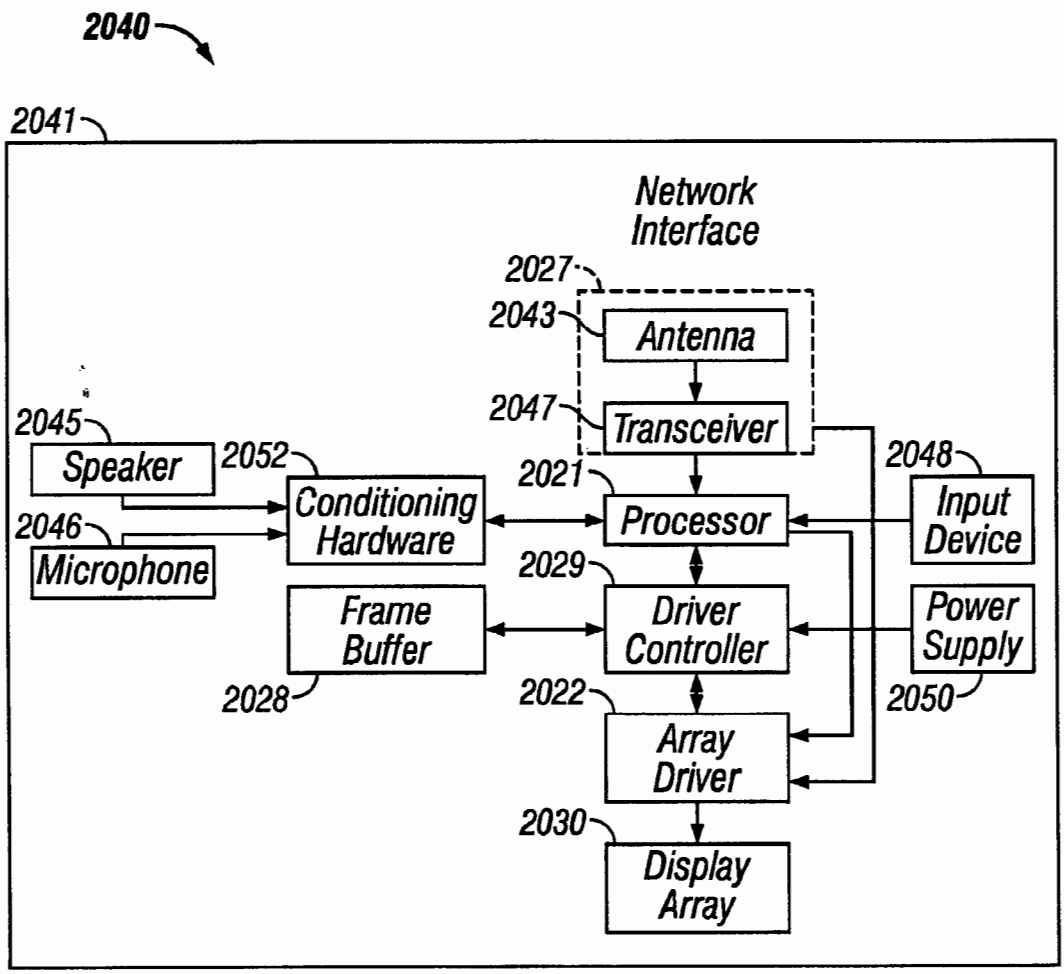


FIG. 15B