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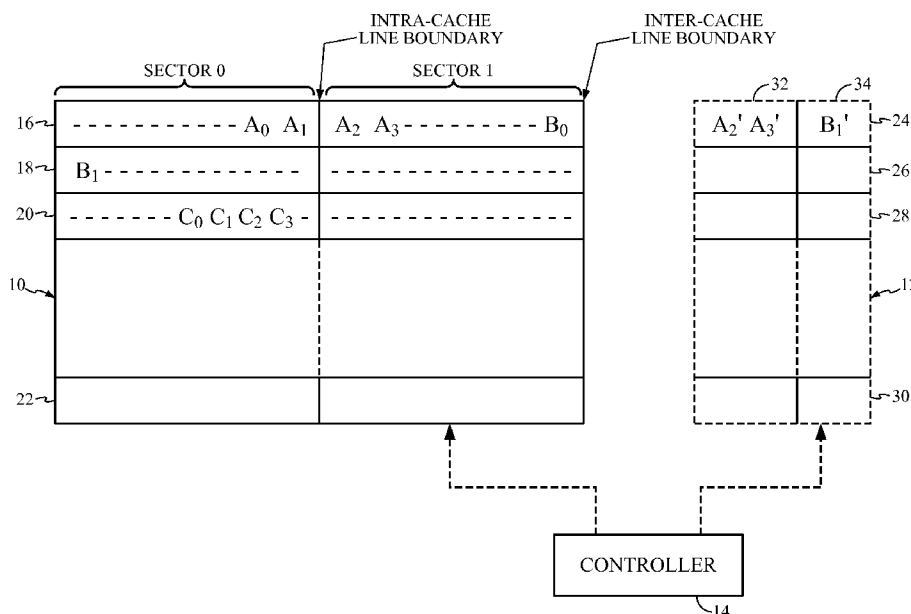
- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
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(54) Title: METHOD AND APPARATUS FOR CACHING VARIABLE LENGTH INSTRUCTIONS



(57) Abstract: An instruction cache controller uses supplemental memory to store a redundant copy of cached instruction data corresponding to a cache boundary position, and thereby enables subsequent single cache access retrieval of an instruction that crosses that boundary position. In one or more embodiments, the cache controller duplicates instruction data for the post-boundary position in the supplemental memory, and multiplexes that copied data into cache data obtained from the pre-boundary position.

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