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METHOD OF PROTECTING BOND WIRES DURING MOLDING AND HANDLING

BACKGROUND OF THE INVENTION

FIELD OF THE INVENTION

This invention relates to a method of protecting bond wires during fine pitch wire bonding and particularly for such wire bonding to pads of semiconductor devices.

BRIEF DESCRIPTION OF THE PRIOR ART

Connection between semiconductor chips and the outside world are generally provided through bond pads disposed on the surfaces of the chips. Due to the constant increase in the number of electrical components capable of being disposed in a given volume of semiconductor material, the circuitry contained in the chips has become increasingly more complex. This has made it necessary to increase the number of pins in the package containing the chip and has also led to an increase the number of bond pads on a chip. In order to accommodate this increase in the number of pins and bond pads, the pitch of the bond pads must be reduced and longer bond wires are required. As the wires increase in length, they tend to sag and can easily cause a malfunction, such as, for example, a short circuit, by sagging against another electrically conductive element from which it should be electrically isolated.

To date, most improvements in long wire capability have been provided by (1) improved looping profiles, (2) improved wire, (3) low viscosity mold compounds and (4) better molding processes. Another prior art approach has been to use a looping ring which is positioned between the die pad and the lead finger between which a wire is to be connected. The wire then extends over the looping ring and is supported thereby, preferably at the highest point on the wire, to minimize to problem caused by wire sag.

With the further decrease in dimensions, the wires, which must be separated during handling and molding, continue to move closer together. This presents a severe problem, especially during molding, because the molding composition tends to move the wires in the direction of molding composition flow and can cause adjacent wires to move against each other.

SUMMARY OF THE INVENTION

In accordance with the present invention, the above noted problems of the prior art are minimized and there is provided a mechanism and procedure whereby the wires are maintained separated from each other during handling and/or molding.

Briefly, the above is accomplished by dispensing an adhesive, preferably an epoxy and preferably an adhesive which can be dispensed in a flowable state and then harden, around the already bonded and separated wires, preferably at the highest point on each wire, this being the location of the looping ring of the prior art. The adhesive will rest on the top of each wire wherein separation

from an adjacent wire may be a problem and will be interconnected as a single mass from wire to adjacent wire with the adhesive being connected to at least one pair of adjacent wires. Some of the adhesive may drop to the underside or beneath the wire. This ties each wire to the adhesive and maintains the wires separated from each other during handling and molding. The adhesive is preferably an adhesive which can be dispensed in a flowable state and then harden and is preferably an epoxy. The adhesive viscosity must be such that the adhesive will rest on the wire surface for a sufficiently long period to harden before a substantial portion thereof can drip off of the wire. The adhesive viscosity must also be such that it maintains contact with the adhesive on adjacent wires through hardening and maintains such contact thereafter. It can be seen that if some of the adhesive drops beneath the wires and is retained on the chip surface below the wires, the adhesive will then provide the uplifting action of the prior art looping ring in addition to providing the wire separation of the adhesive as discussed above.

In operation, wires will be bonded in standard manner between die pads on a chip and lead fingers with the wires extending upwardly from the die pad and then looping downwardly to the die finger. The flowable adhesive is then dispensed over the wires, preferably at the top of the loop, so that the adhesive disposed over any wire also contacts the adhesive disposed over the adjacent wire or wires and the adhesive is allowed to harden. Accordingly, when the adhesive hardens, the wires are separated from each other

and remain in that relation due to the interconnecting nature of the adhesive from wire to wire. The chip can then be handled and/or molding composition can be placed over the chip with the wires retaining their separated position relative to each other. If some of the adhesive drops onto the surface of the chip beneath the wire, such adhesive will serve the additional function of the looping ring of the prior art. It can be seen that the function of the looping ring can be provided without the additional steps of positioning and adhering the looping ring as required in the prior art. Also, the precision of positioning the looping ring of the prior art is not necessary.

BRIEF DESCRIPTION OF THE DRAWINGS

FIGURE 1 is a side view of a standard prior art die pad and lead finger with a wire connected therebetween;

FIGURE 2 is a side view of a standard die pad and lead finger with a wire connected therebetween, the wire being supported at its highest point with a looping ring in accordance with the prior art;

FIGURE 3 is a top view of a semiconductor chip with wires extending from the die pads thereof and a ring of adhesive in accordance with the present invention;

FIGURE 4 is a view taken along the line 4-4 of FIGURE 3; and

FIGURE 5 is an enlarged view of a portion of the wires of FIGURE 3 and bond ring thereon.

DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring first to FIGURE 1, there is shown a standard prior art chip 1 having a plurality of die pads 3, only one of which is shown. A wire 5 is looped upwardly from the die pad 3 and then downwardly to a lead finger 7 which is part of a lead frame (not shown) having pins which extend out of the final package.

FIGURE 2 shows the structure of FIGURE 1 with a prior art looping ring 9 disposed in the vicinity of the highest point of the wire. The looping ring 9 minimizes the likelihood of the wire 5 contacting circuitry on the chip 1 other than the bond pad 3. The looping ring 9 also prevents movement of the wires 5 in the plane normal to the page showing FIGURE 2 and thereby does not prevent adjacent wires from contacting each other during handling and/or encapsulation.

Referring now to FIGURES 3 to 5, there is shown a wire protection scheme in accordance with the present invention. There is shown a chip 1 having die pads 3, best shown in FIGURE 4. There will be a plurality of die pads 3, one die pad generally being associated with one wire 5. A wire 5 is shown bonded to each die pad 3 and to a lead finger 7 as in the prior art. The wires 5 will be separated from each other and disposed above the surface of the chip 1. A ring of flowable, hardenable epoxy of proper viscosity 11 is then disposed over the wires 5 in a continuous ring and allowed to harden in situ on the wires. It should be understood that, while the epoxy adhesive 11 is shown as a continuous ring in the preferred embodient, the adhesive need only extend between

those wires 5 which will or may require separation assistance up to and including the encapsulation procedure, thereby allowing the adhesive to be discontinuous, if appropriate. The chip can then be handled and/or packaged with minimal likelihood of malfunction due to improper contact with the wires 5.

Though the invention has been described with respect to a specific preferred embodiment thereof, many variations and modifications will immediately become apparent to those skilled in the art. It is therefore the intention that the appended claims be interpreted as broadly as possible in view of the prior art to include all such variations and modifications.



CLAIMS

1. A semiconductor device which comprises:

(a) a chip having plural bond pads thereon;

(b) a plurality of wires disposed adjacent each other, each of said wires having one end thereof coupled to and extending from one of said bond pads; and

(c) a unitary mass of hardened adhesive disposed over and secured to each of said wires to provide separation of said wires from each other.

2. The device of claim 1, further including a plurality of lead fingers, each of said wires coupled at its other end to one of said lead fingers.

3. The device of claim 1 further including a second plurality of adjacent wires, each of said second plurality of adjacent wires having one end thereof coupled to and extending from one of said bond pads and a second unitary mass of hardened adhesive disposed over and secured to said second plurality of adjacent wires to provide separation of said second plurality of adjacent wires from each other.

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4. The device of claim 2 further including a second plurality of adjacent wires, each of said second plurality of adjacent wires having one end thereof coupled to and extending from one of said bond pads and a second unitary mass of hardened adhesive disposed over and secured to said second plurality of adjacent wires to provide separation of said second plurality of adjacent wires.

5. The device of claim 1 wherein said adhesive is flowable, hardenable and has a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened.

6. The device of claim 2 wherein said adhesive is flowable, hardenable and has a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened.

7. The device of claim 3 wherein said adhesive is flowable, hardenable and has a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened.

8. The device of claim 4 wherein said adhesive is flowable, hardenable and has a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened.

9. The device of claim 1 further including a portion of said adhesive disposed beneath a said wire on a surface of said chip.

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10. The device of claim 2 further including a portion of said adhesive disposed beneath a said wire on a surface of said chip.

11. The device of claim 5 further including a portion of said adhesive disposed beneath a said wire on a surface of said chip.

12. The device of claim 8 further including a portion of said adhesive disposed beneath a said wire on a surface of said chip.

13. A method of making a semiconductor device comprising the steps of:

(a) providing a chip having plural bond pads thereon and a plurality of wires adjacent each other, each of said adjacent wires having one end thereof coupled to and extending from one of said bond pads; and

(b) forming a unitary mass of hardened adhesive over and secured to each of at least an adjacent pair of said wires to provide separation of said wires from each other.

14. The method of claim 13 further including the step of adhering the other end of each said wire to a lead finger.

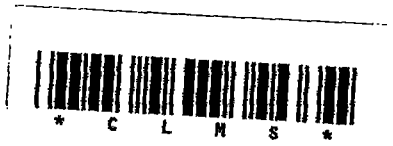
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15. The method of claim 13 further including the step of providing a second plurality of wires adjacent each other, each of said second plurality of wires having one end thereof coupled to and extending from one of said bond pads and providing a second unitary mass of hardened adhesive disposed over and secured to each of said second plurality of wires to provide separation of said second plurality of wires from each other.

16. The method of claim 14 further including the step of providing a second plurality of adjacent wires, each of said second plurality of adjacent wires having one end thereof coupled to and extending from one of said bond pads and providing a second unitary mass of hardened adhesive disposed over and secured to each of said second plurality of adjacent wires to provide separation of said second plurality of adjacent wires.

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17. A method of making a semiconductor device comprising the steps of:

(a) providing a chip having plural bond pads thereon, a plurality of adjacent wires, each said wire having one end thereof coupled to and extending from one of said bond pads and a plurality of lead fingers, the other end of said wire coupled to one of said lead fingers;

(b) disposing a unitary mass of hardenable, flowable adhesive having a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened; and

(c) hardening said adhesive.

18. The method of claim 17 wherein said adhesive is an epoxy.

19. The method of claim 17 further including the step of permitting some of said adhesive in step (b) to be disposed on a surface of said chip and beneath a said wire and hardening said adhesive on said surface of said chip.

20. The method of claim 18 further including the step of permitting some of said adhesive in step (b) to be disposed on a surface of said chip and beneath a said wire and hardening said adhesive on said surface of said chip.

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METHOD OF PROTECTING BOND WIRES DURING MOLDING AND HANDLING

ABSTRACT OF THE DISCLOSURE

A method of making a semiconductor device and the device wherein a chip is provided having plural bond pads thereon. A plurality of adjacent wires is provided, each wire having one end thereof coupled to and extending from one of the bond pads. The other end of each wire is coupled to one of a plurality of lead fingers. A mass of hardenable, flowable adhesive having a viscosity in its flowable state sufficient to enable the adhesive to rest on a the wire until hardened, preferably an epoxy, is disposed on adjacent wires and the adhesive is then hardened. Optionally, some of the adhesive is permitted to be disposed on a surface of the chip and beneath a wire and then hardened.

FIGURE 4

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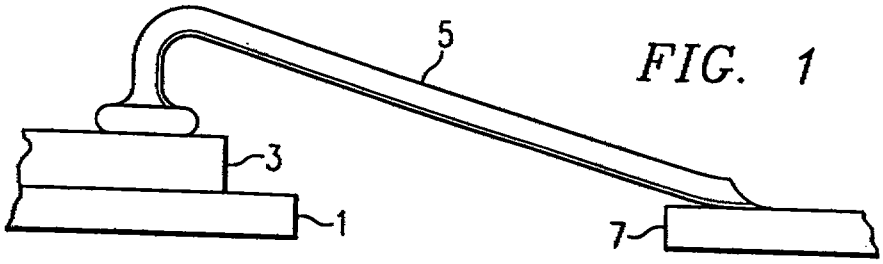


FIG. 1

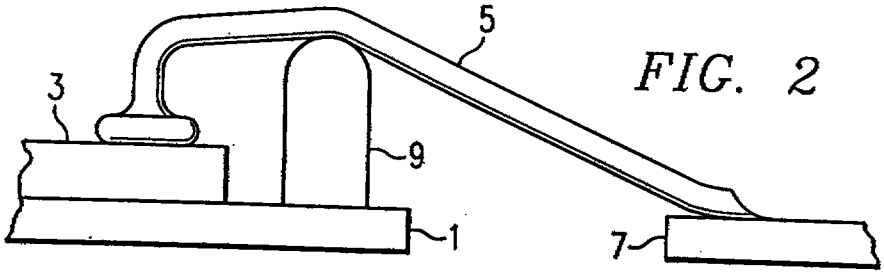


FIG. 2

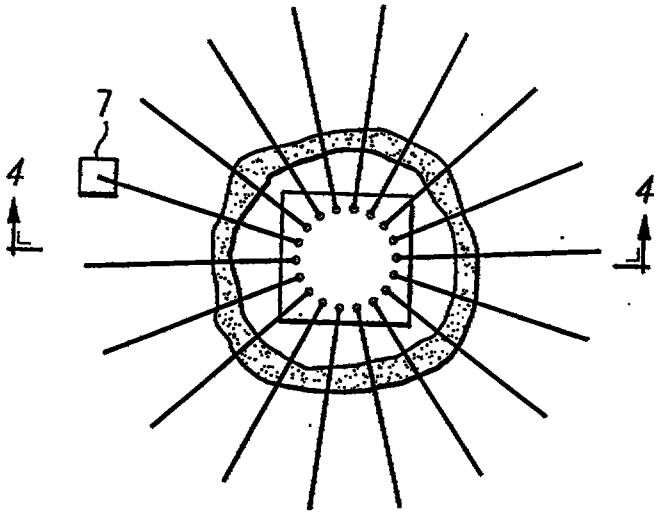


FIG. 3

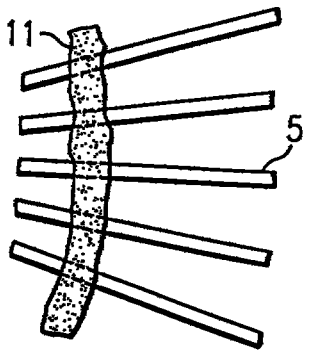


FIG. 5

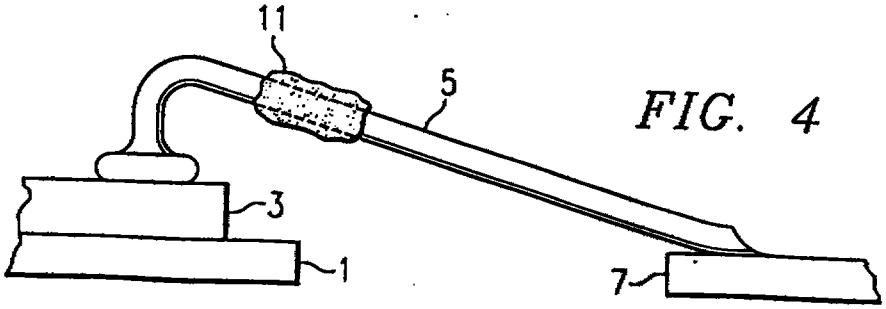


FIG. 4

CLAIMS

1. A semiconductor device which comprises:

(a) a chip having plural bond pads thereon;

(b) a plurality of wires disposed adjacent each other, each of said wires having one end thereof coupled to and extending from one of said bond pads; and

(c) a unitary mass of hardened adhesive disposed over and secured to each of said wires to provide separation of said wires from each other, a portion of said adhesive being disposed beneath said wire on a surface of said chip.

2. A semiconductor device which comprises:

(a) a chip having plural bond pads thereon;

(b) a plurality of wires disposed adjacent each other, each of said wires having one end thereof coupled to and extending from one of said bond pads; and

(c) a unitary mass of hardened adhesive disposed over and secured to each of said wires to provide separation of said wires from each other, further including a plurality of lead fingers, each of said wires coupled at its other end to one of said lead fingers, further including a portion of said adhesive disposed beneath said wire on a surface of said chip.

3. A semiconductor device which comprises:

(a) a chip having plural bond pads thereon;

(b) a plurality of wires disposed adjacent each other, each of said wires having one end thereof coupled to and extending from one of said bond pads; and

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(c) a unitary mass of hardened adhesive disposed over and secured to each of said wires to provide separation of said wires from each other, wherein said adhesive is flowable, hardenable and has a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened, further including a portion of said adhesive disposed beneath said wire on a surface of said chip.

4. A semiconductor device which comprises:

(a) a chip having plural bond pads thereon;

(b) a plurality of wires disposed adjacent each other, each of said wires having one end thereof coupled to and extending from one of said bond pads; and

(c) a unitary mass of hardened adhesive disposed over and secured to each of said wires to provide separation of said wires from each other, further including a plurality of lead fingers, each of said wires coupled at its other end to one of said lead fingers, further including a second plurality of adjacent wires, each of said second plurality of adjacent wires having one end thereof coupled to and extending from one of said bond pads and a second unitary mass of hardened adhesive disposed over and secured to said second plurality of adjacent wires to provide separation of said second plurality of adjacent wires, wherein said adhesive is flowable, hardenable and has a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened, further including a portion of said adhesive disposed beneath said wire on a surface of said chip.

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5. A method of making a semiconductor device comprising the steps of:

(a) providing a chip having plural bond pads thereon, a plurality of adjacent wires, each said wire having one end thereof coupled to and extending from one of said bond pads and a plurality of lead fingers, the other end of said wire coupled to one of said lead fingers;

(b) disposing a unitary mass of hardenable, flowable adhesive having a viscosity in its flowable state sufficient to enable said adhesive to rest on said wire until hardened;

(c) hardening said adhesive; and

(d) further including the step of permitting some of said adhesive in step (b) to be disposed on a surface of said chip and beneath said wire and hardening said adhesive on said surface of said chip.

6. A method of making a semiconductor device comprising the steps of:

(a) providing a chip having plural bond pads thereon, a plurality of adjacent wires, each said wire having one end thereof coupled to and extending from one of said bond pads and a plurality of lead fingers, the other end of said wire coupled to one of said lead fingers;

(b) disposing a unitary mass of hardenable, flowable adhesive having a viscosity in its flowable state sufficient to enable said adhesive to rest on a said wire until hardened;

(c) hardening said adhesive;

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(d) further including the step of permitting some of said adhesive in step (b) to be disposed on a surface of said chip and beneath a said wire.and hardening said adhesive on said surface of said chip, wherein said adhesive is an epoxy.

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METHOD OF PROTECTING BOND WIRES DURING MOLDING AND HANDLING

ABSTRACT OF THE DISCLOSURE

A method of making a semiconductor device and the device wherein a chip is provided having plural bond pads thereon. A plurality of adjacent wires is provided, each wire having one end thereof coupled to and extending from one of the bond pads. The other end of each wire is coupled to one of a plurality of lead fingers. A mass of hardenable, flowable adhesive having a viscosity in its flowable state sufficient to enable the adhesive to rest on a the wire until hardened, preferably an epoxy, is disposed on adjacent wires and the adhesive is then hardened. Optionally, some of the adhesive is permitted to be disposed on a surface of the chip and beneath a wire and then hardened.

FIGURE 4

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