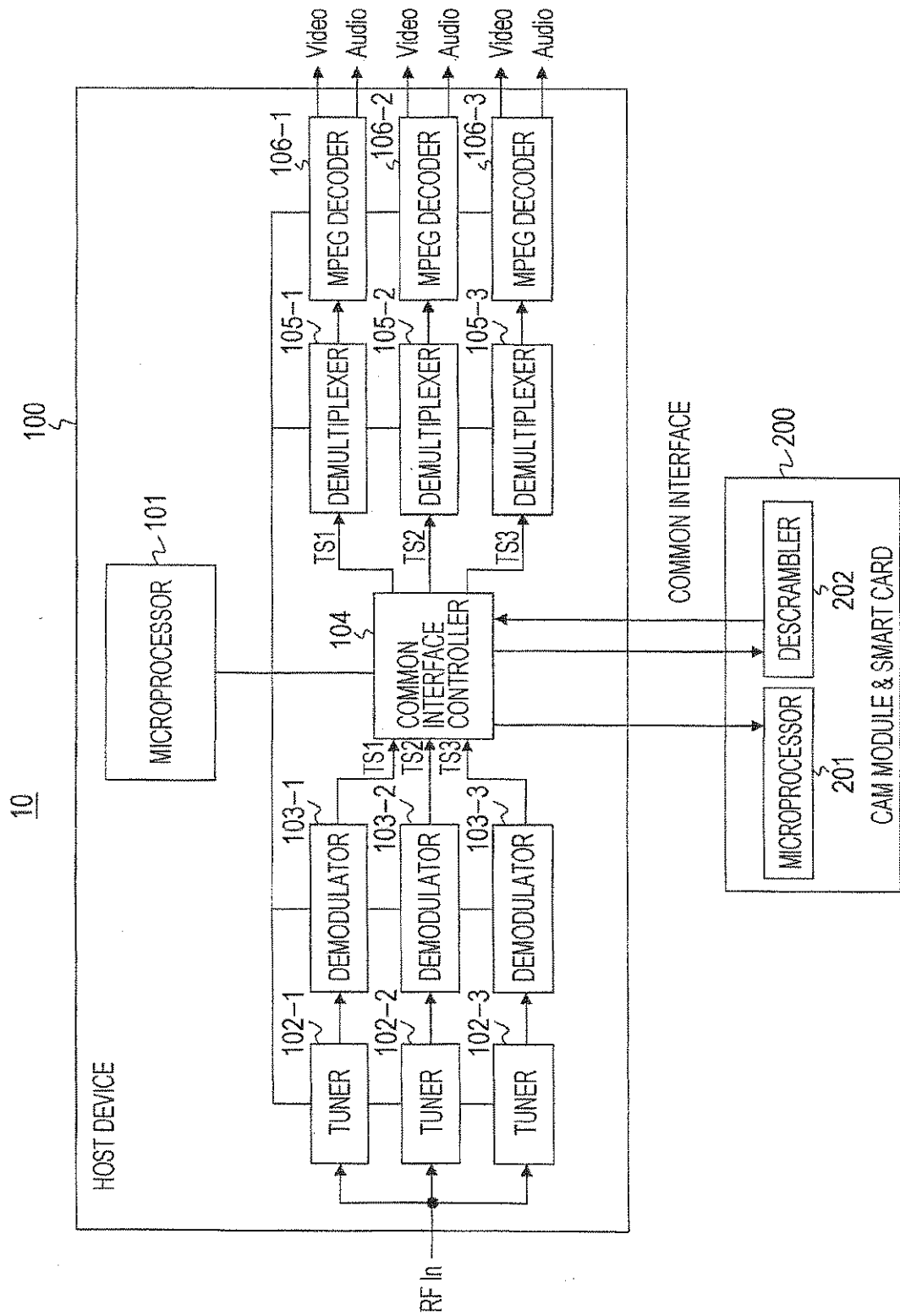


FIG. 1



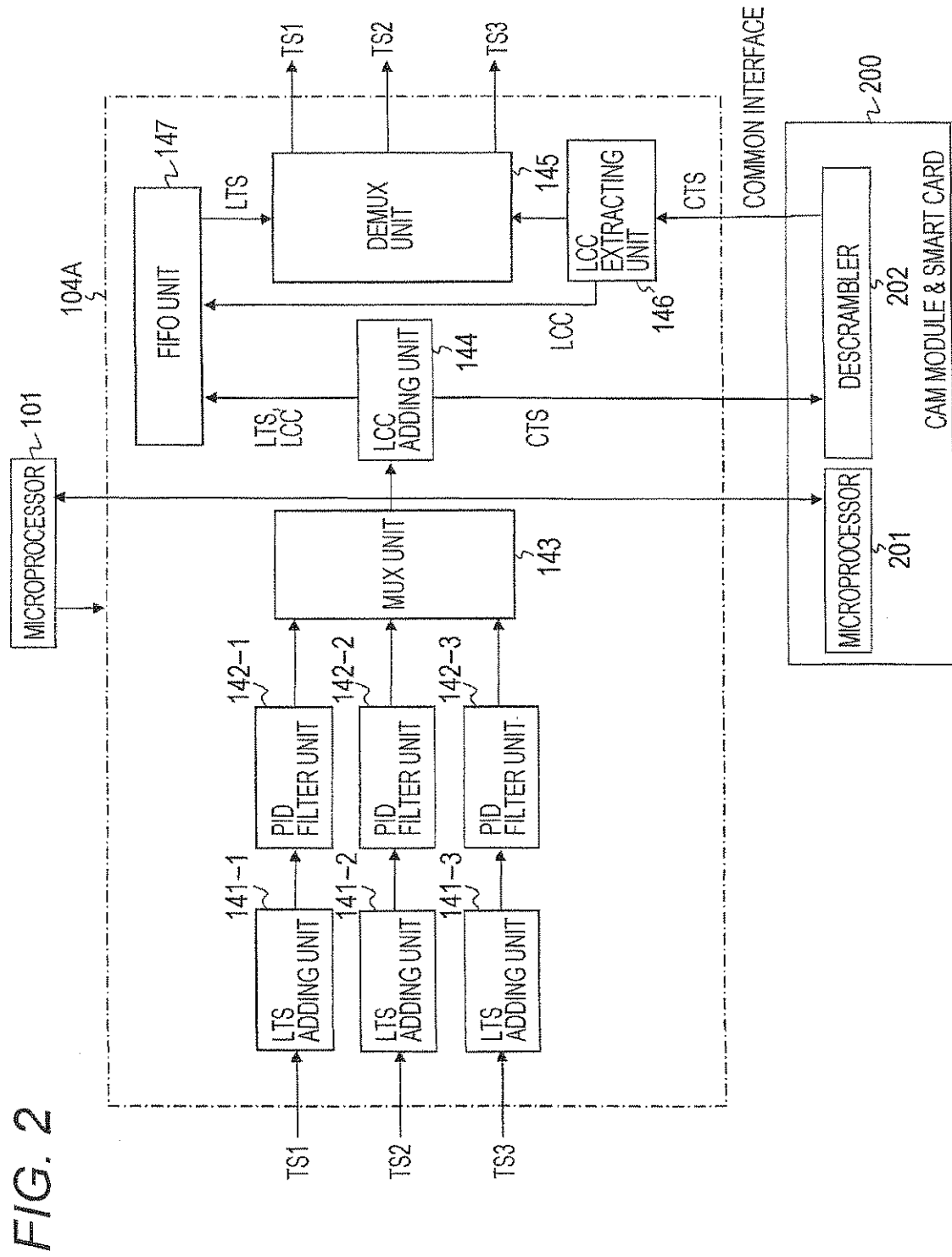


FIG. 3

STRUCTURE OF TSP

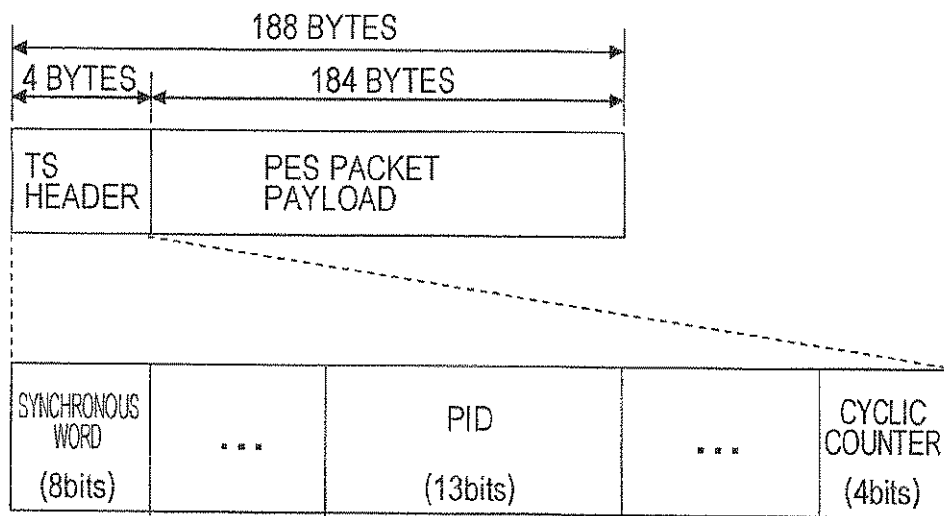


FIG. 4

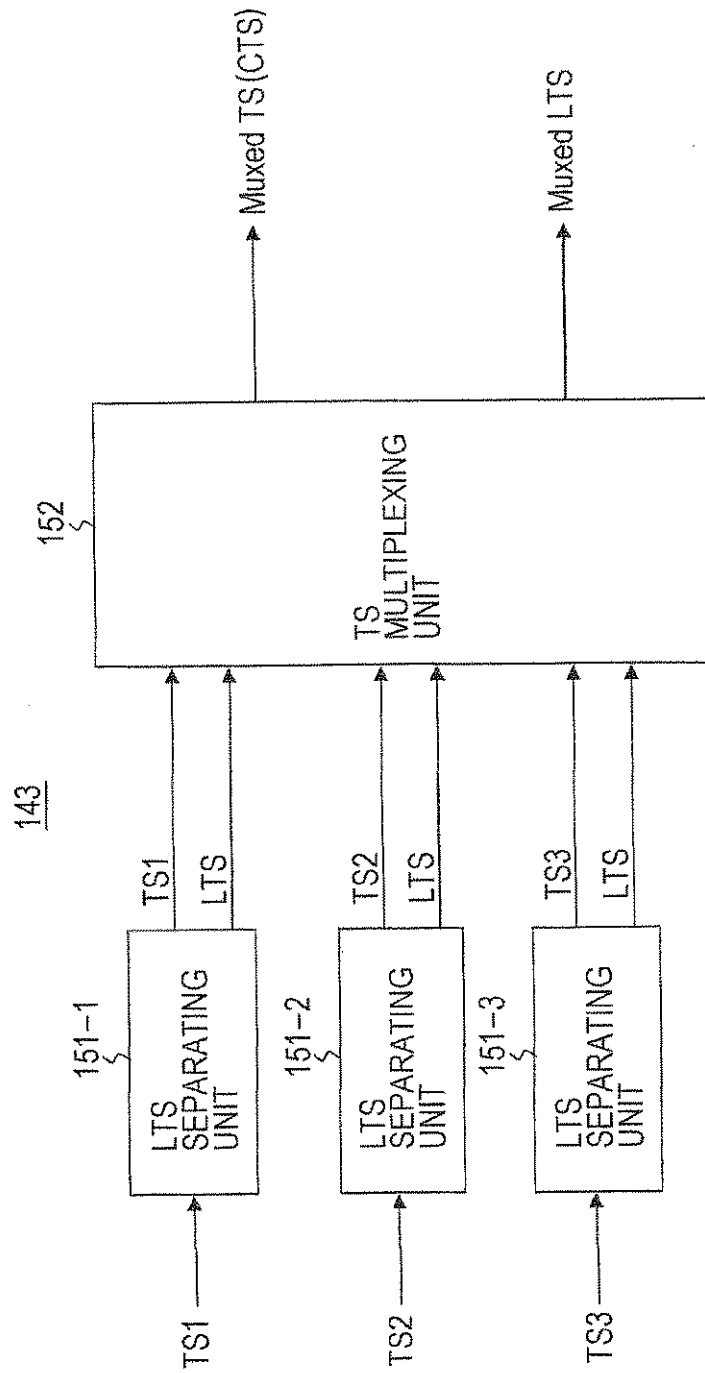


FIG. 5

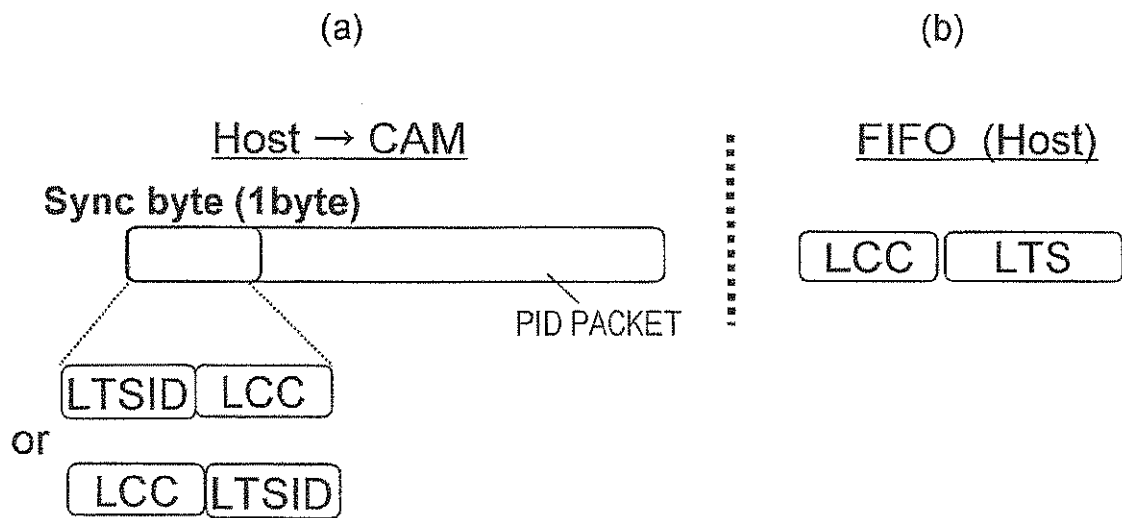


FIG. 6

	LTSID	LCC	Recover range of packet order change
1	6bit	2bit	64TS / recover less than 2 packets order change
2	5bit	3bit	32TS / recover less than 6 packets order change
3	4bit	4bit	16TS / recover less than 14 packets order change
4	3bit	5bit	8TS / recover less than 30 packets order change
5	2bit	6bit	4TS / recover less than 62 packets order change
6	1bit	7bit	2TS / recover less than 126 packets order change

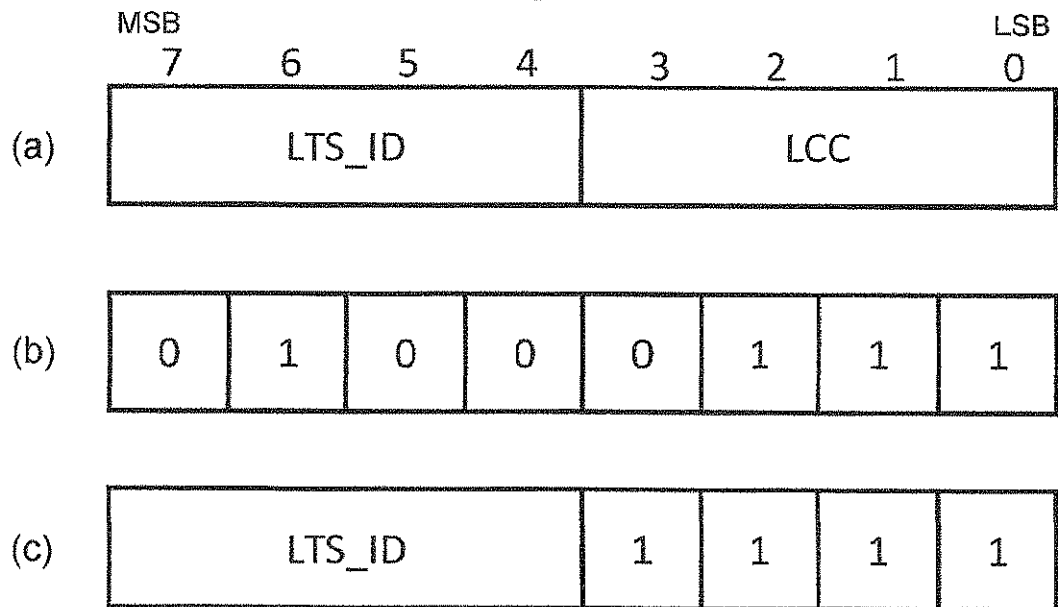
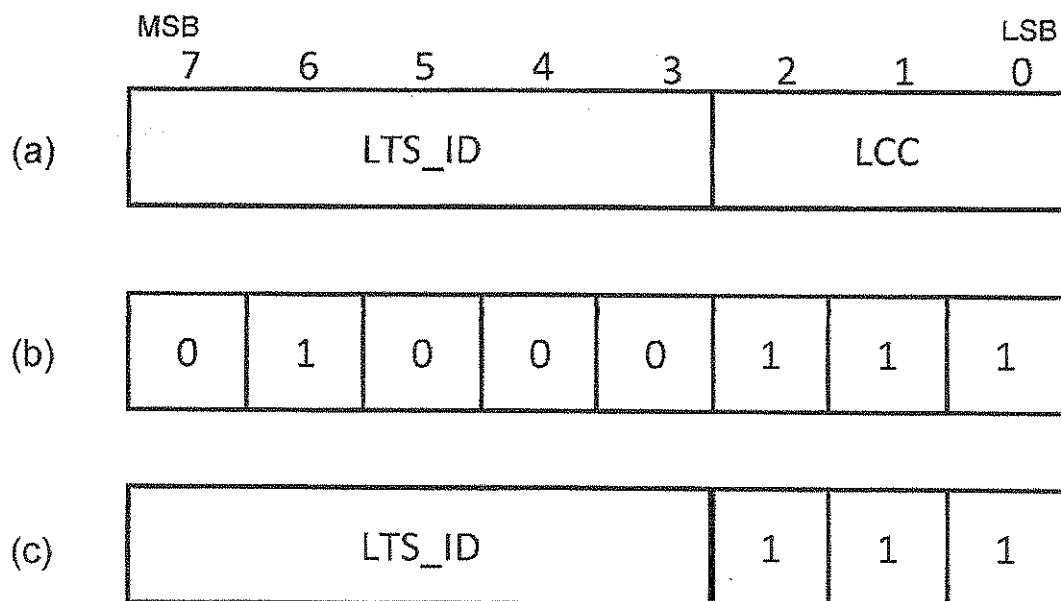
FIG. 7*FIG. 8*

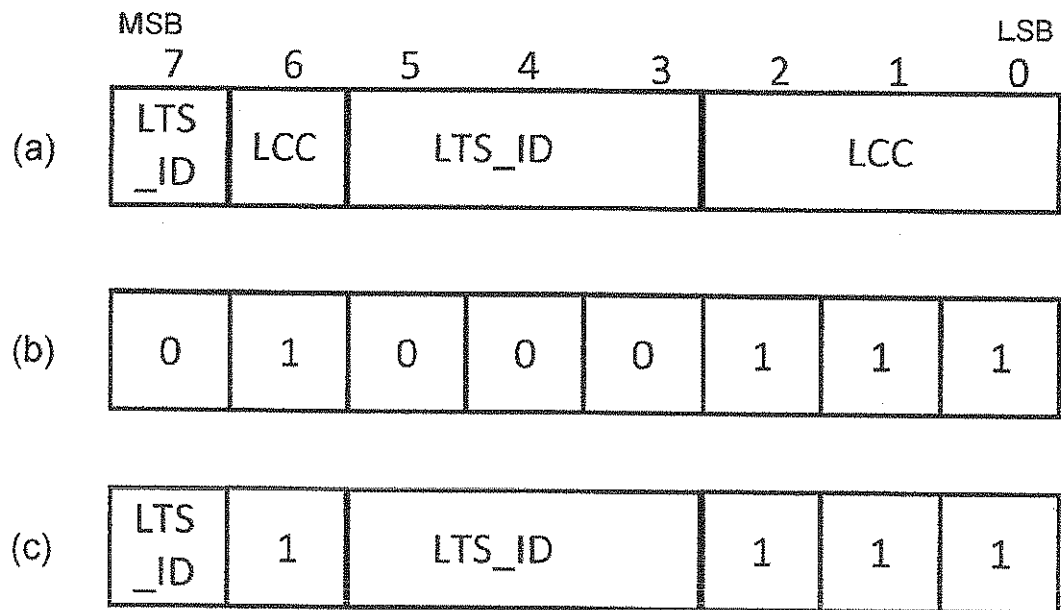
FIG. 9

FIG. 10

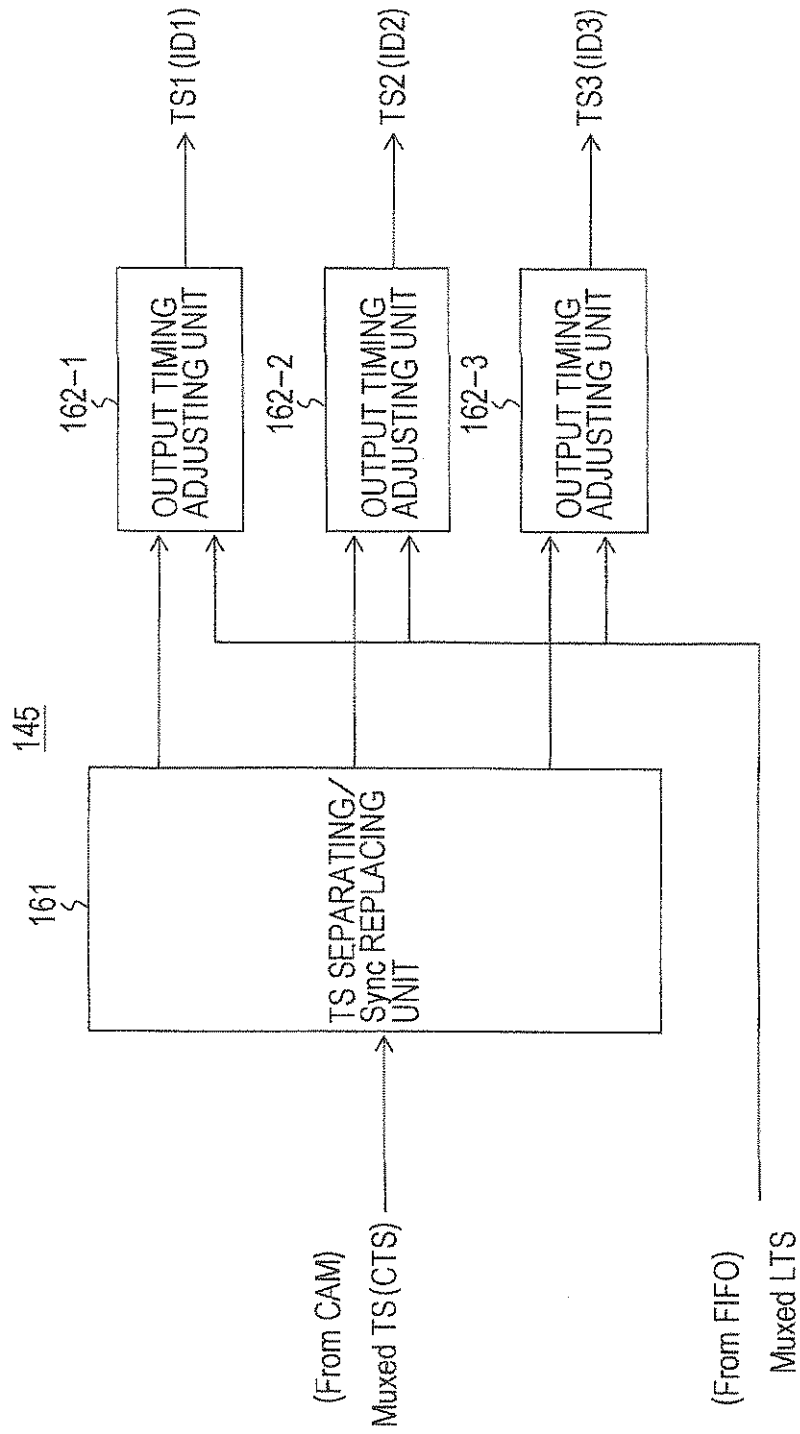


FIG. 12

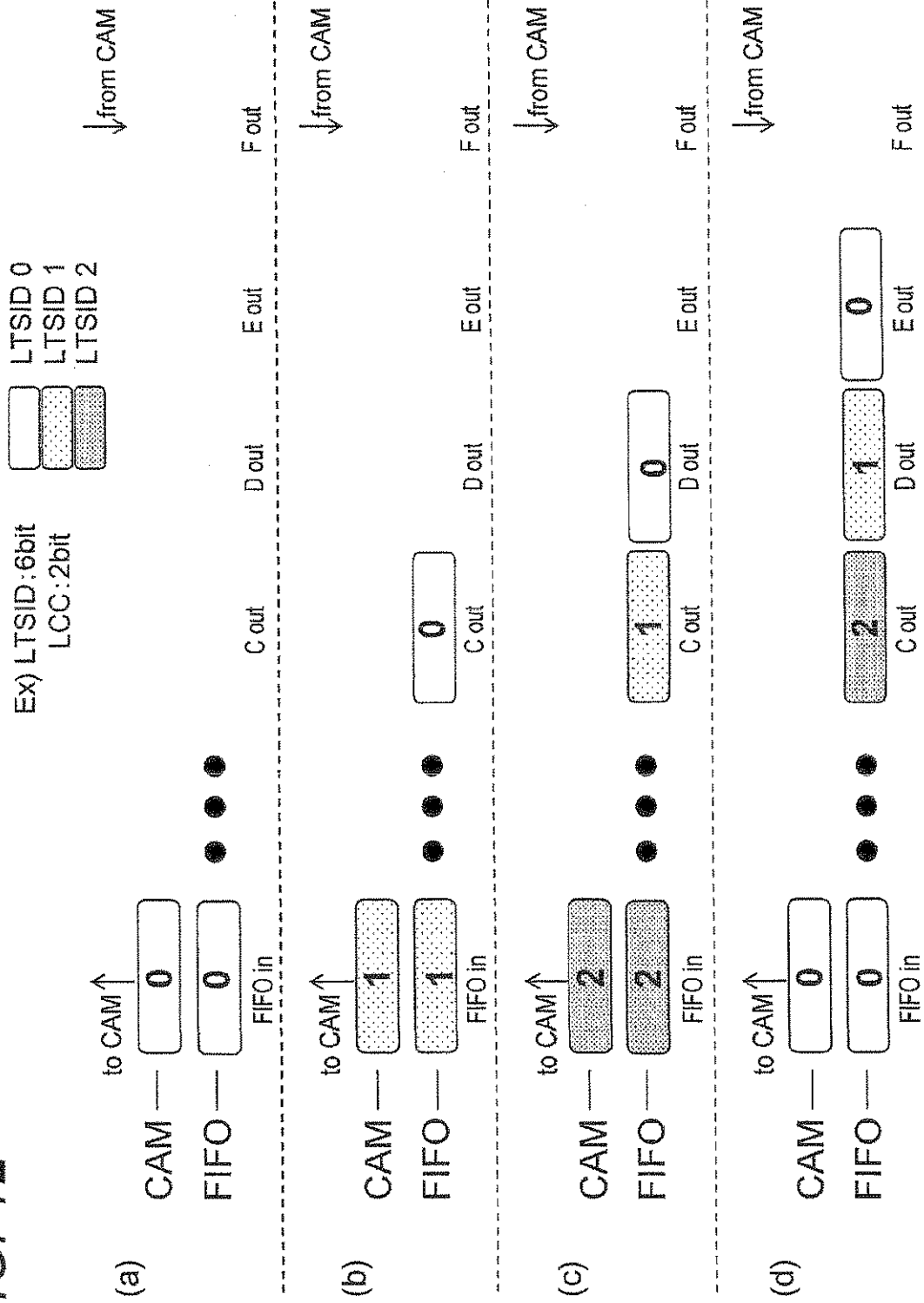


FIG. 13

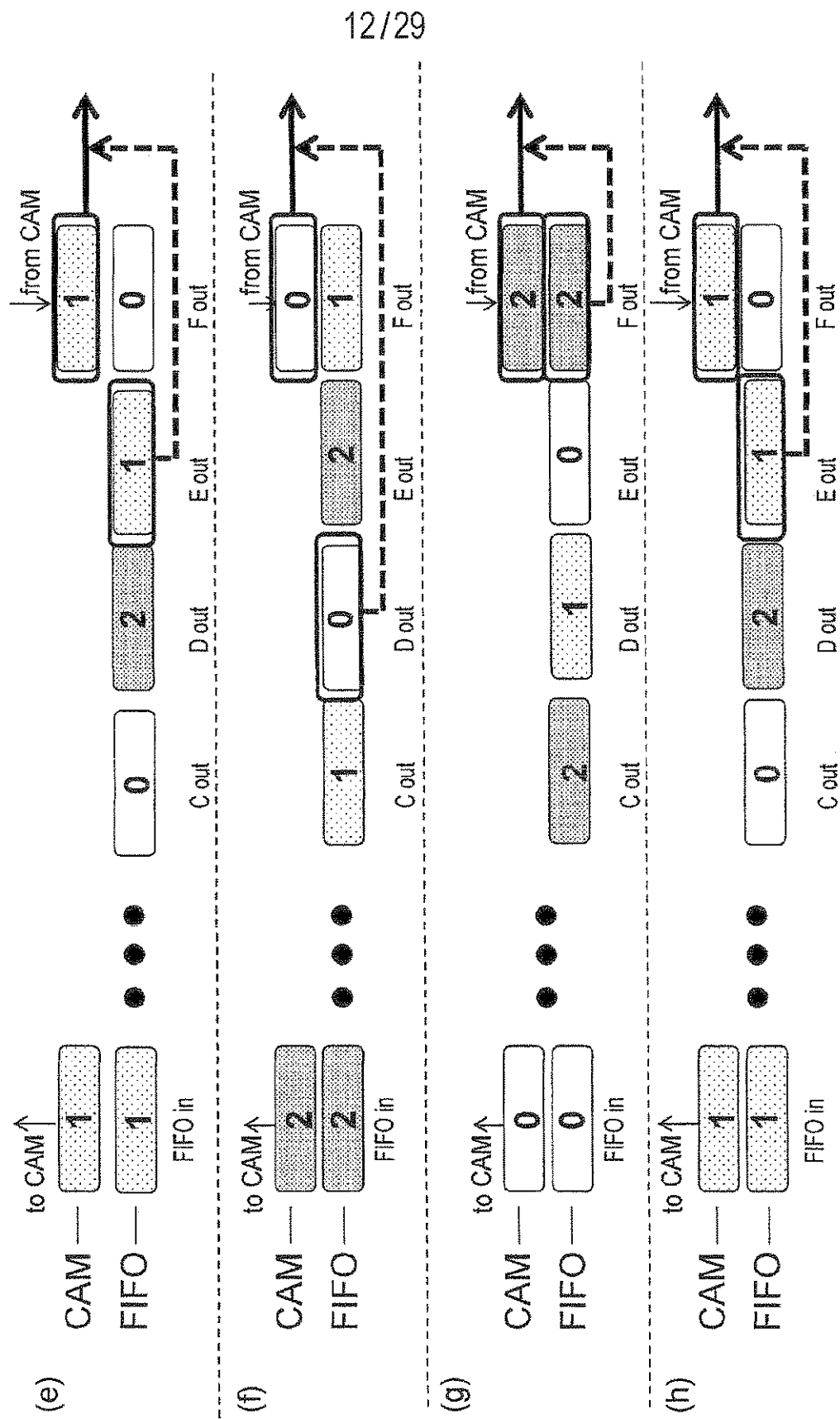
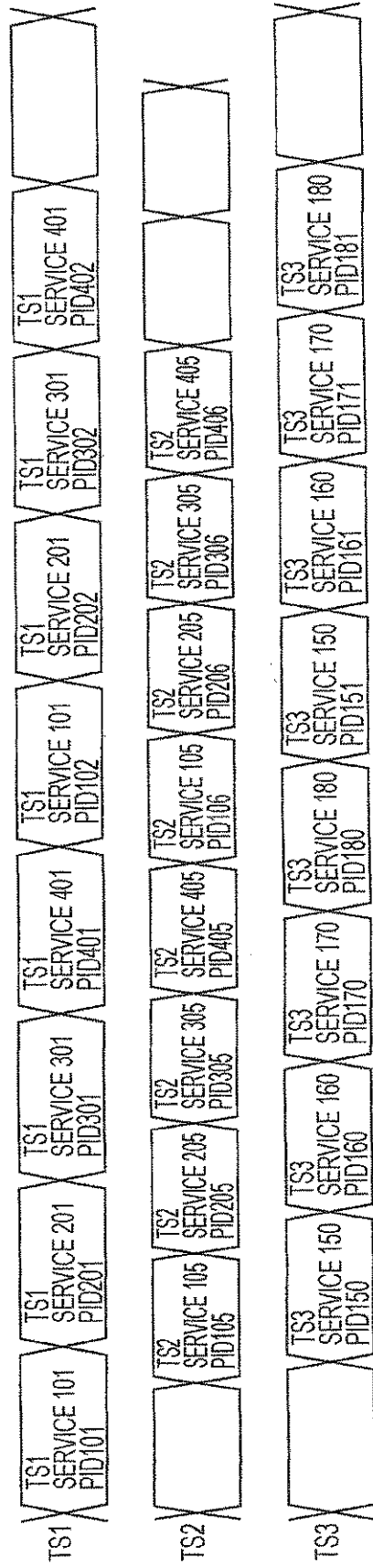


FIG. 14

(a) TS INPUT



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(b) NOT UNNECESSARY PID PACKET

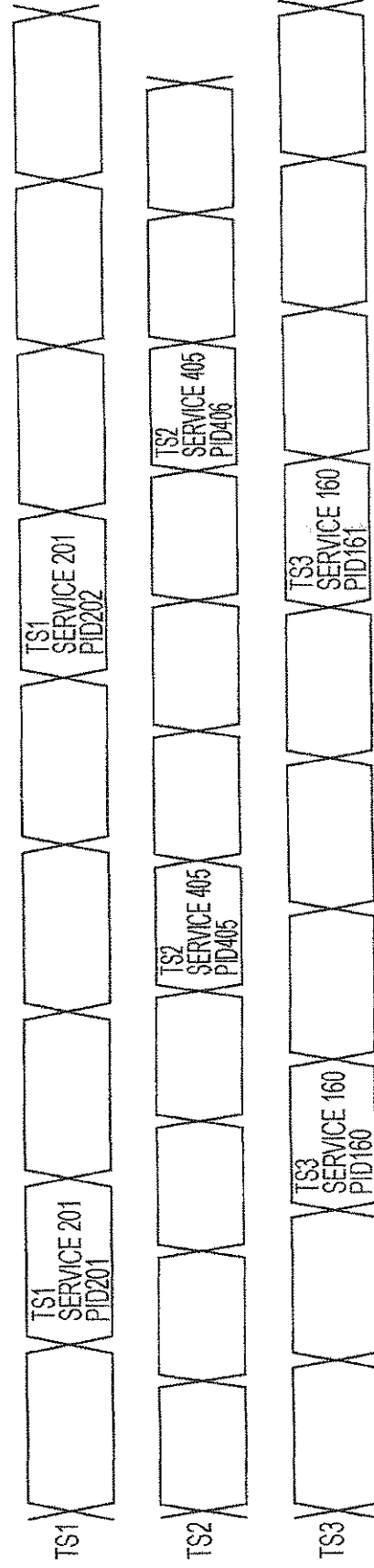


FIG. 15

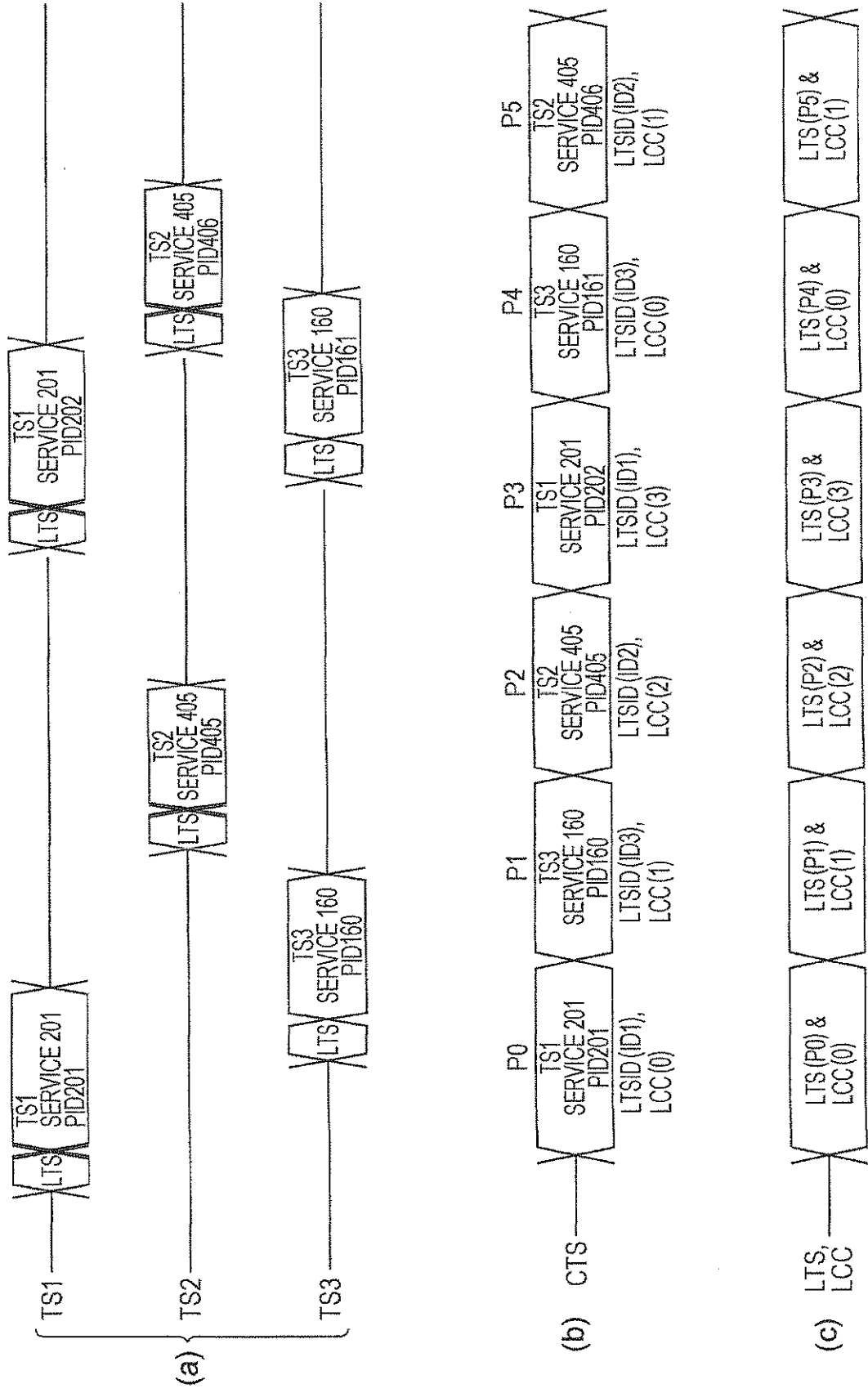


FIG. 16

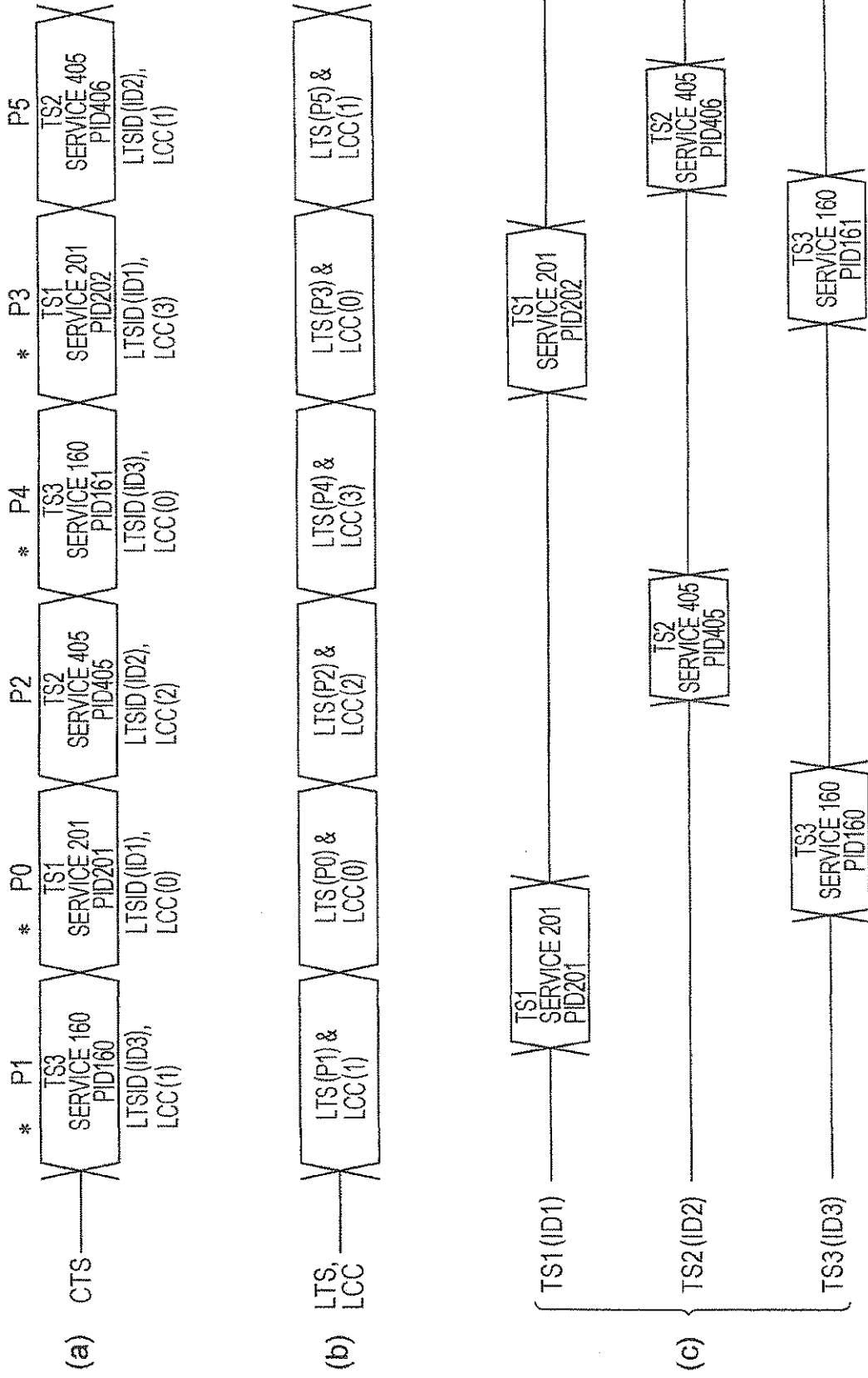


FIG. 17

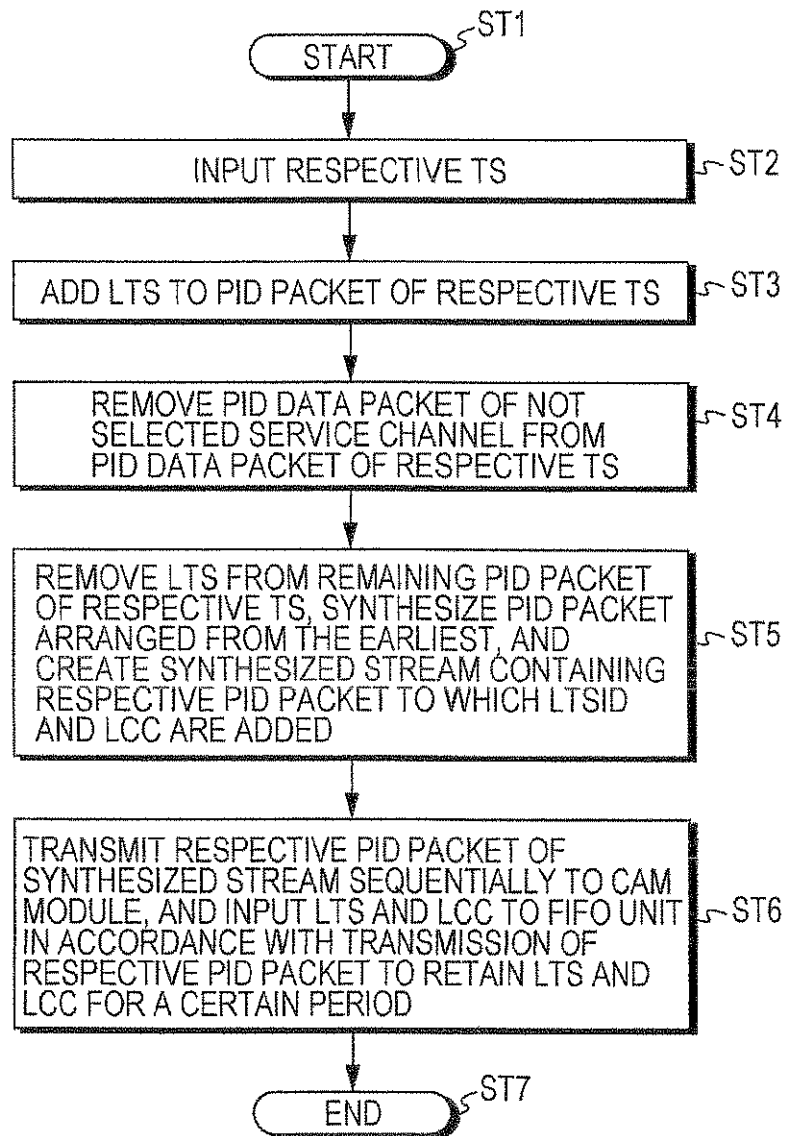


FIG. 18

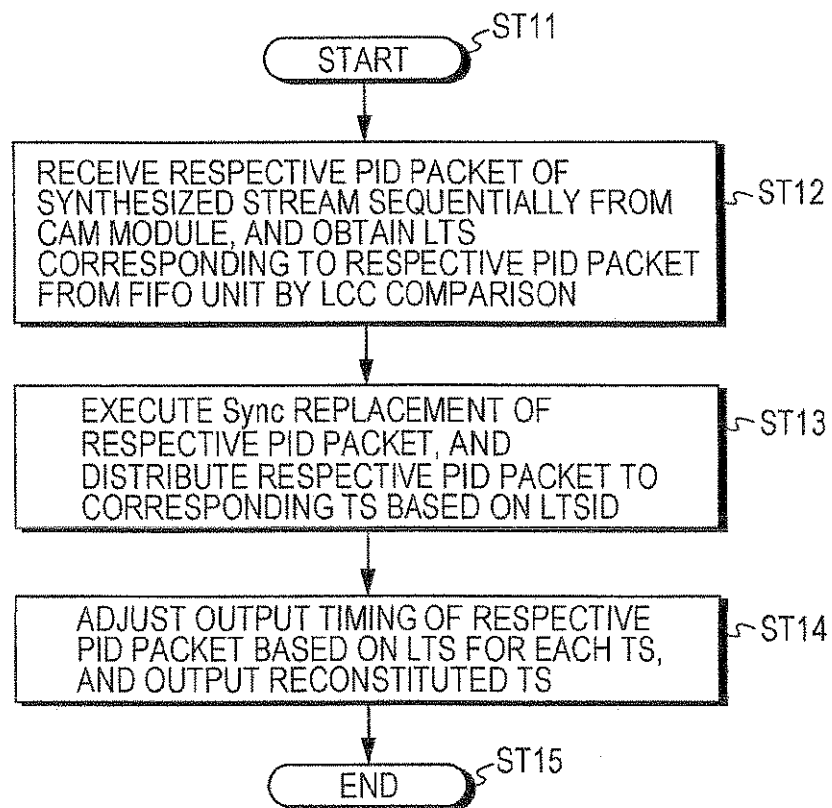


FIG. 19

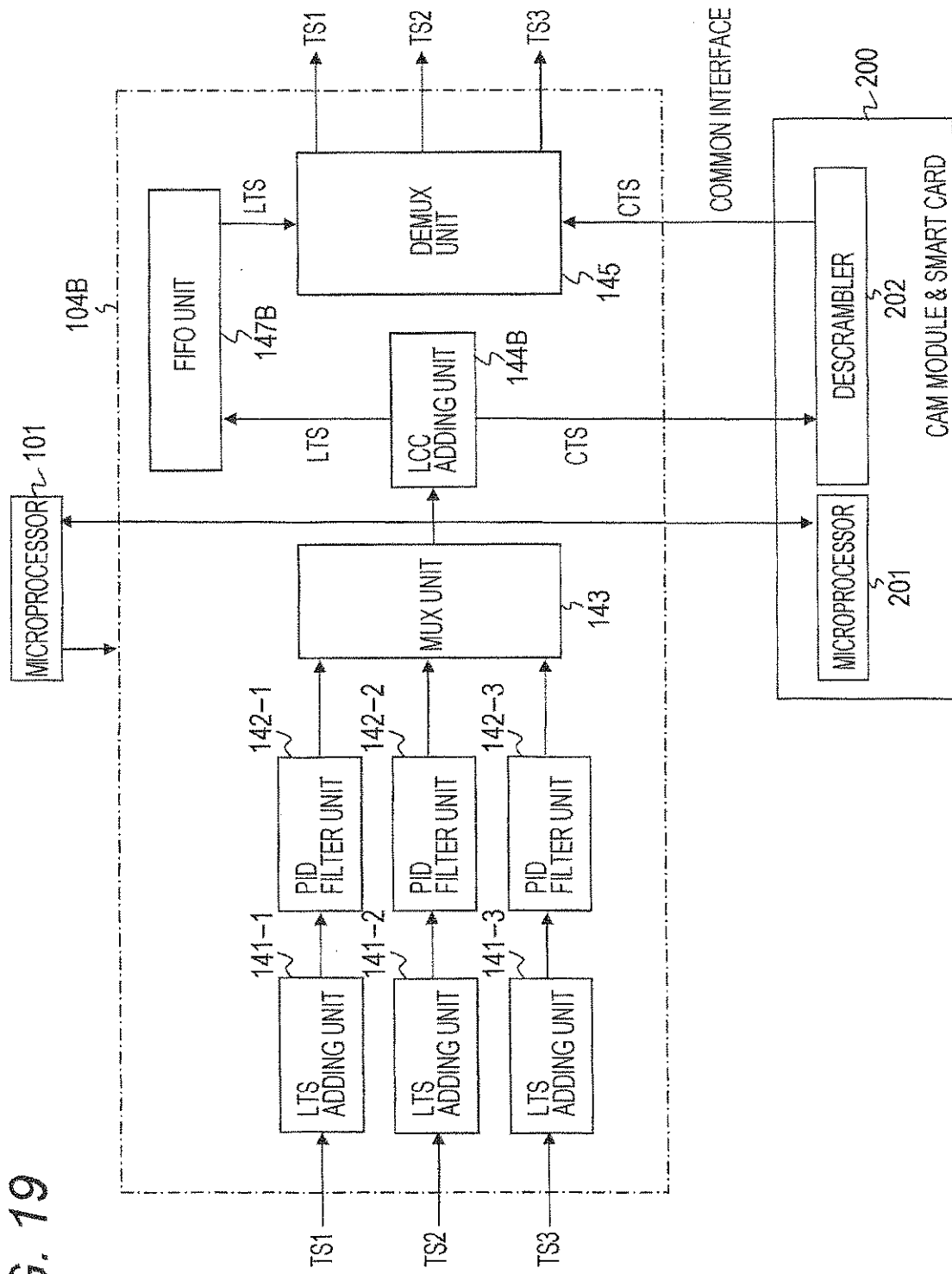


FIG. 20

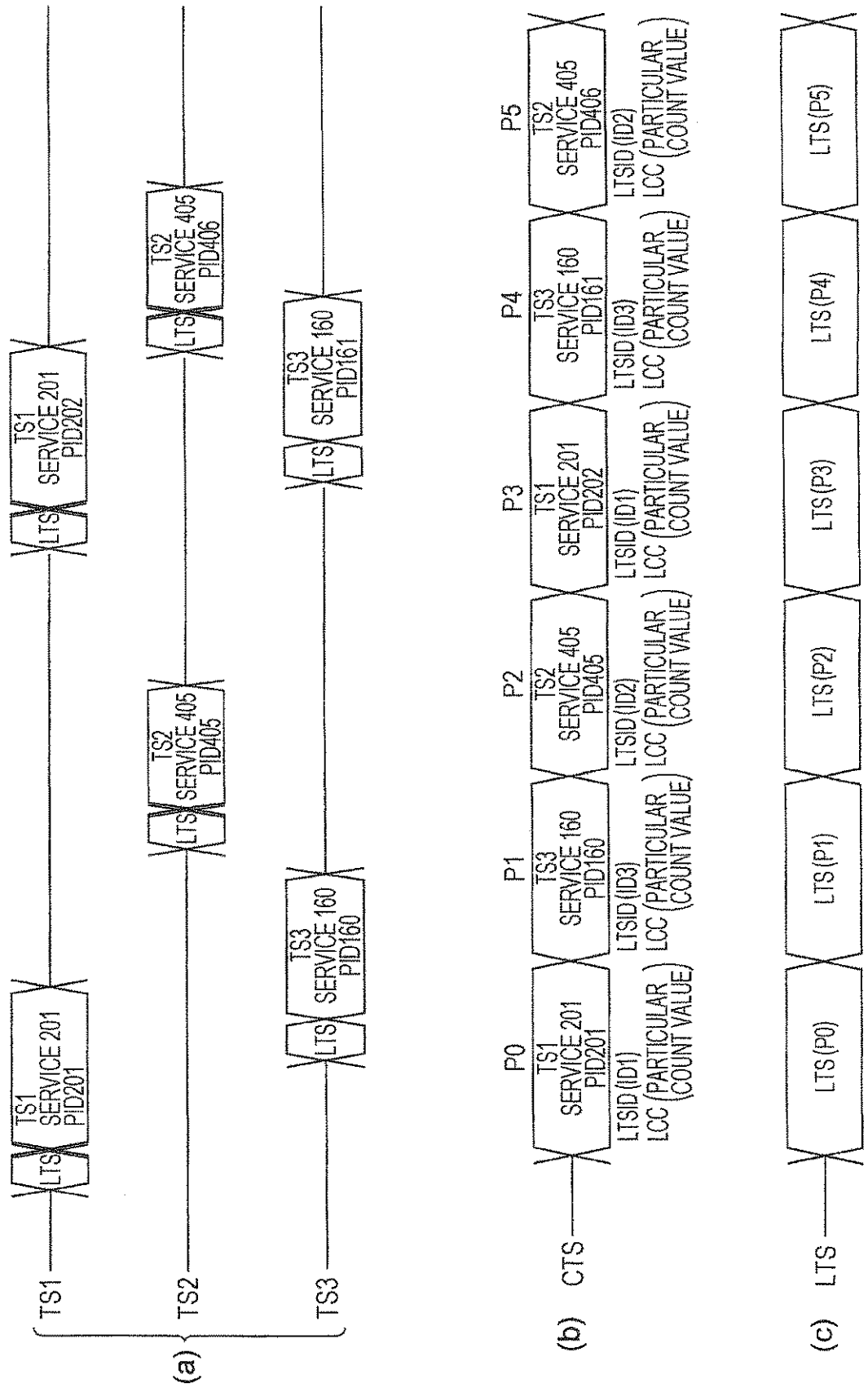


FIG. 21

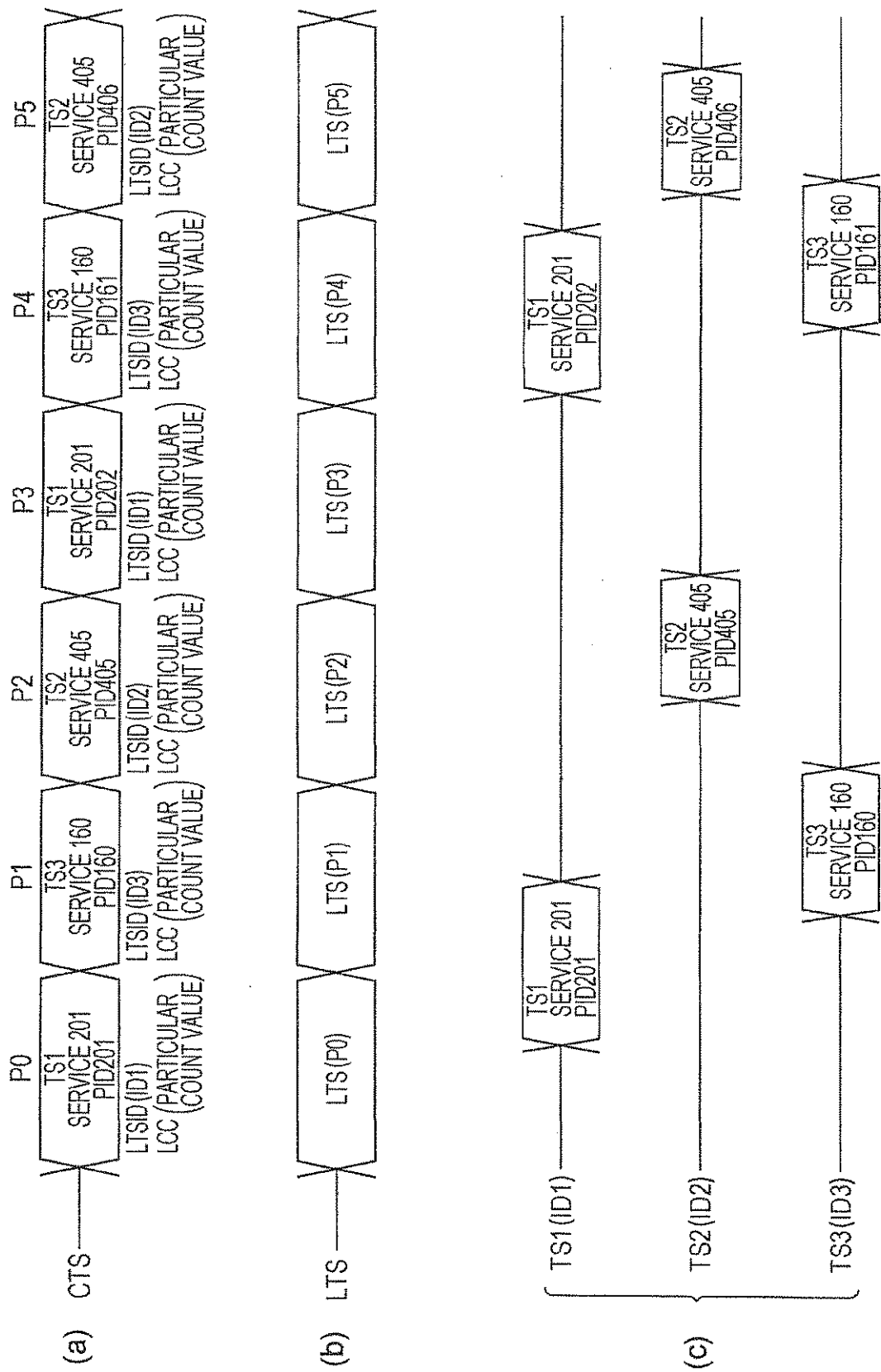


FIG. 22

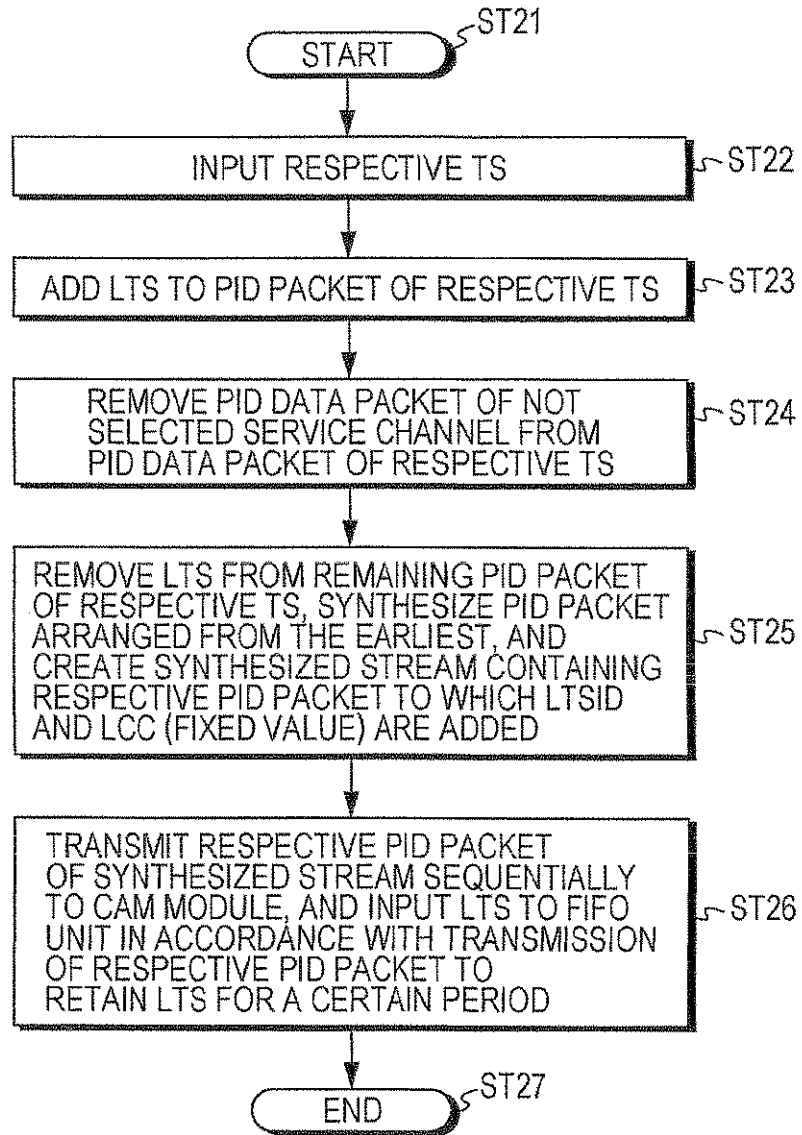


FIG. 23

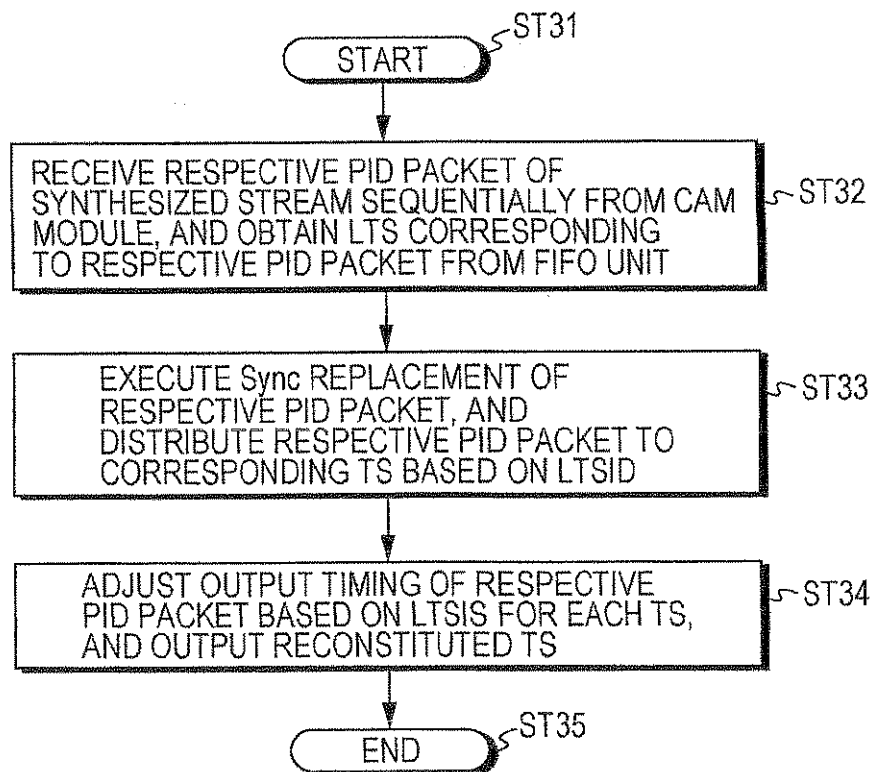


FIG. 24

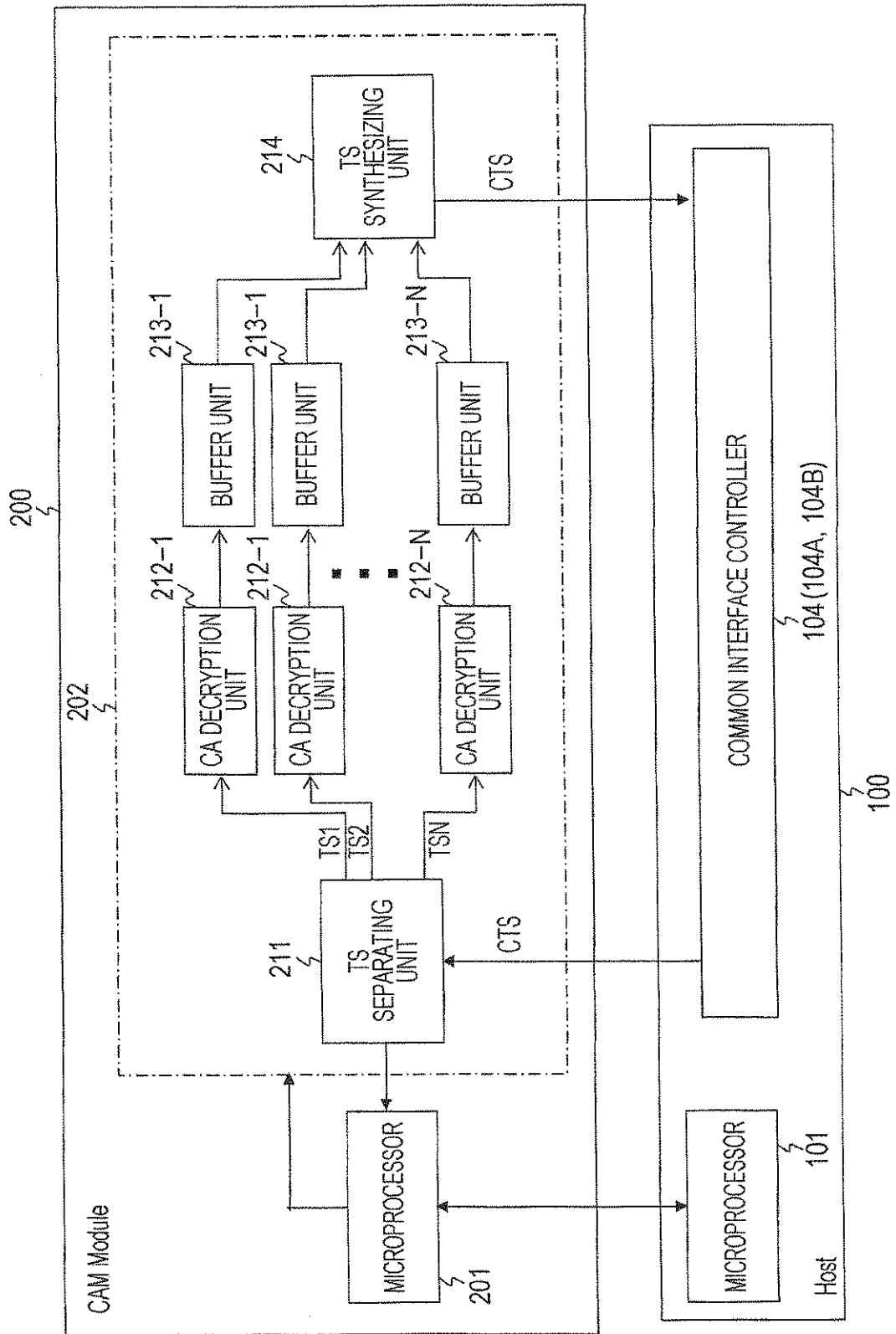


FIG. 25

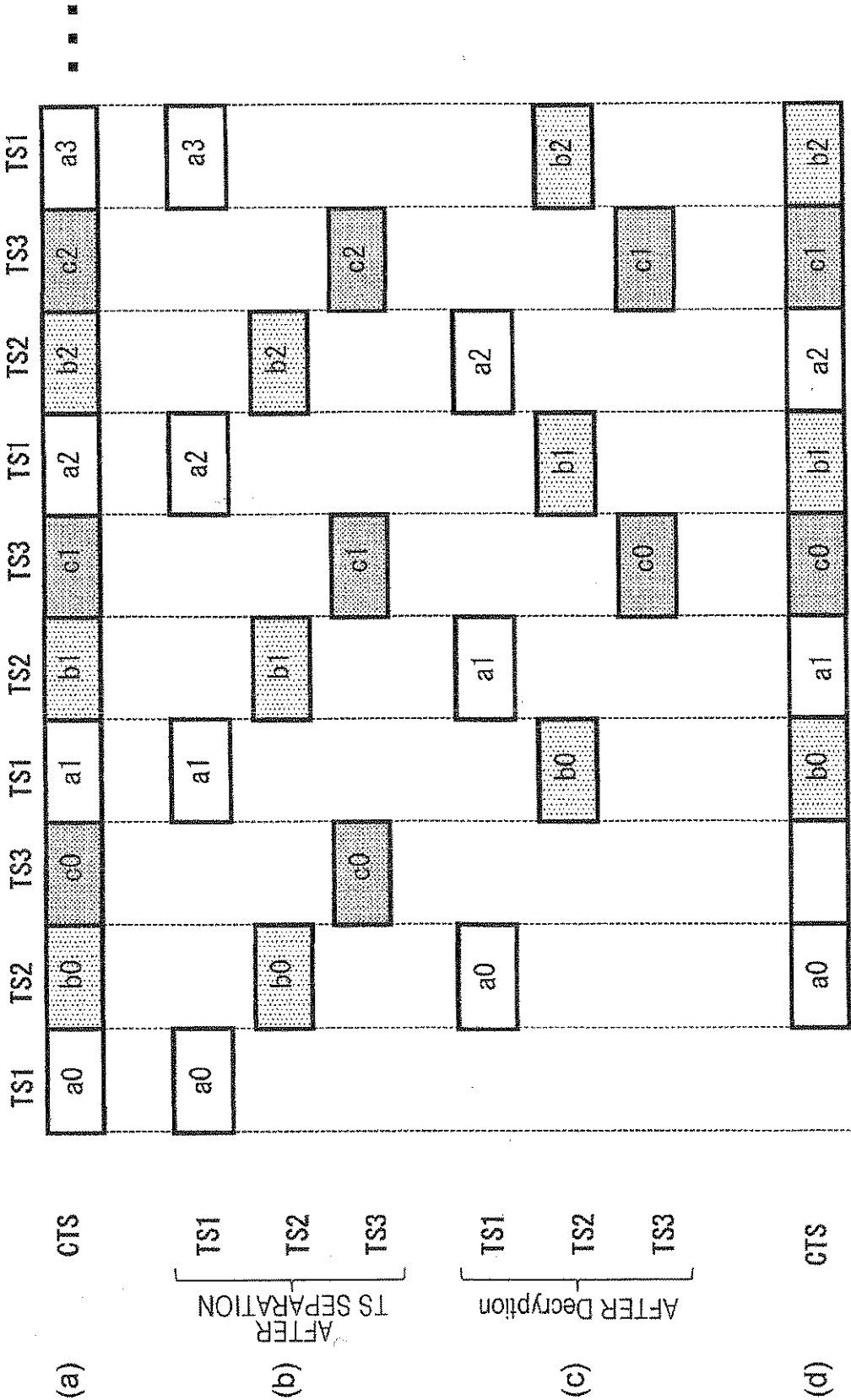


FIG. 26

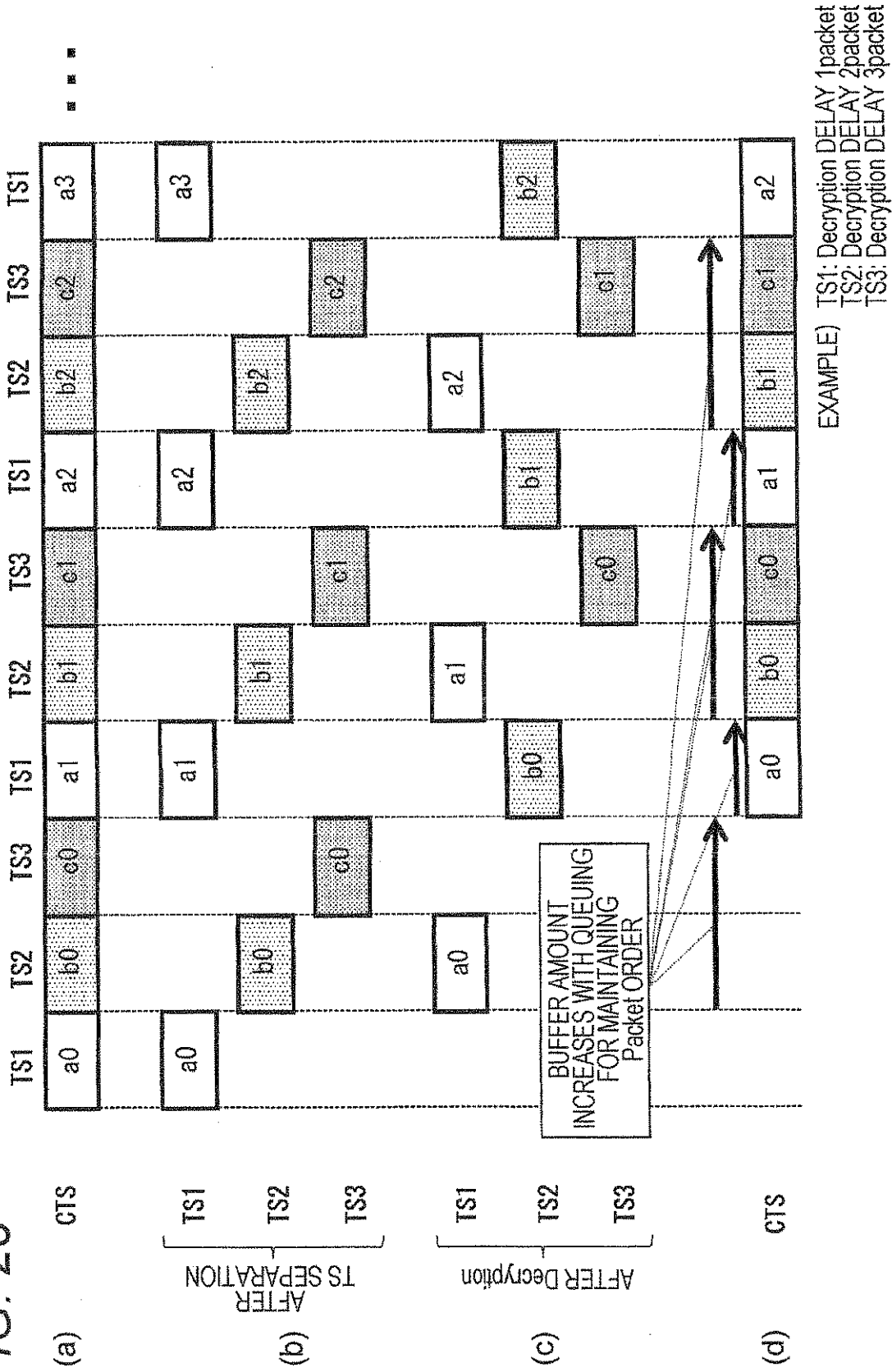


FIG. 27

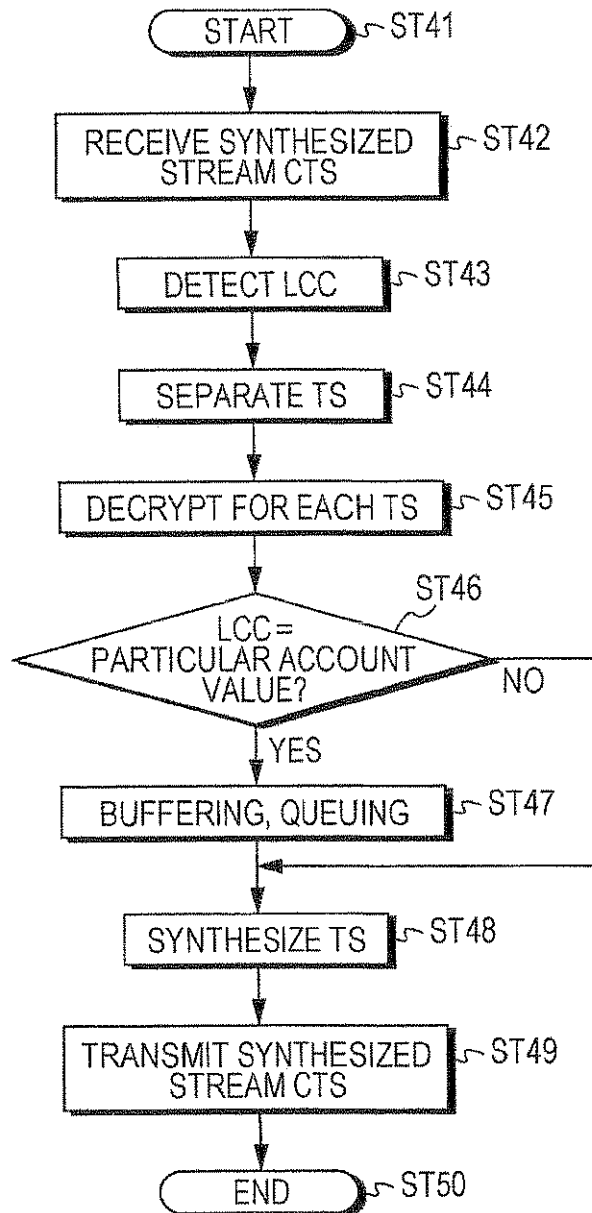


FIG. 29

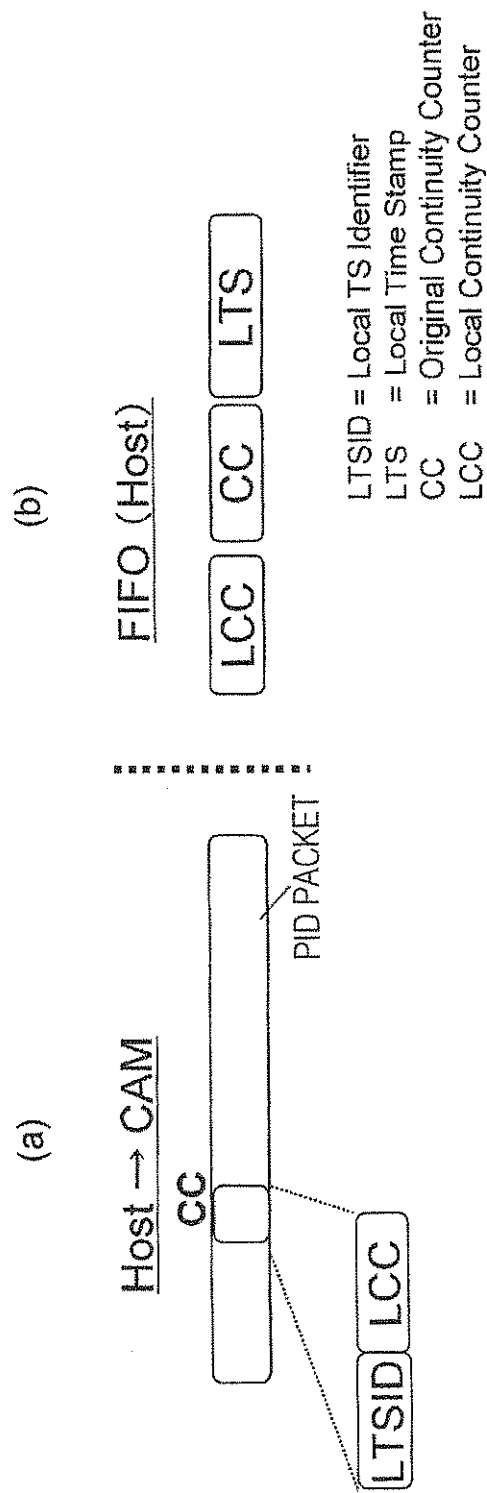


FIG. 30

	LTSID	LCC	Recover range of packet order change
1	2bit	2bit	4TS / recover less than 2 packets order change
2	1bit	3bit	2TS / recover less than 6 packets order change