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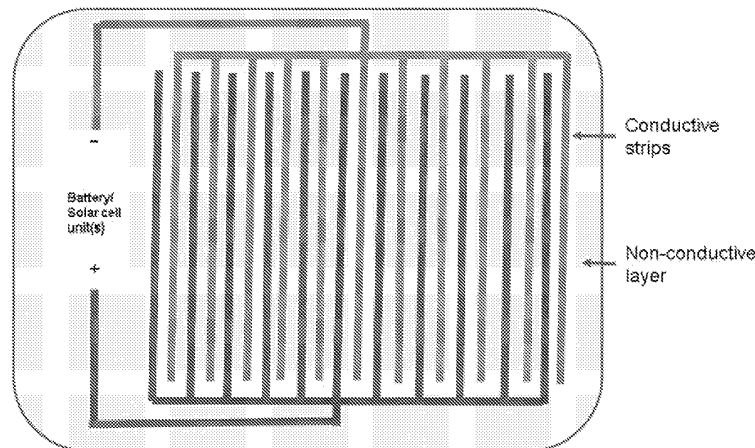


Figure 2

(57) Abstract: The present invention relates to antimicrobial and antiviral coatings. Described is an insulator substrate having an antimicrobial/antiviral layer on a surface thereof in which the antimicrobial/antiviral layer comprises a conductive material connected to a power supply. The power supply and conductive material together induce a voltage across the antimicrobial/antiviral layer such that a capacitor structure is formed on the substrate surface.

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