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(54) Title: TUNABLE, ENERGY EFFICIENT PLL CLOCK

(57) Abstract

A Phase Lock Loop circuit (110) for providing a clock signal to an integrated IC chip. The PLL comprising a voltage tunable inductive-capacitive (LC) oscillator, a phase detector, a charge pump all coupled together to form a loop. A vco includes a varactor (150), inductor (130), and capacitances (160, 170, 180, 190, 185 and 195) forming the LC oscillator.

