

VERTICAL DEFLECTION WITH VERTICAL SHRINK MODE

ABSTRACT

A differential amplifier formed by a pair of transistors (U01C, U01B) couples a vertical sawtooth signal (VRAMP1) to an input side of a vertical deflection amplifier. Nonlinearity of the transistor pair provides S-correction in the vertical direction. The current in the transistor pair varies when vertical height is adjusted, in service operation, so that the nonlinearity changes. In a vertical shrink mode of operation, the amplitude of the sawtooth signal is reduced. The nonlinearity (ratio of VRAMP1/VSAW) associated with a given level of the sawtooth signal that corresponds to a given vertical position in the vertical shrink mode is the same as that associated with the same level of the sawtooth signal in the vertical, non-shrink mode.

FIGURE 1b