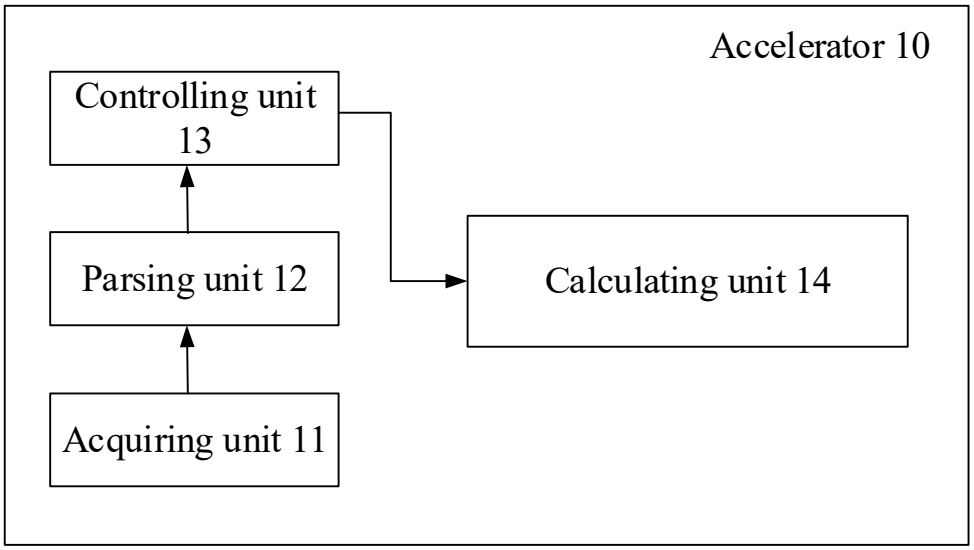

Accelerator 10
Controlling unit 13
Parsing unit 12
Acquiring unit 11
Calculating unit 14

FIG. 1

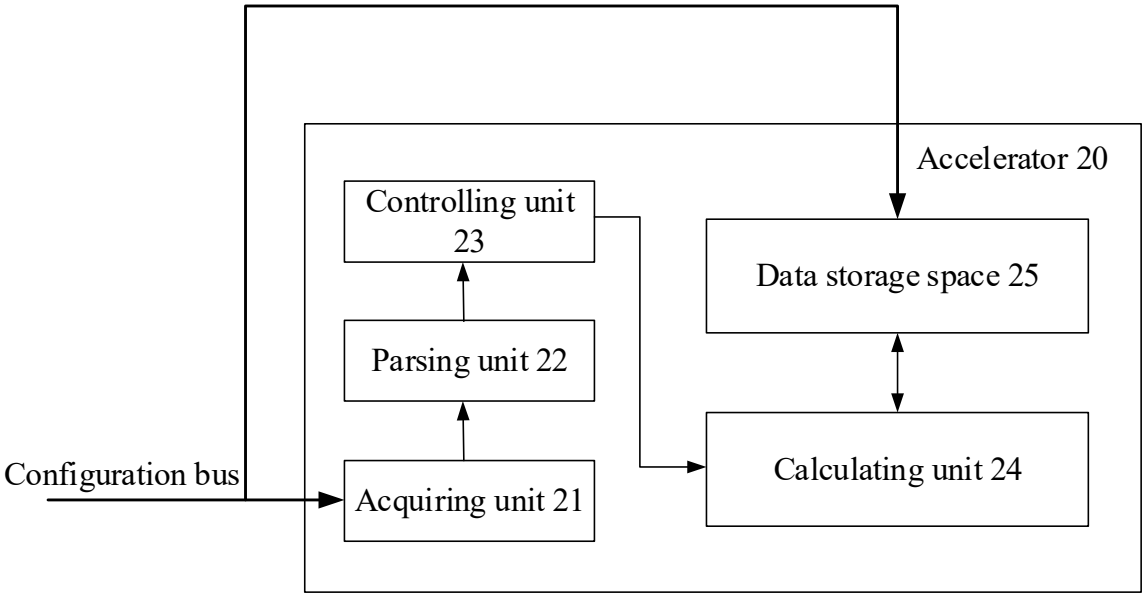

Accelerator 20
Controlling unit 23
Parsing unit 22
Acquiring unit 21
Data storage space 25
Calculating unit 24
Configuration bus

FIG. 2

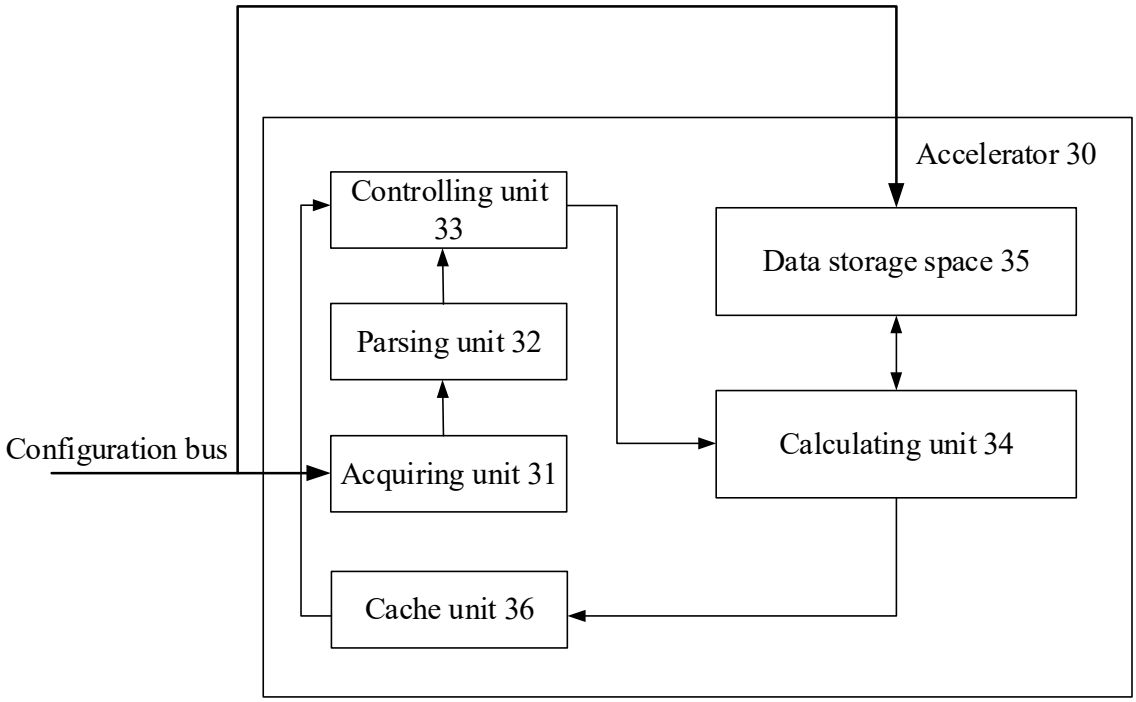

Accelerator 30
Controlling unit 33
Data storage space 35
Parsing unit 32
Configuration bus
Acquiring unit 31
Calculating unit 34
Cache unit 36

FIG. 3

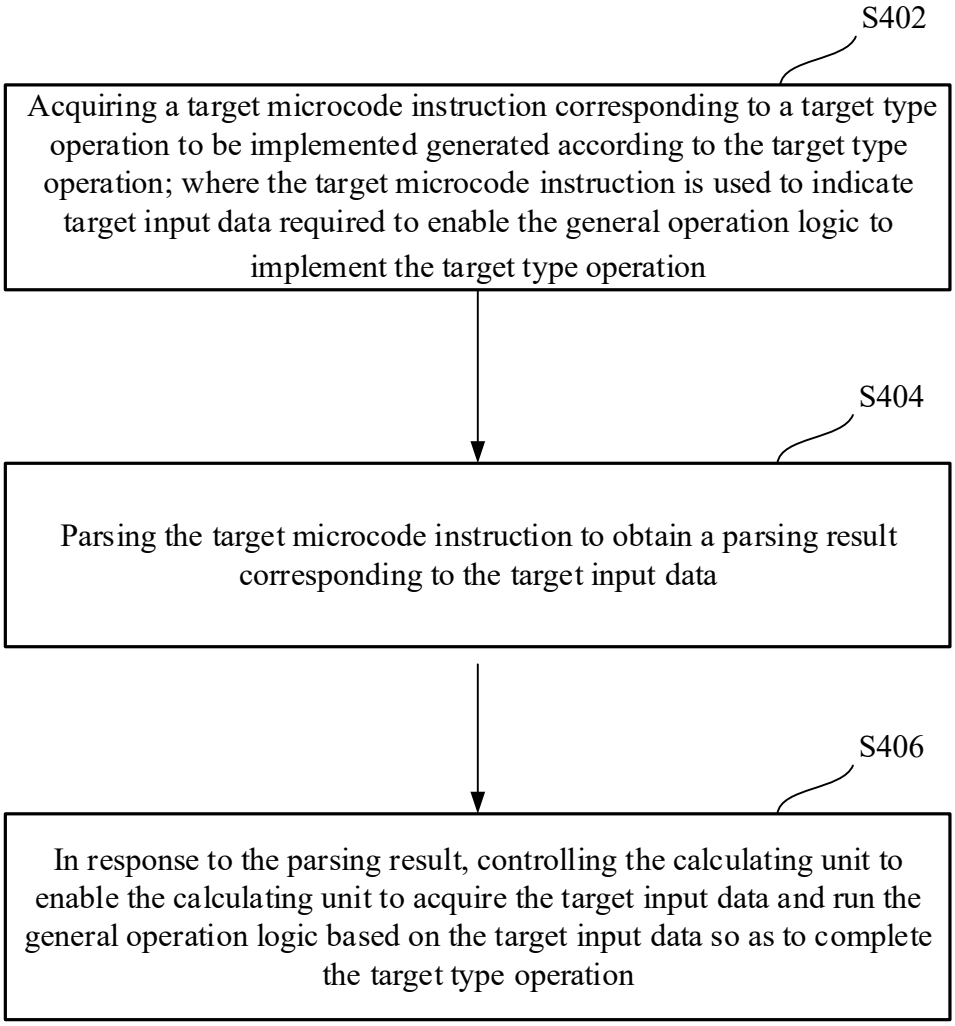

S402
Acquiring a target microcode instruction corresponding to a target type operation to be implemented generated according to the target type operation; where the target microcode instruction is used to indicate target input data required to enable the general operation logic to implement the target type operation
S404
Parsing the target microcode instruction to obtain a parsing result corresponding to the target input data
S406
In response to the parsing result, controlling the calculating unit to enable the calculating unit to acquire the target input data and run the general operation logic based on the target input data so as to complete the target type operation

FIG. 4