

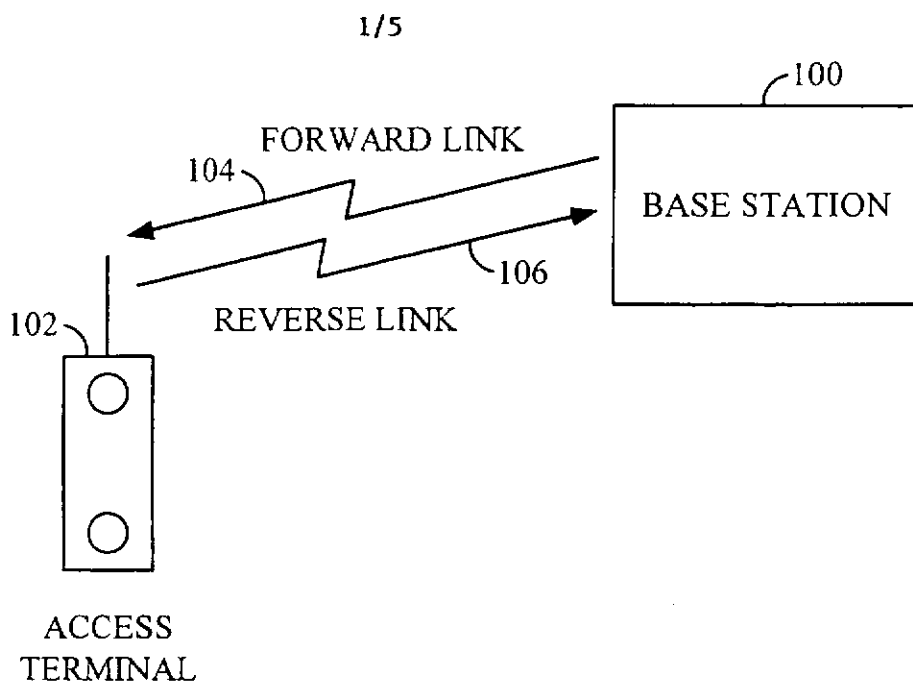
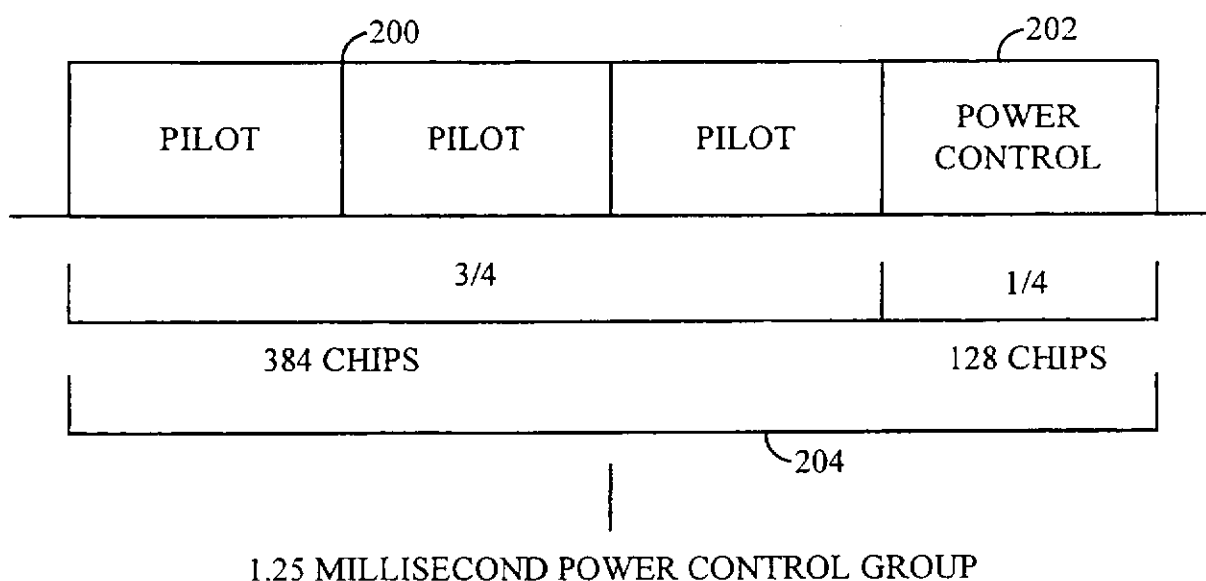
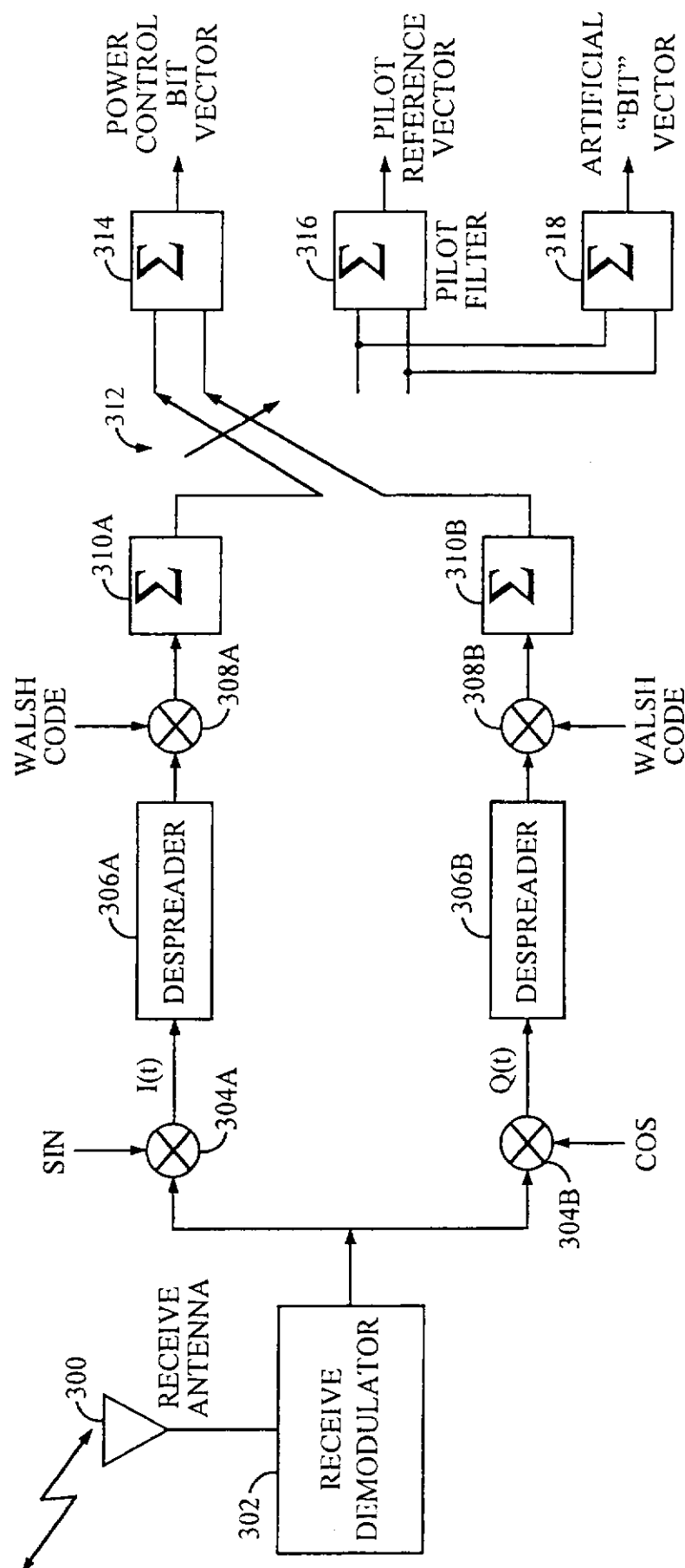


FIG. 1



REVERSE LINK POWER CONTROL SUBCHANNEL STRUCTURE

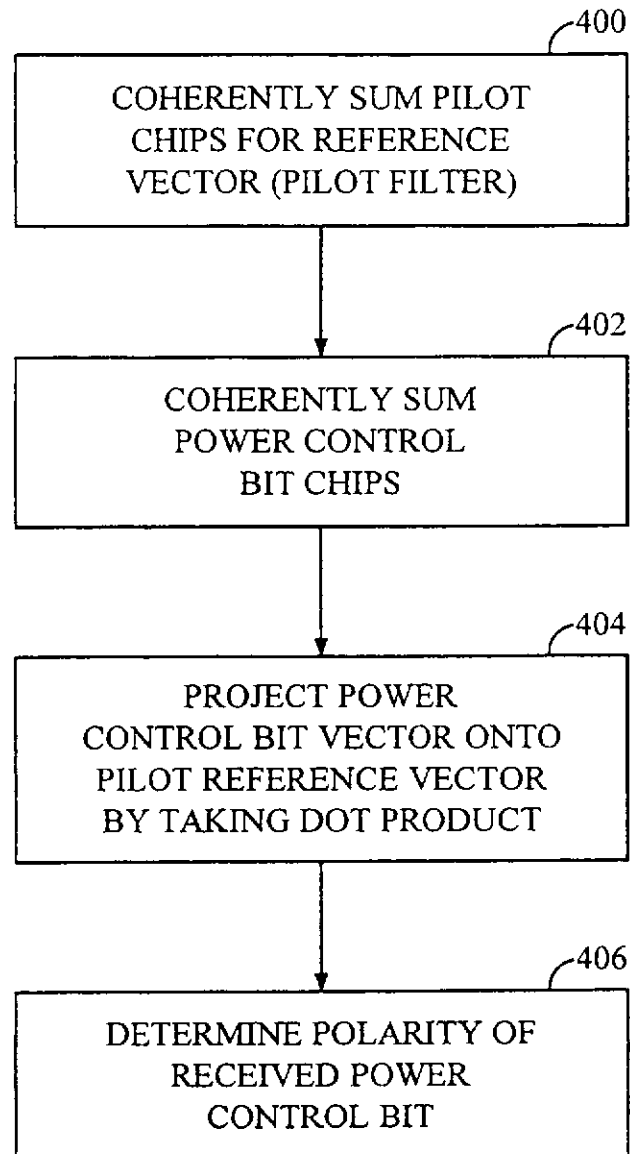
FIG. 2



CHANNEL QUALITY MONITORING APPARATUS

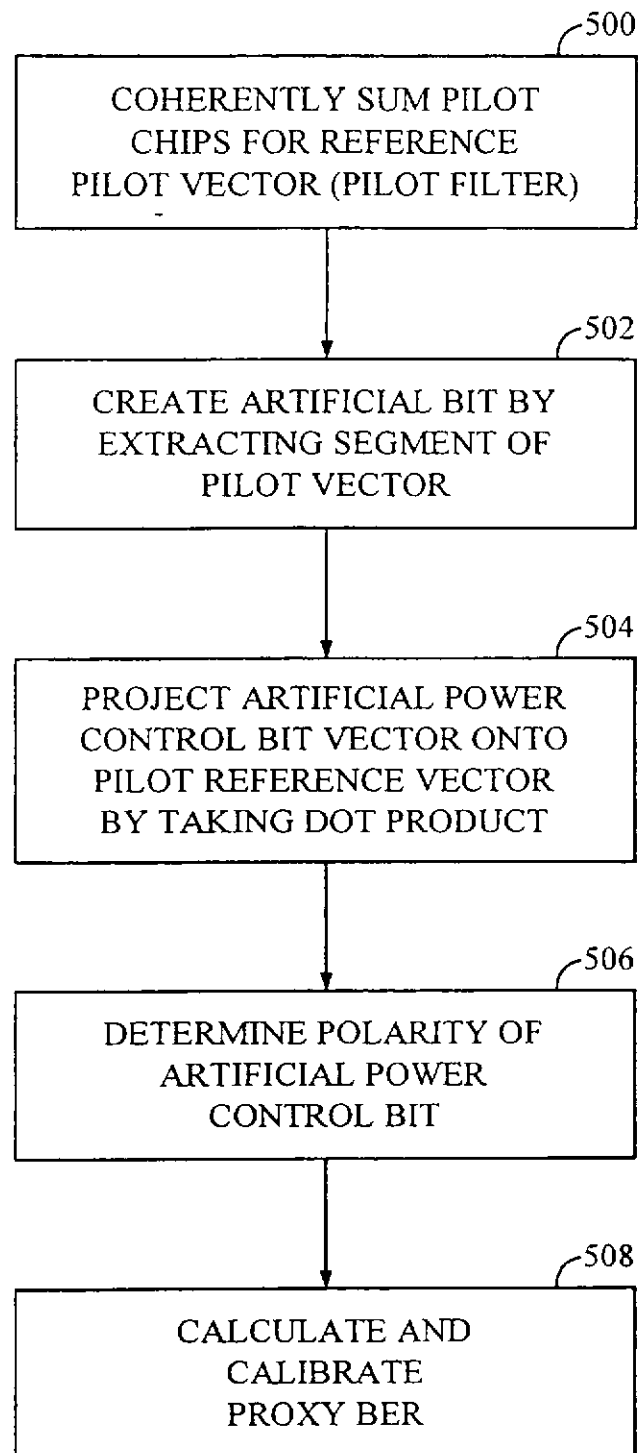
FIG. 3

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POWER CONTROL BIT POLARITY
DETERMINATION METHOD

FIG. 4



CHANNEL QUALITY MONITORING METHOD

FIG. 5

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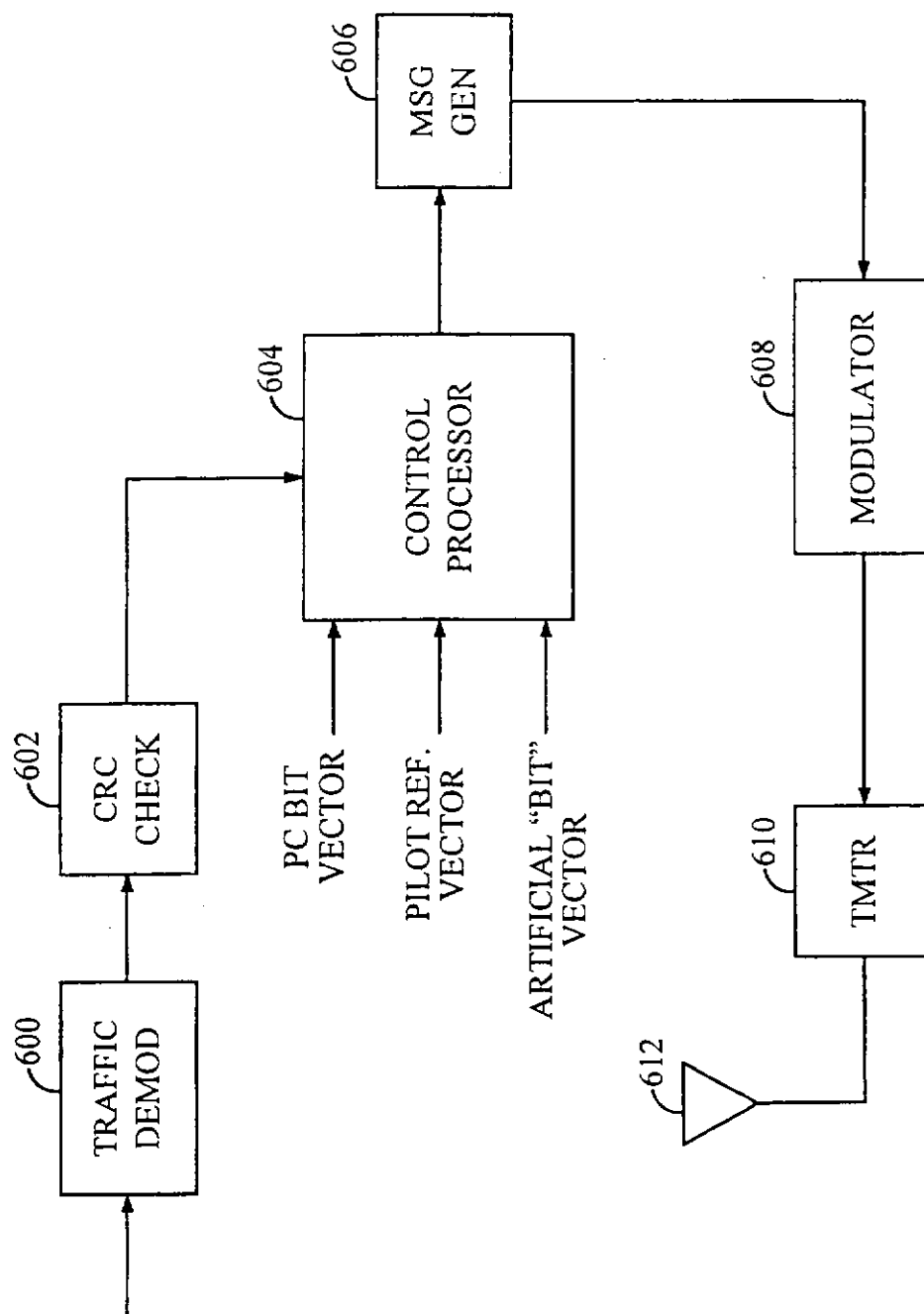


FIG. 6