

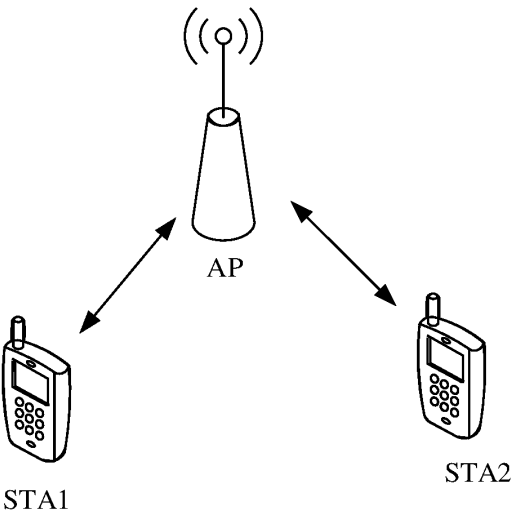


图 1

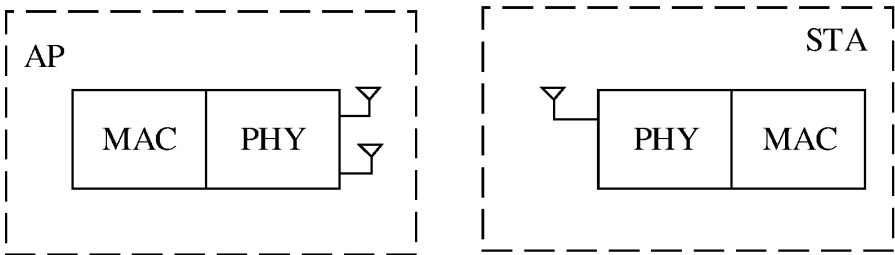


图 2

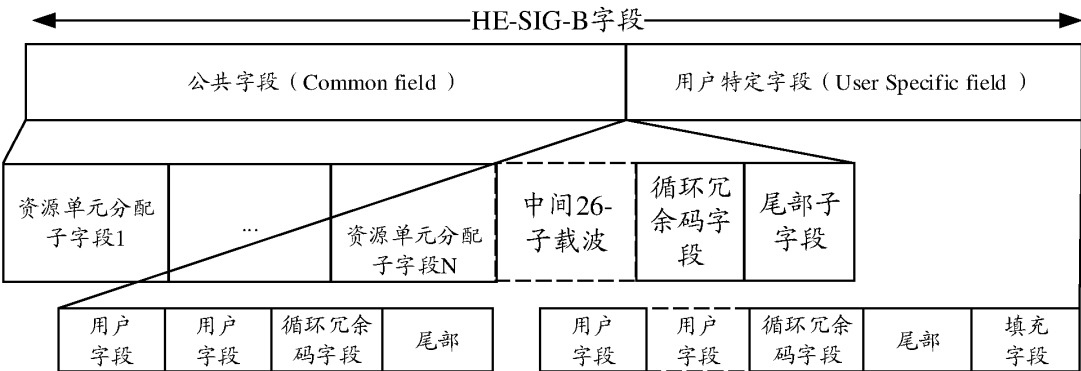


图 3



图 4

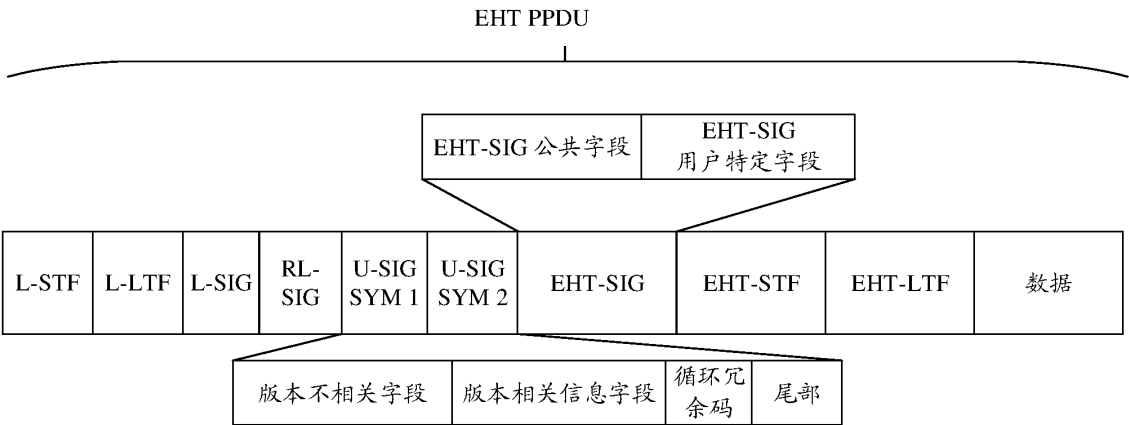
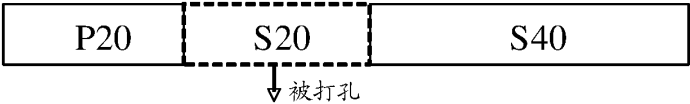


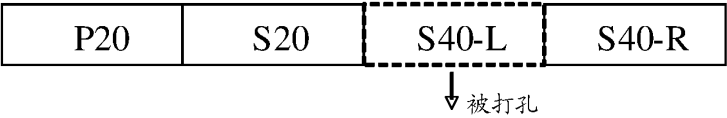
图 5

主80MHz	{	L-Preamble	RL-SIG	U-SIG1	EHT-SIG1_1
		L-Preamble	RL-SIG	U-SIG1	EHT-SIG1_2
		L-Preamble	RL-SIG	U-SIG1	EHT-SIG1_1
		L-Preamble	RL-SIG	U-SIG1	EHT-SIG1_2
第二个80MHz	{	L-Preamble	RL-SIG	U-SIG2	EHT-SIG2_1
		L-Preamble	RL-SIG	U-SIG2	EHT-SIG2_2
		L-Preamble	RL-SIG	U-SIG2	EHT-SIG2_1
		L-Preamble	RL-SIG	U-SIG2	EHT-SIG2_2
第三个80MHz	{	L-Preamble	RL-SIG	U-SIG3	EHT-SIG3_1
		L-Preamble	RL-SIG	U-SIG3	EHT-SIG3_2
		L-Preamble	RL-SIG	U-SIG3	EHT-SIG3_1
		L-Preamble	RL-SIG	U-SIG3	EHT-SIG3_2
第四个80MHz	{	L-Preamble	RL-SIG	U-SIG4	EHT-SIG4_1
		L-Preamble	RL-SIG	U-SIG4	EHT-SIG4_2
		L-Preamble	RL-SIG	U-SIG4	EHT-SIG4_1
		L-Preamble	RL-SIG	U-SIG4	EHT-SIG4_2

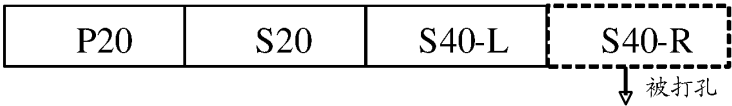
图 6



(a)



(b)



(c)

图 7

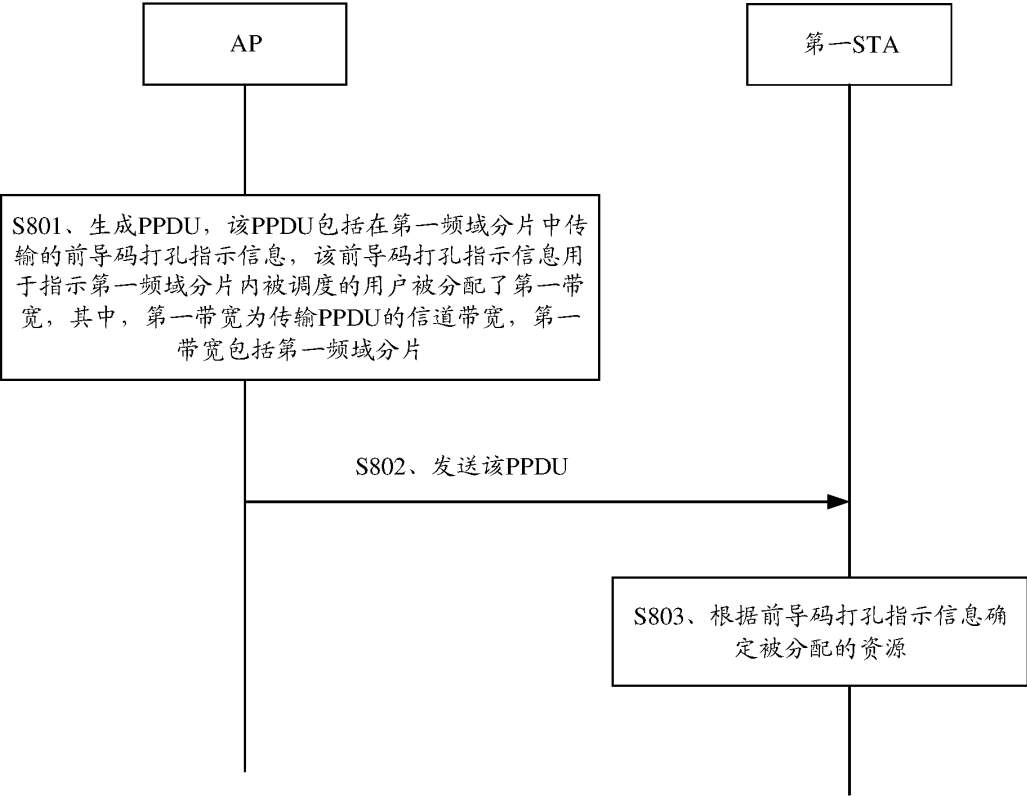


图 8

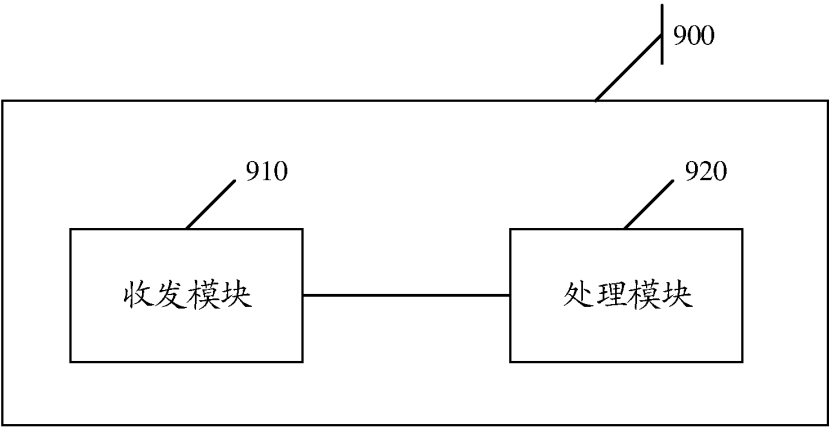


图 9

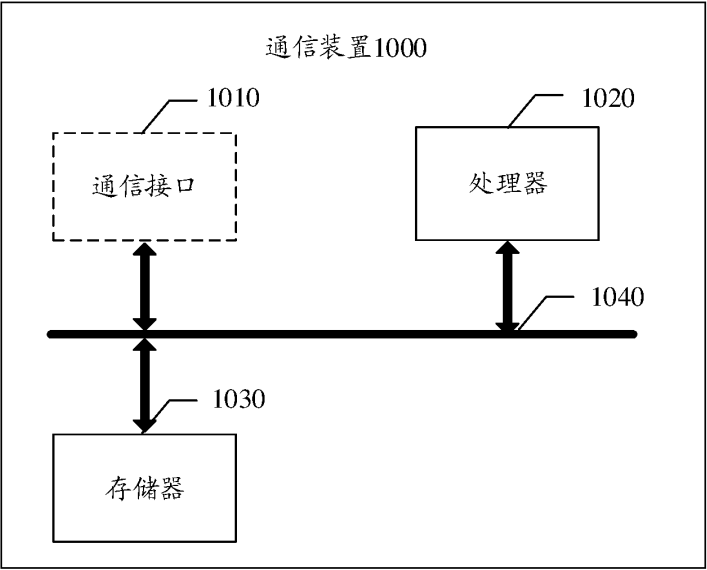


图 10