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(54) Title: PARALLEL DECISION TREE PROCESSOR ARCHITECTURE

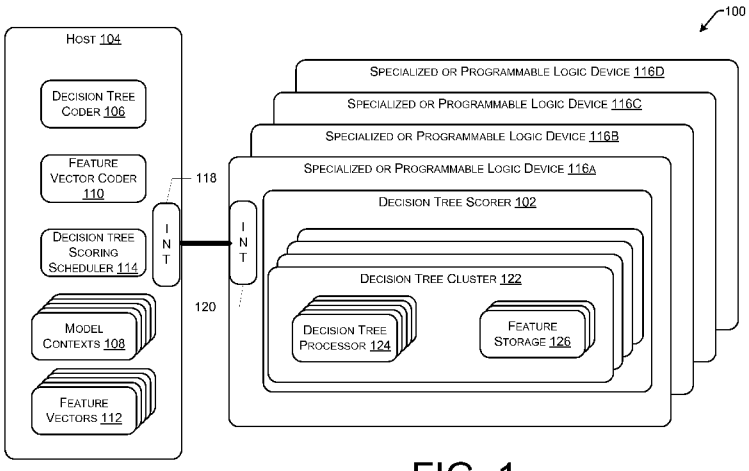

FIG. 1

(57) Abstract: Disclosed herein is a decision tree multi-processor system. The system includes a plurality of decision tree processors that access a common feature vector and execute one or more decision trees with respect to the common feature vector.

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