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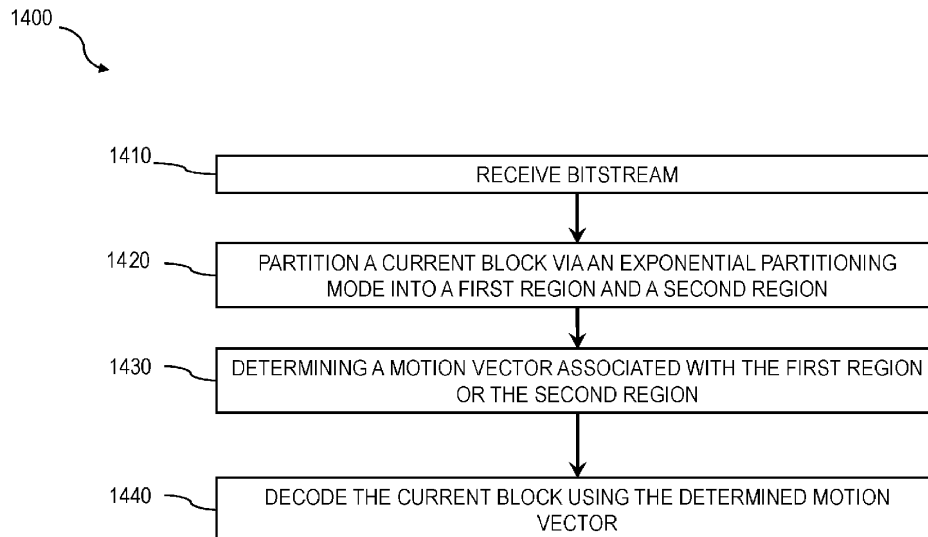


FIG. 14

(57) **Abstract:** A decoder includes circuitry configured to receive a bitstream; partition a current block via an exponential partitioning mode into a first region and a second region; determine a motion vector associated with the first region or the second region, the determining including constructing a candidate list; and decode the current block using the determined motion vector. Related apparatus, systems, techniques and articles are also described.

[Continued on next page]

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