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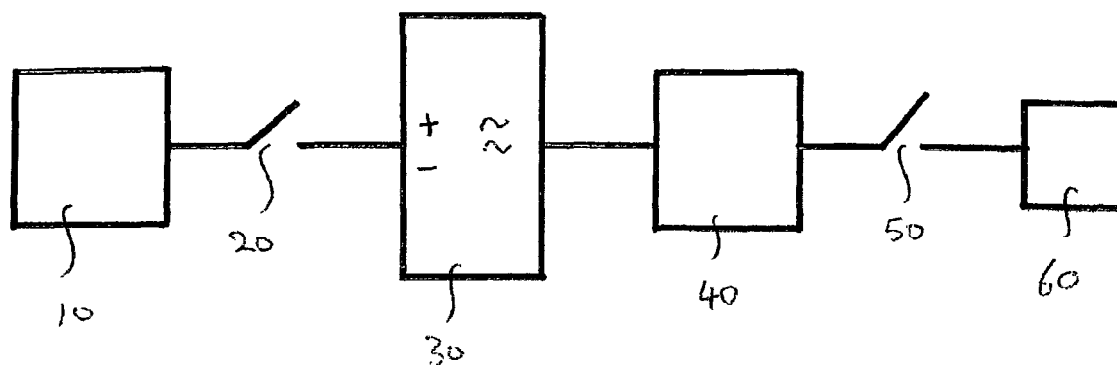
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(54) Title: IMPROVED DIMMER CIRCUIT ARRANGEMENT



(57) Abstract: A dimmer circuit arrangement is disclosed including a second control circuit (40) for controlling the operation of a triac (50) for delivering current to a load (60), and a first control circuit (10) for controlling the operation of an IGBT power semiconductor switch (20) for controlling the rate of rise of load voltage. The first control circuit also controls the operation of the second control circuit.



WO 03/081961 A1