

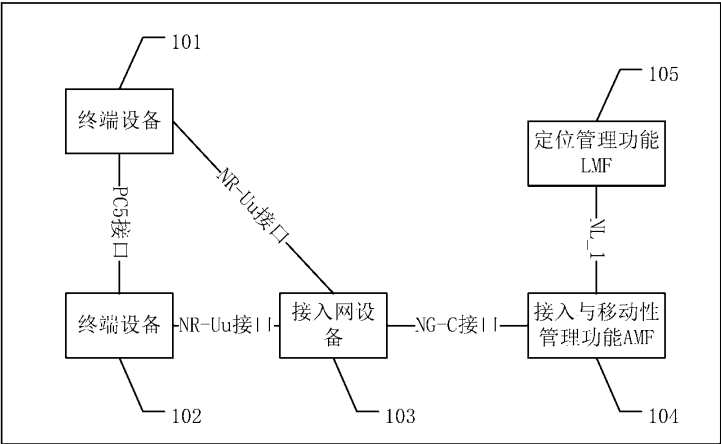


图 1

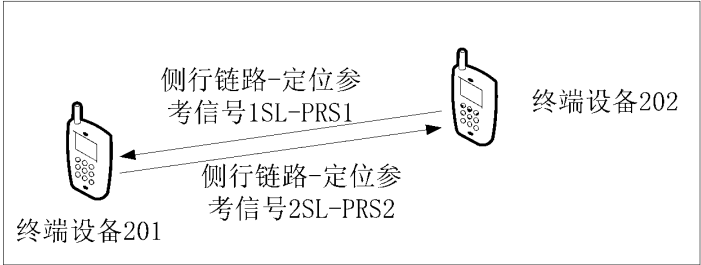


图 2

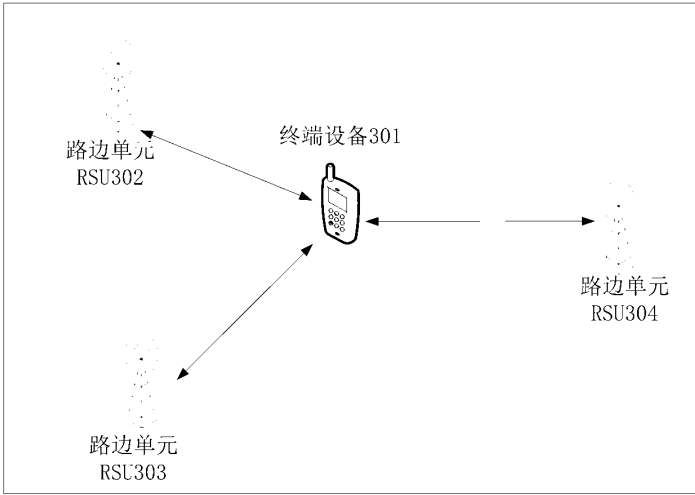


图 3

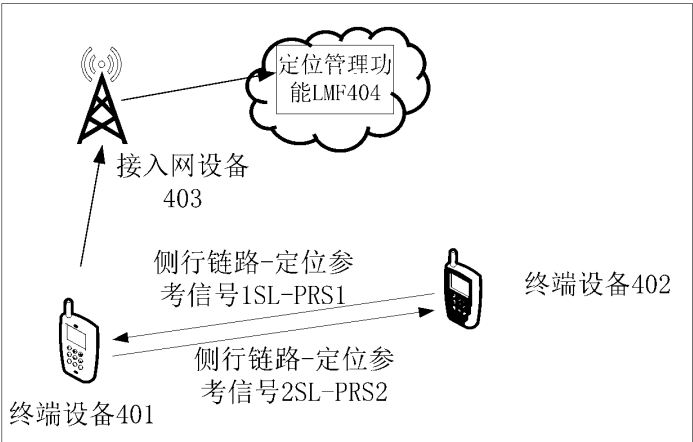


图 4

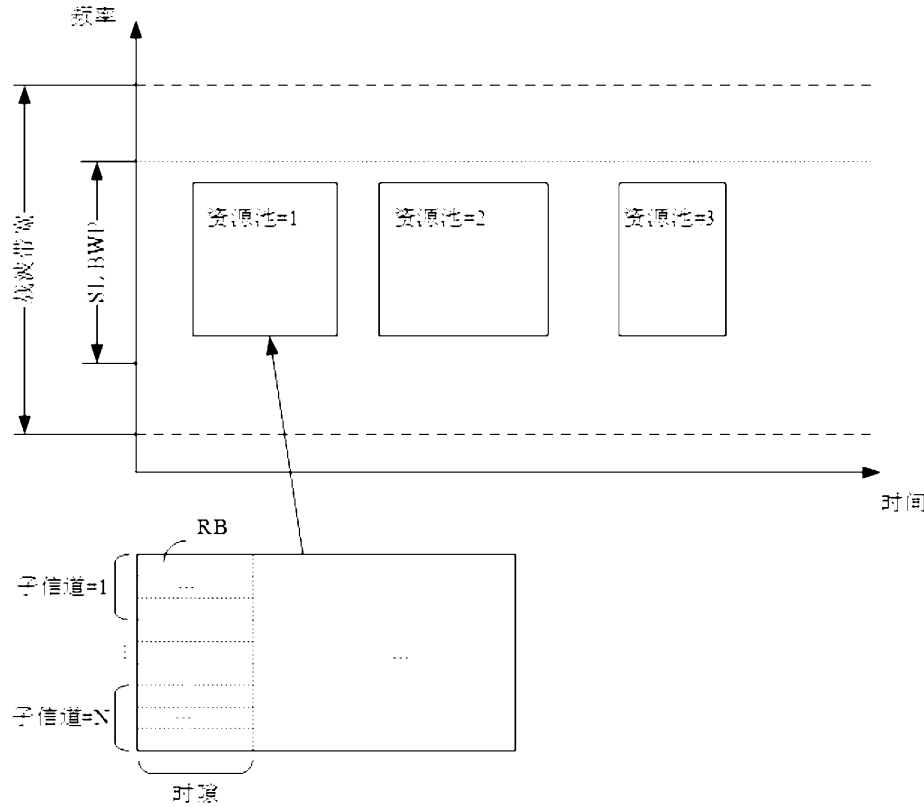


图 5A

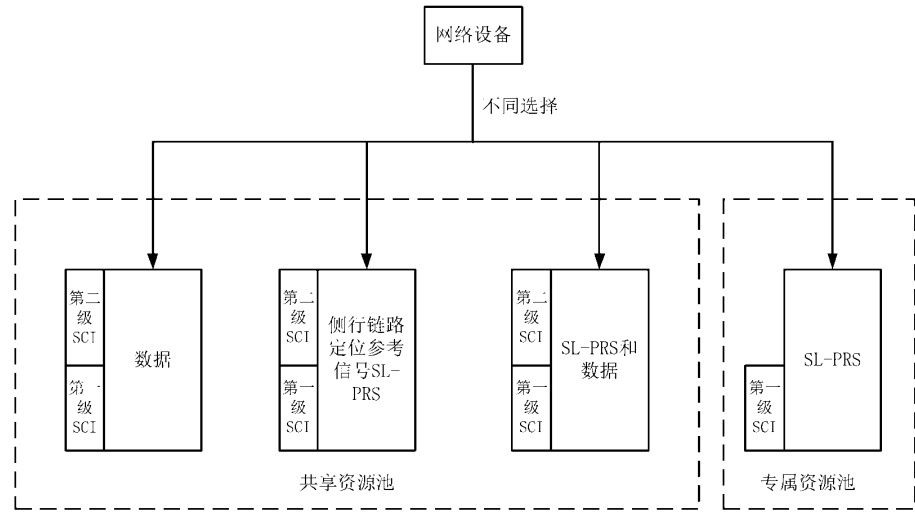


图 5B

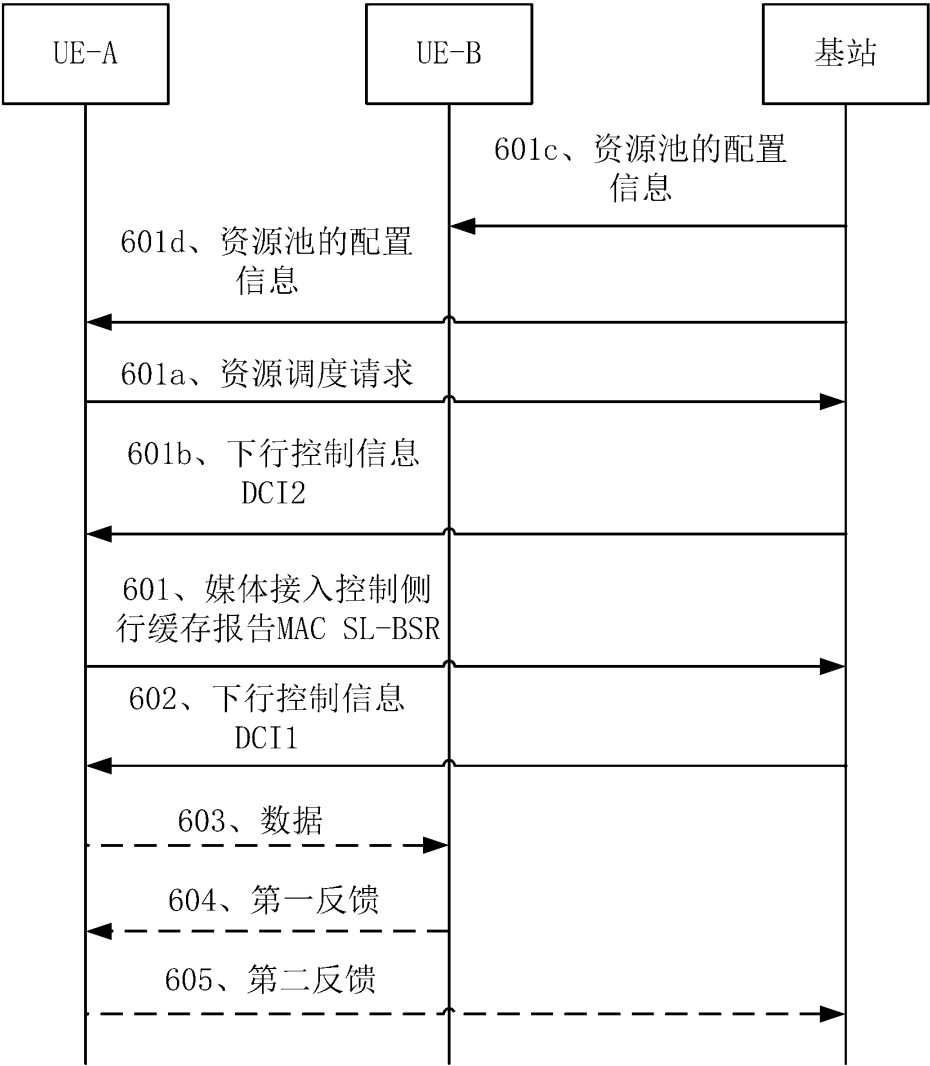


图 6

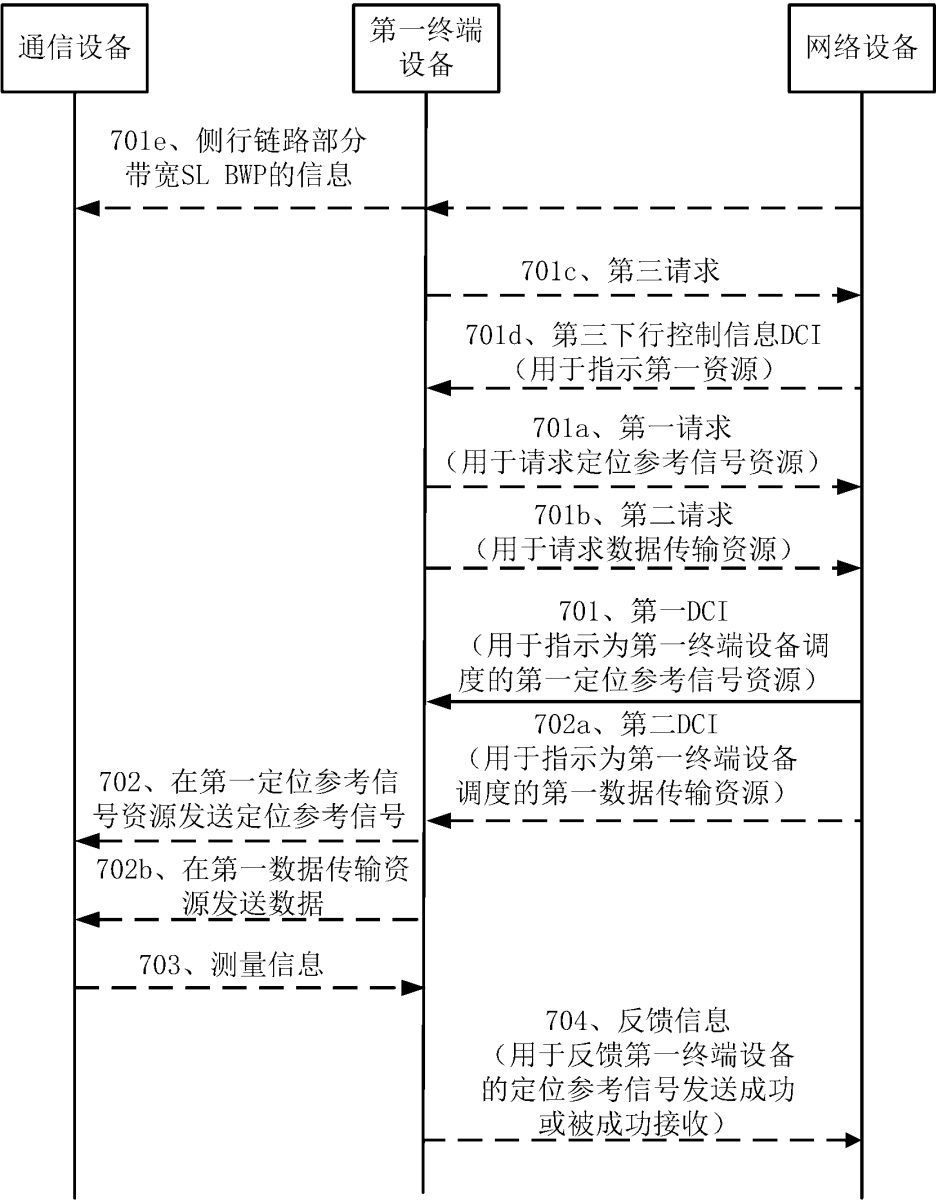


图 7

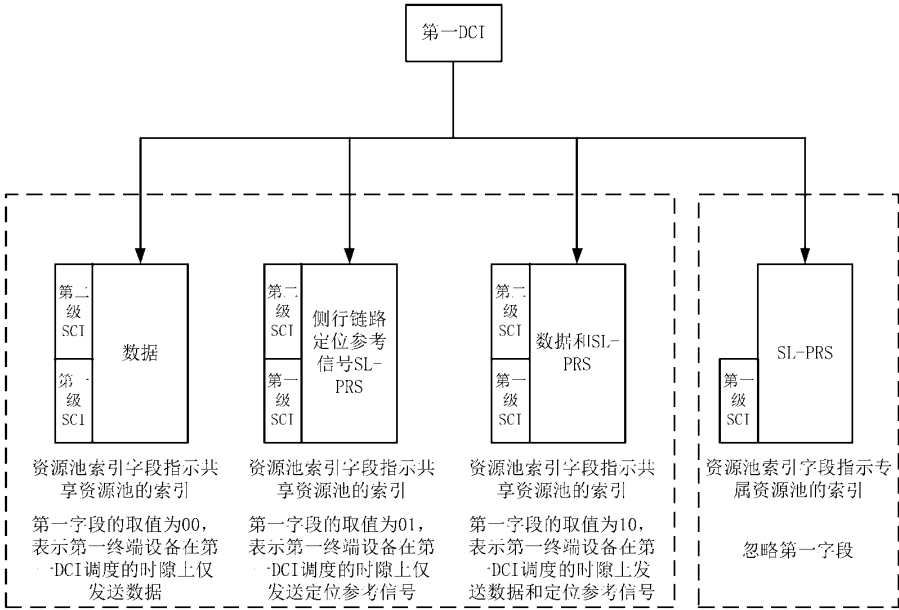


图 8

Bit	1	2	3	4	5	6	7	8	
	Destination Index ₁					LCG ID ₁			Oct1
	Buffer Size ₁								Oct2
	Destination Index ₂					LCG ID ₂			Oct3
	Buffer Size ₂								Oct4
									
	Destination Index _N					LCG ID _N			Oct2N
	Buffer Size _N								Oct2N

图 9A

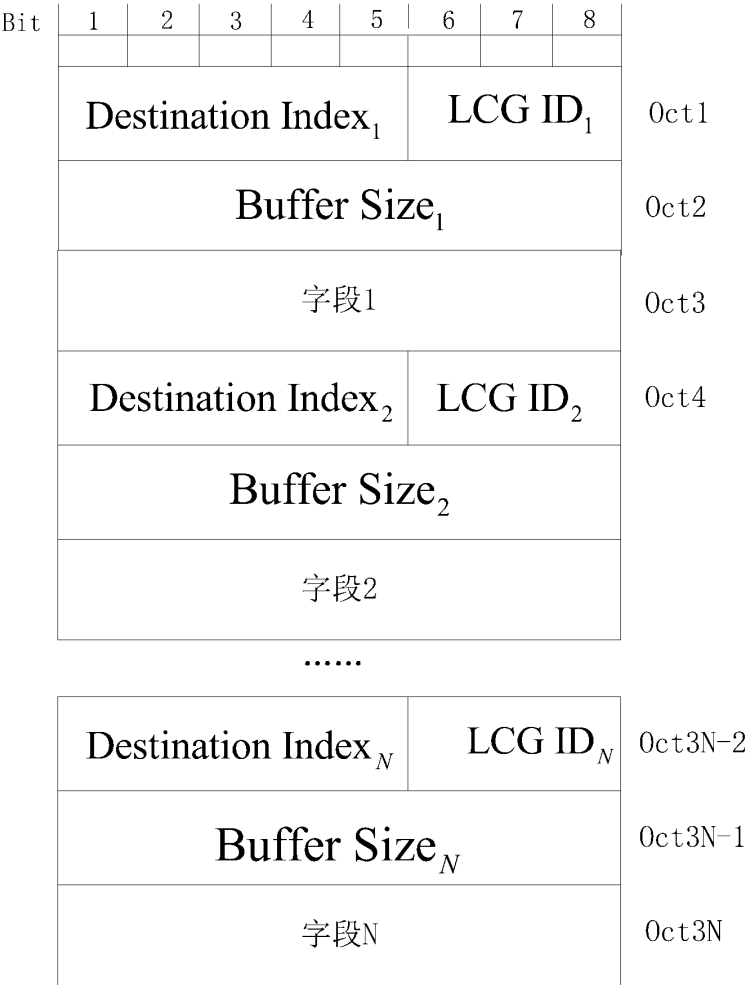


图 9B

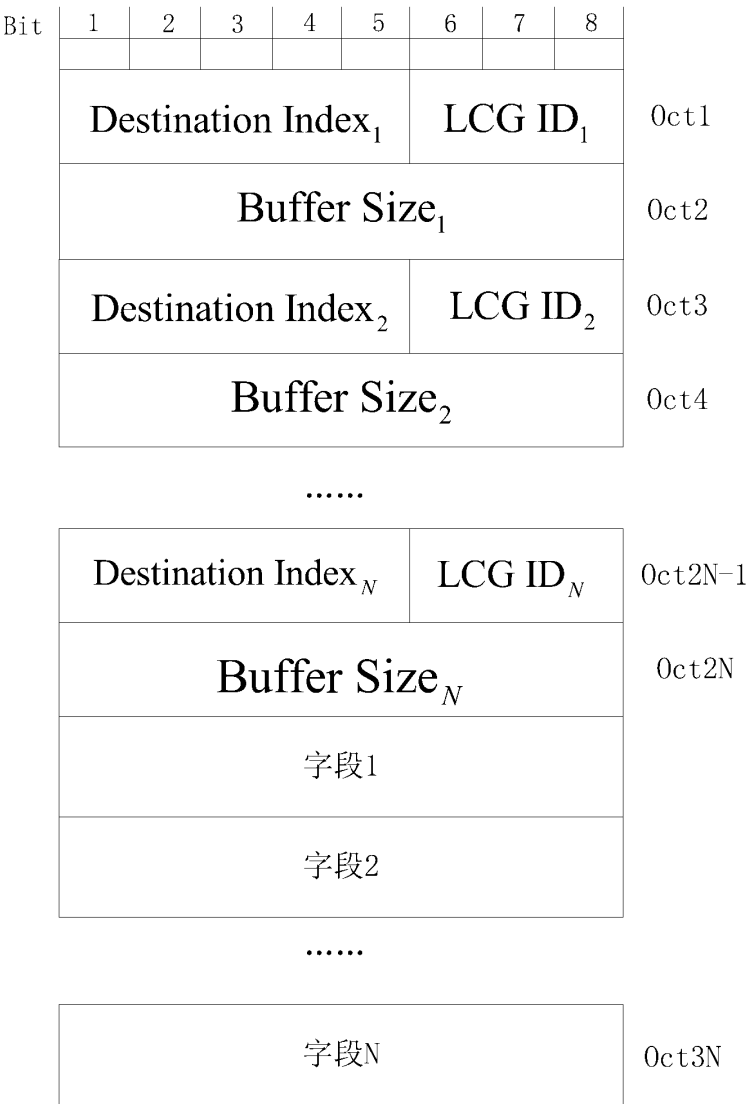


图 9C

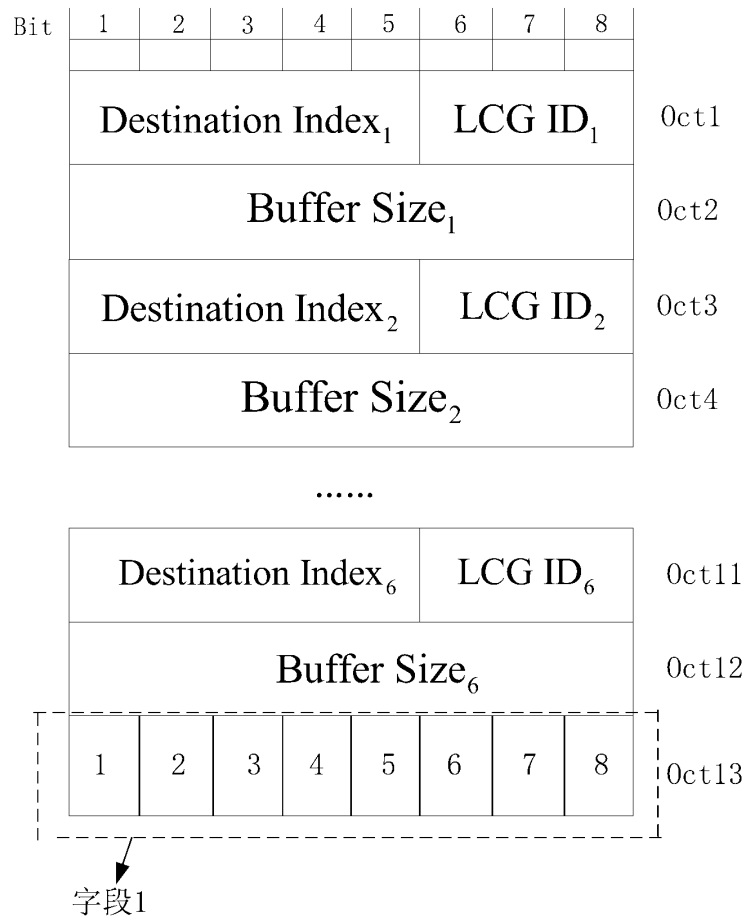


图 9D

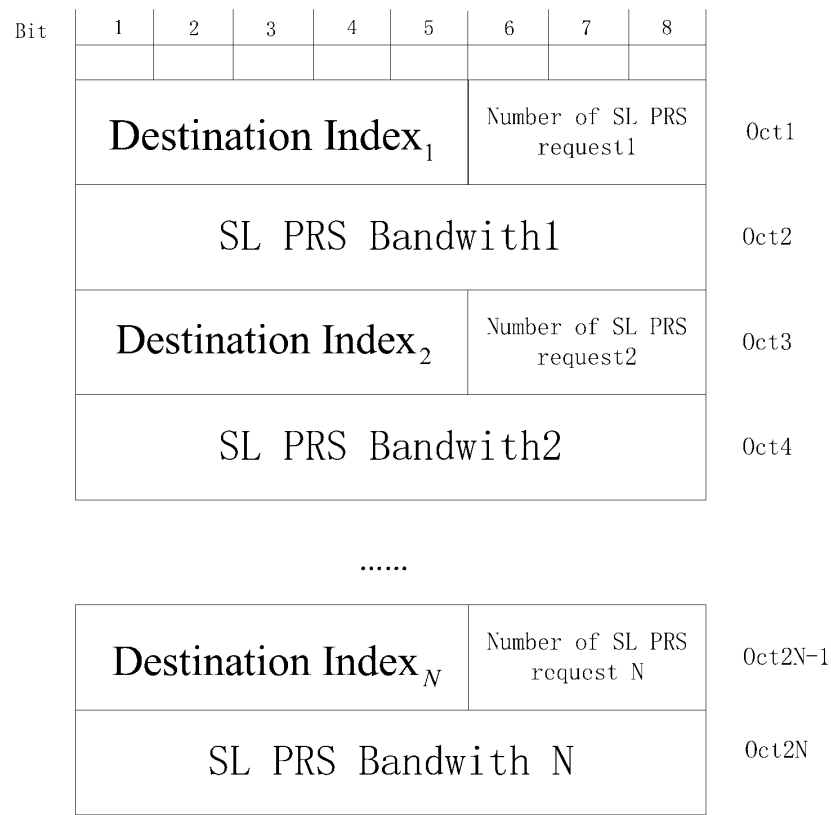


图 9E

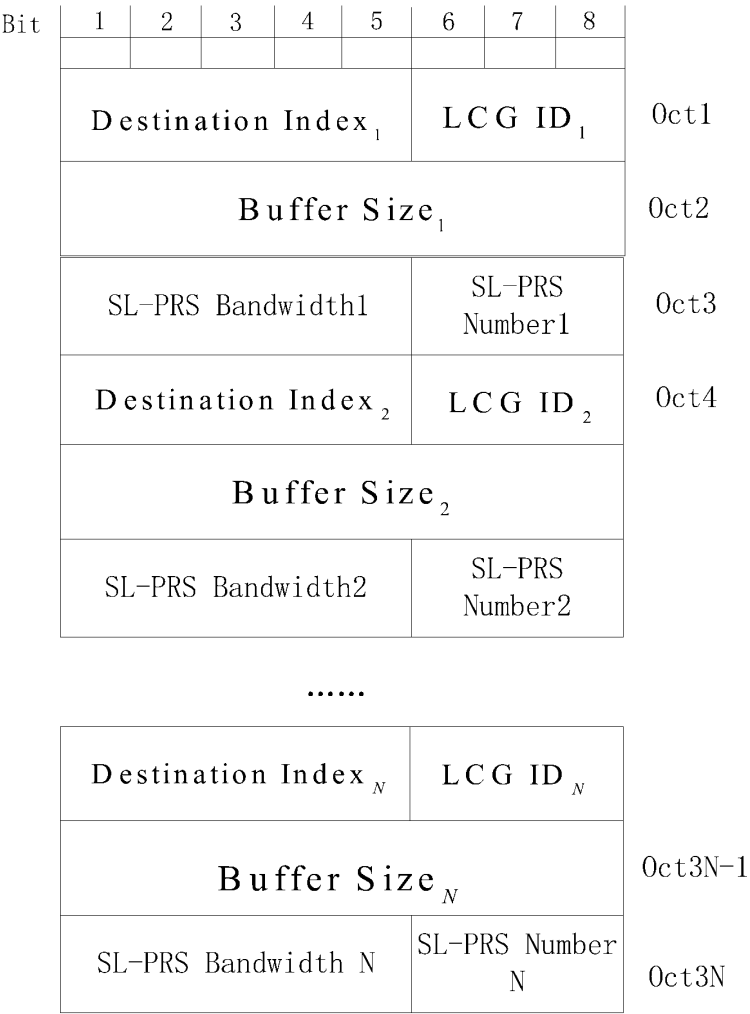


图 9F

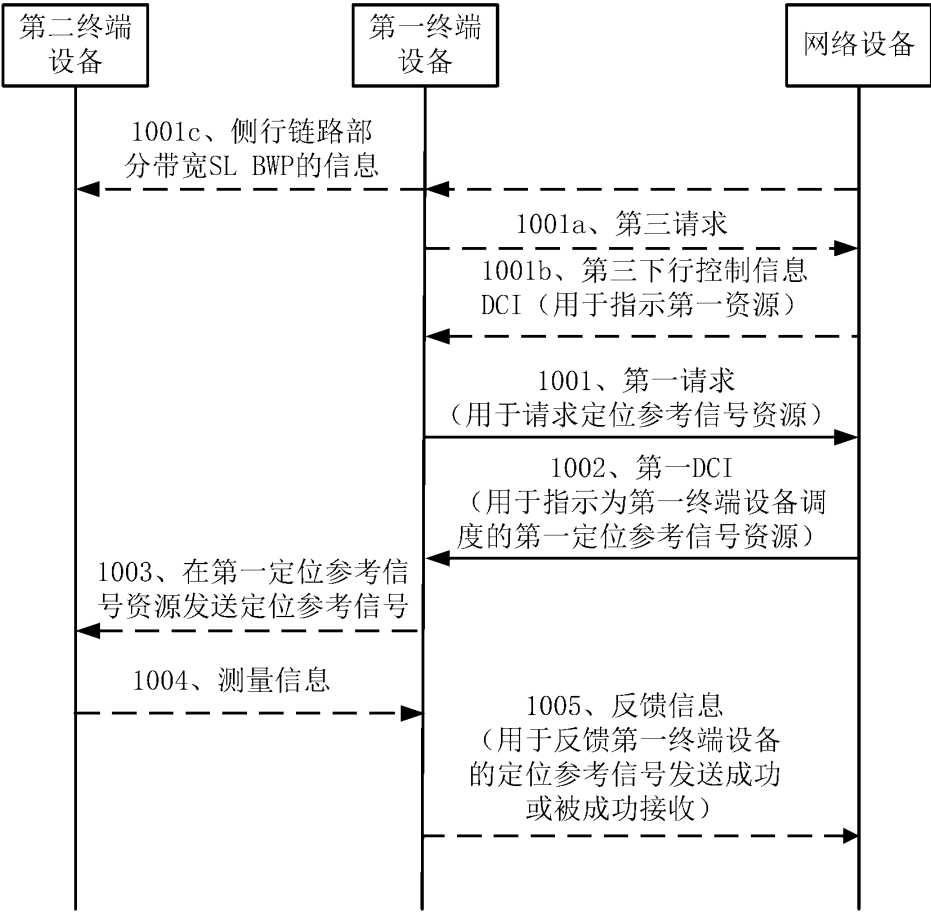


图 10

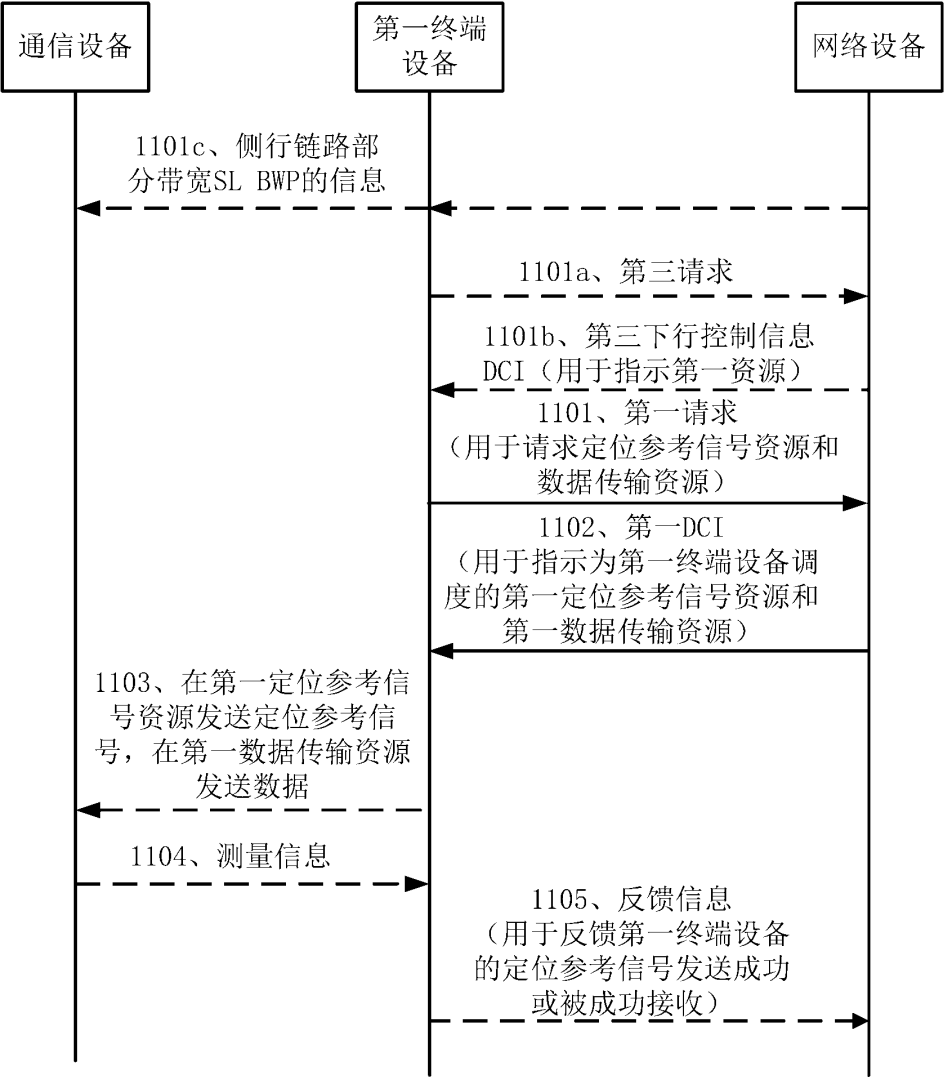


图 11

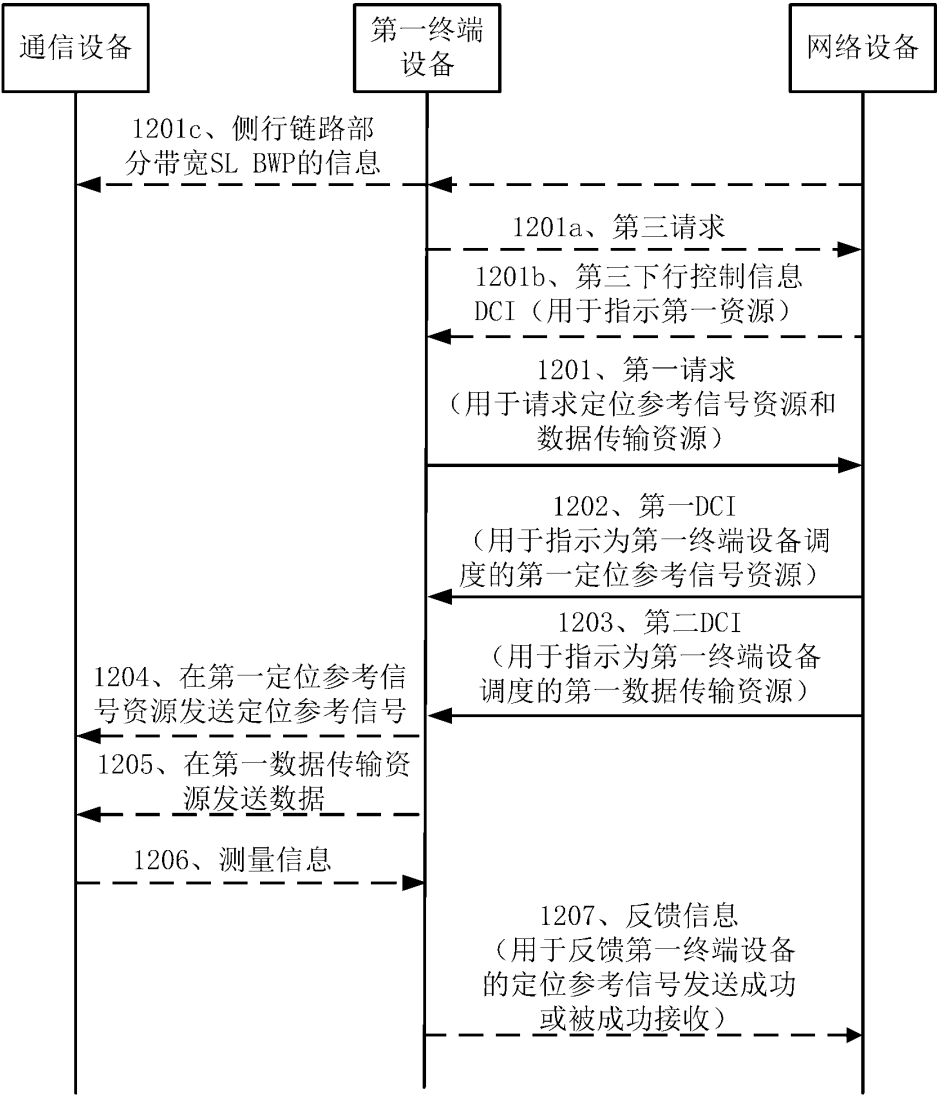


图 12

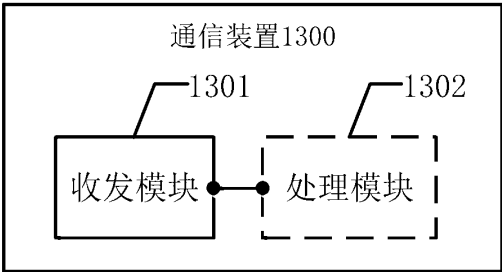


图 13

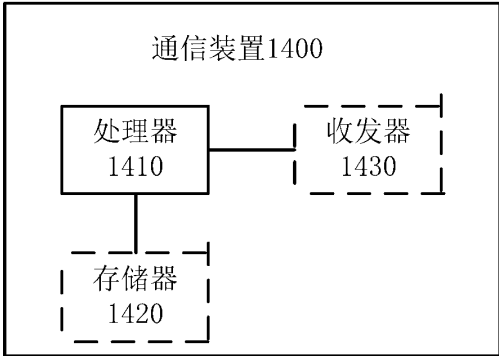


图 14

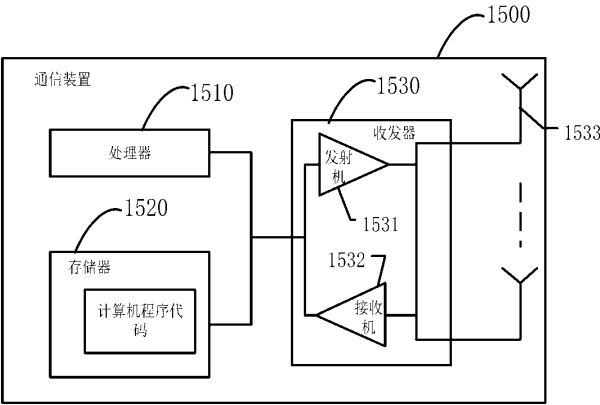


图 15