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A B S T R A C T

An array substrate includes a plurality of signal lines ($X_1 - X_m$) in a display unit (DSP). One switching element is connected per row to each signal line.

- 5 A switching element in the N th row of the M th pixel column and a switching element in the $(N + 1)$ th row of the $(M + 1)$ th pixel column are connected to the same signal line, and video signals having opposite polarities are supplied to adjacent signal lines.