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International Bureau

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 6 : H04N 5/445, 9/64

A1

(11) International Publication Number: WO 95/35626

(43) International Publication Date: 28 December 1995 (28.12.95)

(21) International Application Number: PCT/US95/06851

(22) International Filing Date: 30 May 1995 (30.05.95)

(30) Priority Data:
261,623 17 June 1994 (17.06.94) US

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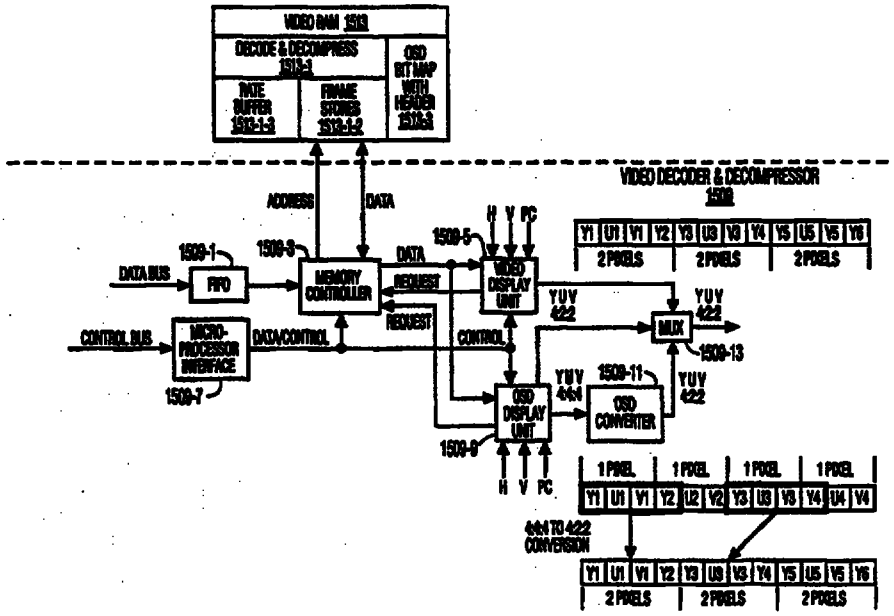
(81) Designated States: BR, CA, CN, FI, JP, KR, MX, RU, SG, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published
With international search report.
Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

9612066-2.

RTMP.

(54) Title: ON SCREEN DISPLAY ARRANGEMENT FOR A DIGITAL VIDEO SIGNAL PROCESSING SYSTEM



(57) Abstract

A digital video signal processing system receives encoded packets of data representing video image information in compressed form. A video decoder (1509) and an associated video RAM (1513) are used to decode and decompress the video packets to form groups of video image representative digital words. The video decoder (1509) also includes an on-screen display (OSD) unit (1509-9) which converts a graphics bit map stored in a section of the video RAM (1513) to groups of graphic image representative digital words. The image representative digital words and the graphic representative digital words are multiplexed together under the control of the OSD display unit (1509-9).