



TRAY FOR BALL TERMINAL INTEGRATED CIRCUITS

Background of the Invention

Field of the Invention

This invention generally relates to integrated circuits and more specifically to devices for the storage and transport of such integrated circuits characterized by solder ball terminals.

Description of Related Art

The storage and transportation of semiconductor components have become important considerations in the production of electronic assemblies, especially as semiconductor components have matured from inexpensive, elementary circuit elements into expensive, sophisticated, complex circuit components. As these components have grown in complexity, they have become more susceptible to damage from a number of external influences, such as mechanical shock and discharges of accumulated electrostatic charge. Consequently, there have been many changes in the transportation, assembly and testing procedures that electronic assembly production facilities utilize. Such procedures, when properly implemented, now contribute significantly to the success of such electronic assembly facilities.

For example, in certain circumstances it is important to transport a component, such as an integrated circuit, pretest it, and provide ready access to the component for delivery to or insertion into a printed circuit board. "Chip carriers" provide such functions for individual components. "Chip carriers" are special fixtures that house and protect individual components, such as an integrated circuits, from damage due to mechanical



shock or electrostatic discharge during processing, production, testing and assembly operations. A chip carrier also can orient an integrated circuit during the production process, assure proper placement and alignment of terminals for testing and for insertion into a printed circuit board.

In other circumstances, there may only be a requirement for the storage and shipment of large numbers of integrated circuits and like components without testing. For example, integrated circuit manufacturers ship such components to customers in bulk quantities. Some customers may desire to move such components in groups or sets directly to a printed circuit board. Other customers may desire to transport or store such components and then transfer the components to chip carriers or the like for further transportation and testing. When this limited requirement exists, it is difficult to justify the costs of purchasing and handling individual chip carriers. Examples of devices that store or carry multiple semiconductor components on a single device are illustrated in the following Letters Patent:

3,489,686 (1969) Gutsche et al.

3,482,682 (1969) Cronkhite.

3,361,253 (1972) Cronkhite.

3,946,864 (1976) Hutson.

4,057,142 (1977) Lechner et al.

The foregoing references disclose generally devices for storing semiconductor wafers. Such wafers, however, do not have terminals that are normally encountered in finished integrated circuits. These trays are not readily adapted to devices with terminals. However, the following US

Letters Patent disclose devices for the shipment and transportation of integrated circuit components or for circuits utilizing such integrated circuit components:

4,210,243 (1980) McDowell.

4,725,918 (1988) Bakker.

4,792,042 (1988) Koehn et al.

5,103,976 (1992) Murphy.

United States Letters Patent No. 4,210,243 to McDowell discloses a tray for holding integrated circuit packages of the transistor outline type. This tray has a top plate with a plurality of funnel shaped openings each having a cylindrical bottom portion for receiving a case. The top and bottom edges of the trays are dimensioned so that the top of a first tray nests inside the bottom of a second tray. When the nested trays are turned over, packages and openings in the first tray sit with the flat portions of their leads on the bottom of the second tray. A rim limits transverse movement of the packages set on the plate when the tray is shaken to cause them to fall into associated openings.

United States Letters Patent No. 4,725,918 Bakker discloses a box for storing electronic devices apparently including integrated circuits. The box includes a material that minimizes electrostatic accumulation and resultant discharges that could otherwise damage the electronic device.

United States Letters Patent No. 4,792,042 to Koehn et al. discloses a chip carrier for individual electronic circuits. It is possible to stack these chip carriers for shipment in bulk, albeit, in a single package.

Each of the foregoing McDowell, Bakker and Koehn et al. patents describes a device for use with a component of a single size or limited range of sizes. The Murphy patent discloses a tray for the transportation and storage of pin grid array integrated circuit components. A pin grid array (PGA) integrated circuit component typically has a thin planar housing of a ceramic or other material for containing a semiconductor substrate and related circuitry. Terminal pins extend perpendicularly to one planar surface of the housing. The terminal pins define an array or matrix of columns and rows with an industry-standard spacing. Currently the spacing is 0.1 inch so the terminal density is about 100 terminals per square inch. PGA integrated circuit components come in myriad sizes that are defined alternatively by the size of the housing (from a 1 inch square to a 2.5 inch square) or by the size of the matrix (from a 9 x 9 terminal pin matrix to a 25 x 25 terminal pin matrix).

The Murphy patent discloses stackable trays for carrying multiple PGA integrated circuit components. Each tray has a lattice framework that defines discrete storage pocket areas. Each storage pocket area comprises a base support that spans portions of the framework and includes upstanding ribs that engage the integrated circuit component. The locus of the upstanding ribs of a given set constitutes a rectangle or square that is concentric with and spaced from the locus of other sets of upstanding ribs. Depending terminal pins from the housing lie between individual ones of the upstanding ribs.

These trays have proven to accommodate various characteristics

inherent in such components and to satisfy other requirements imposed by manufacturing processes. For example, this tray provides a direct, repeatable correlation between the tray and the terminal pins. It also protects the terminal pins from damage due to mechanical shock during transport and prevents the accumulation of an electrostatic charge on the PGA integrated circuit component to avoid a potential discharge and damage.

Integrated circuit manufacturers now produce an integrated circuit package called a ball grid array (BGA) integrated circuit. BGA integrated circuits are characterized by a thin planar housing and a plurality of external terminals on one side of that housing. Each external terminal comprises a small solder ball. Like prior art PGA integrated circuit packages, the solder ball terminals in a BGA integrated circuit package can be arranged in a two-dimensional array. However, the terminal density in a BGA integrated circuit package is many times (up to 7 or more times) greater than attained with PGA integrated circuit packages.

The manufacturers of BGA integrated circuit packages and their customers who assemble these packages onto circuit boards have evolved a series of handling requirements that are more restrictive than those associated with PGA integrated circuit packages. For example, solder ball terminals are more susceptible to physical damage than the terminal pins in PGA integrated circuit packages. Consequently both manufacturers and customers want a common component tray that protects the integrated circuit from physical and electrical damage. Both want a tray that provides

repeatable positioning of the BGA integrated circuit package in the tray to enhance automated assembly techniques. Both want a tray that will store multiple BGA integrated circuit packages and that will stack with other trays to facilitate bulk transport and storage.

However, the manufacturers and customers have a set of apparently antithetical requirements that must be met without sacrificing any of the common requirements. Specifically, manufacturers want to transport BGA integrated circuit packages in a "terminals up" configuration in which the planar housing is substantially horizontal and the solder ball terminals are "on top" of the housing. This "terminals up" position facilitates the inspection and testing of the BGA integrated circuit package by the manufacturer prior to shipment. Moreover, some customers will desire to work with the components in this "terminals up" orientation for initial inspection and preliminary testing. However, most users will want to "flip" the component to a "terminals down" position for robotics pick and place when being joined to a circuit board. In a "terminals down" position the planar housing is essentially horizontal and the terminals are on the "bottom" of the planar housing.

None of the prior art storage trays is capable of providing all these requirements. In each it would be necessary to design separate trays for each of the "terminals up" and "terminals down" orientations and to establish some methodology for transferring the BGA integrated circuit packages individually in one orientation in one tray to the second orientation in the other tray. Such a methodology is not acceptable because it

introduces extra manufacturing time and costs and because any such handling introduces an increased risk of integrated circuit component damage.

Summary

Therefore it is an object of this invention to provide an economical tray for the transportation and storage of electronic components, particularly ball grid array integrated circuit components, with the component in either of two orientations.

Another object of this invention is to provide a tray for storing and transporting electronic components, particularly ball grid array integrated circuit components, that accurately positions the components and the terminals on those components in either of two orientations.

Another object of this invention is to provide a tray for storing and transporting electronic components, particularly ball grid array integrated circuit components, that protects the terminals emanating from such components from damage due to contact with the tray or other devices in either of two orientations of the component.

Still another object of this invention is to provide a tray for storing and transporting ball grid array integrated circuit components with solder terminals on one side of a planar housing that stabilizes the position of such components both when the terminals are oriented above the housing and below the housing.

Yet another object of this invention is to provide stackable trays for storing and transporting ball grid array integrated circuit components in

which each tray can carry the components in either of two orientations.

Yet still another object of this invention is to provide a tray system with identical stackable trays for storing and transporting ball grid array integrated circuit components that facilitates both manual and automated handling techniques by allowing the stacked tray and components stored therein to be flipped between two orientations.

In accordance with this invention, a tray for storing and transporting integrated circuit components characterized by a thin planar housing and plurality of terminals on one side of the housing comprises a framework that defines a storage pocket area for the component. A first support carried by the framework defines a first support plane for engaging the one side of the integrated circuit component. A second support carried by said framework defines a second support plane that is parallel to the first support plane for engaging the other side of an integrated circuit component. This structure enables the tray to store the integrated circuit component in a first orientation with the first support plane above the second support plane and a second orientation with the first support plane below the second support plane. The framework stabilizes the position of the integrated circuit component in both orientations.

In accordance with another aspect of this invention, a tray system for storing a plurality ball grid array integrated circuit components includes first and second trays. Each integrated circuit component has a planar housing and an array of ball terminals located on one side of the housing. Each of the first and second trays includes a framework, first and second sets of

supports. The framework defines a storage pocket area for each integrated circuit component and locates each integrated circuit component at and stabilizes the position of the integrated circuit component within a corresponding storage pocket area. The first support set carried by the framework in each said storage pocket area defines a first support plane engages the one side of the integrated circuit component in that storage pocket area. The second support set carried by the framework in each said storage pocket area defines a second support plane that is parallel to and spaced from the first support plane and engages the other side of the integrated circuit component. Complementary structures on each tray enable the first and second trays to be stacked and capture the integrated circuit component therebetween.

Brief Description of the Drawings.

The appended claims particularly point out and distinctly claim the subject matter of this invention. The various objects, advantages and novel features of this invention will be more fully apparent from a reading of the following detailed description in conjunction with the accompanying drawings in which like reference numerals refer to like parts, and in which:

Fig. 1 is a perspective view of a tray that is constructed in accordance with this invention for storing and transporting electronic components in a first orientation;

Fig. 2 is a perspective view of the tray in Fig. 1 in a second orientation;

Fig. 3 is a top view of the tray;

Fig. 4 is a bottom view of the tray;

Fig. 5 is an enlarged cross-sectional view of a portion of a pair of stacked trays taken generally along lines 5-5 of Fig. 1 in a first orientation; and

Fig. 6 is an enlarged cross-sectional view of the pair of trays in Fig. 5 in a second orientation.

Description of Illustrative Embodiments

A tray system for storing and transporting integrated circuit components or the like constructed in accordance with this invention comprises a tray 10 as shown in Figs. 1 through 4 that can be stacked with one or more additional trays, such as a second tray 10A in Figs. 5 and 6. In this particular embodiment, the tray 10 carries a number of ball grid array ("BGA") integrated circuit components at discrete locations, or storage pocket areas, in the tray 10. By way of example, each of Figs. 1 through 6 depicts a BGA component 11 with a thin, planar housing 20 and a plurality of solder ball terminals 21 formed on one planar surface 22 of the housing 20 as shown in Figs. 1, 3 and 5. In this orientation, the terminals 21 lie "on top" of the housing 20 and arranged in a two-dimensional matrix. This is a "terminals up" orientation. In Figs. 2, 4 and 6 the integrated circuit terminals lie below the housing 20 in a "terminals down" orientation, with only the other surface 23 of the housing visible.

The tray 10 is an integrally molded device, typically of a conductive plastic. The conductive plastic may comprise any of a wide variety of easily molded, conductive and thermally and dimensionally stable materials. A

conductor-filled, polyether sulfone material is preferred. Other materials include carbon or aluminium-filled polyetherimides, polyacryl sulfones and polyesters.

The tray 10 in Figs. 1 through 4 is molded in a lattice-like framework 24 that segregates the tray 10 into an arbitrary number of discrete storage pocket areas. In the particular embodiment of Fig. 1, the framework comprises four storage pocket areas 25, 26, 27, and 28 arranged in two columns and two rows. Specifically, the tray 10 includes outer peripheral beams 30, 31, 32 and 33 that form the periphery of the tray 10. A series of intersecting walls define the storage pocket areas and include a first set of walls 34, 35 and 36 that are parallel to the beams 30 and 32 and a second set of spaced parallel walls 40, 41 and 42 between the beams 31 and 33. The walls 34, 35 and 36 intersect the walls 40, 41 and 42. Collectively these walls form the peripheries of the various pocket storage areas. More specifically, portions of walls 34, 35, 41 and 42 form the storage pocket area 25; walls 35, 36, 41 and 42 form storage pocket area 26; walls 34, 35, 40 and 41 form storage pocket area 27; and walls 36, 36, 40 and 41 form storage pocket area 28.

Each of the storage pocket areas 25 through 28 has identical construction, so only storage pocket area 25 is described in detail. Looking at storage pocket area 25, particularly in Figs. 1 and 3, the portion of the walls 34, 35, 41 and 42 that bound the storage pocket area 25 include a number of inwardly projecting tabs. Specifically, tabs 43 and 44 extend from the portion of wall 41; tabs 45 and 46, from a portion of wall 35; tabs

47 and 48, from a portion of wall 42; and tabs 50 and 51, from a portion of wall 34. Each of these tabs 43 through 48, 50 and 51 forms a coplanar surface. In Fig. 5 (on the top of the tabs in the orientation scheme in Fig. 1.), the top edge surfaces on a corresponding tab 52 on wall 34, tab 53 on wall 35 and tabs 54 and 55 on wall 40 form a first support plane ⁷⁴. More specifically, in the position shown in Fig. 1, tabs in the storage pocket area 27 corresponding to tabs 43 through 48, 50 and 51, engage the other surface 23 of the integrated circuit 11 thereby to support the integrated circuit 11 in a terminals up position.

Each of the walls includes additional tabs that are formed between individual ones of the first tabs and adjacent intersections of walls. Again as shown in Figs. 1 and 3, the portion of the wall surface forming storage pocket area 25 carries inwardly extending tabs 60 and 61 while portions of the wall 35, 41 and 34 carry, respectively, tabs 62 and 63, tabs 64 and 65 and tabs 66 and 67. Edges of corresponding tabs 70 on wall 34, 71 on wall 35 and tabs 72 and 73 on wall 40 form a second support plane ⁵⁶ that, as shown in Fig. 6, engages the one planar surface 22 of the integrated circuit 11 to support the integrated circuit 11 in a terminals down position as shown in Figs. 2 and 6. These edges are the top tab edges in Fig. 6 and the bottom tab edges in Fig. 5.

Each wall portion around each storage pocket area additionally includes structures for restraining any motion of the integrated circuit 11 in a direction that is parallel to either of the first or second support planes ⁷⁴ and ⁵⁶, shown in Figs. 5 and 6. The wall 41 between the storage pocket

areas 25 and 27 has a central body portion 80 that extends between the walls 35 and 36. A central extension in the form of a tongue 81 carries the tabs 43 and 44 that extend into the storage pocket area 25 and corresponding tabs that extend into the storage pocket area 27. The tongue 81 extends above the central portion 80 in the orientation of Fig. 1 and includes chamfered or angled surface 82 and 83 that form a transition between the upper edge of the central portion 80 and the free edge of the tongue 81. A recess 84 located below the central portion 80 has chamfered surfaces 85 and 86 and corresponds in form to the tongue 81.

Each of the wall intersections, such as the intersection 90 formed by walls 35 and 41, has a corner extension structure that extends along each wall portion into each of the adjacent storage pocket areas. In the storage pocket area 25, for example, a portion of a corner extension 92 extends downwardly or oppositely from the tongue 81 at the intersection 90. A similar corner extension 92 extends along a portion of the wall 41 from the intersection of the walls 41 and 34. Each of these corner extensions mates with a corresponding recess 93 or 94 as shown in Figs. 1 and 3 formed by chamfered surfaces 82 and 83 thereby to facilitate the interengagement of corner extensions and tongue portions. In addition each of the corner extensions, such as corner extensions 91 and 92 terminate with chamfered free edges 97 and 98 respectively. The edges facilitate the placement of components on the tray in a terminals down orientation.

As shown in Fig. 5, the edges of the housing 11 rest on the first support plane 56 defined by edges of the central tabs 52, 53, 54 and 55.

Corner extensions 100 and 101 on the tray 10A interengage recesses 102 and 103 on the tray 10. These corner extensions 100 and 101 extend vertically down past the integrated circuit in the orientation shown in Fig. 5. A central extension or tongue 104, corresponding to the central extension 81, extends upwardly into a recess 105 formed in the tray 10A that corresponds to the recess 84 shown in Figs. 1 and 3. The tongue 105 also extends vertically upwardly past the integrated circuit 11. Consequently, each of the wall portions surrounding a particular storage pocket area includes portions of the walls of the adjacent stacked trays 10 and 10A, and they form an essentially continuous wall surface. Each of these walls is identical and positioned proximally the edges of the integrated circuit 11 thereby to capture the integrated circuit 11 in a storage pocket area and limit any motion of the integrated circuit 11 parallel to a support plane (i.e. horizontally in the orientation shown in Figs. 5 and 6).

Fig. 6 depicts the structure of the stacked trays of Fig. 5 after they have been flipped to orient the integrated circuit 11 in a terminals down orientation. In this position edges of the tabs, such as tabs 106 and 107 in Fig. 10A, that correspond to tabs 72 and 73 in tray 10, support the housing 11 in a second support plane 74A corresponding to the support plane 74. The support plane 56 and 74A are spaced by an amount corresponding to the thickness of the integrated circuit housing 20.

In accordance with another aspect of this invention, Figs. 1 and 2, the beams 30 through 33 include bosses 110, 111, 112 and 113 on one surface of the tray 10 and aligned receptacles 114, 115, 116 and 117 formed in the

opposite side of the tray 10. Certain of the bosses can have differing shapes to assure registration and appropriate alignment of adjacent trays. In Figs. 1 and 2, for example, the boss 110 is formed as a cruciform while bosses 111 through 113 are cylindrical and the receptacle 114 is larger than the receptacles 115 through 117. This assures that adjacent trays can only stack with the receptacle 114 and mating with the boss 110. Moreover, the extensions of the tongues, such as tongue 104 and the corner extensions, such as corner extensions 100 and 101 past the integrated circuit assure that improperly oriented trays will not engage and damage the terminals 21 on the integrated circuit 11.

If the trays are stacked as shown in Fig. 5 and the upper tray 10A is then removed, the lower tray extends the terminals 21 as shown in Fig. 1. In this orientation the terminals are accessible for inspection and tests. The central tongues such as the tongue 81, engage each of the edges of the integrated circuit 11 thereby to stabilize its position in a horizontal plane. If the tray 10A is stacked on top of the tray 10 as shown in Fig. 5, the stacked trays can then be turned over to the orientation shown in Fig. 6. During this process, the various tabs in both trays coact to stabilize the position of the integrated circuit relative to the two trays during tray rotation. If the upper tray, now the tray 10, is removed, the tray 10A holds the integrated circuit 11 in a terminals down position as is also shown in Fig. 2. In this orientation the corner extensions stabilize the position of the integrated circuit 11 within the storage pocket area 25.

Thus, it will now be apparent that the tray 10 constructed in

accordance with this invention satisfies each of the objects of this invention. The tray is directly adapted for use with ball grid array integrated circuit components. A single tray can orient the components in a "terminals up" or "terminals down" position. Wall portions around each BGA component accurately position each integrated circuit to facilitate pick and place operations. Moreover, wall portions form pockets and protect the integrated circuits from physical damage. Yet, all the terminals are readily accessible for inspection and test in a "terminals up" orientation.

This invention has been disclosed in terms of certain embodiments. It will be apparent that many modifications can be made to the disclosed apparatus without departing from the invention. For example, the specifically disclosed tray 10 uses a plurality of inwardly extending tabs to define each of two support planes. Other structures could be substituted for such tabs and serve to establish one or both of these support planes. Therefore, it is the intent of the appended claims to cover all such variations and modifications as come within the true spirit and scope of this invention.

Claims:

1. A tray (10) for storing an integrated circuit component (11) having a planar housing (20) and an array of terminals (21) located on one side of the housing (20), in predetermined column and row positions with predetermined spacings, said tray (10) comprising:

A. framework means (24) for defining at least one storage pocket area (25-28) that is accessible from first and second opposite sides of said framework means (24),

B. first support means (70-73) carried by said framework means (24) for defining a first support plane (74) in a storage pocket area that is transverse to said framework means (24), for engaging the one side of an integrated circuit component (11) about its periphery when the integrated circuit component (11) is inserted into said storage pocket area from said first side of said framework means (24),

C. second support means (60-67) carried by said framework means (24) for defining a second support plane (56) in the storage pocket area that is parallel to and spaced from said first support plane (74) for engaging the other side of an integrated circuit component (11) inserted into said storage pocket area from said second side of said framework means (24), and

D. wall means of said framework means defining the periphery of the or each said storage pocket area on both sides of the tray, said wall means having extensions and recesses such that when the first side of one of said trays is mounted to the second side of another one of said trays, the extensions of one fit

into the recesses of the other , whereby said tray (10) is able to store an integrated circuit component (11) in a terminals-down orientation when said tray (10) is in a first, substantially horizontal position with the first support plane (74) above the second support plane (56) and store an integrated circuit component (11) in a terminals-up orientation when said tray (10) is in a second, substantially horizontal position with the first support plane (74) below the second support plane (56), and wherein said wall means is configured to stabilize the position of an integrated circuit component housing (20) in the or each storage pocket area in a plane transverse to said framework means (24), in said storage pocket area tray in both the terminals-down and terminals-up orientations.

2. A tray (10) for storing an integrated circuit component (11) as recited in claim 1 wherein said wall means on one side of said tray includes a central recess portion (84) and corner extensions (91, 92) and said wall means on the other side of the tray includes a central extension (80), whereby said central extensions or said corner extensions (91, 92) stabilize an integrated circuit component (11) when the tray (10) is in either of its first and second orientations.

3. A tray (10) for an integrated circuit component (11) as recited in claim 2 wherein each of said central and corner extensions (91, 92) have chamfered edges intersecting said central portion (80).

4. A tray (10) for an integrated circuit component (11) as recited in claim 2 wherein at least one of said first and second support means comprises a plurality of discrete, spaced tabs carried by said wall means and extending into the storage pocket area.

5. A tray (10) for an integrated circuit component (11) as recited in claim 2 wherein each of said first and second support means comprises a plurality of discrete, spaced tabs carried by said wall means and extending into the storage pocket area and wherein each of said tabs forming said first support means terminates at the first support plane (74) and each of said tabs forming said second support means terminates at the second support plane (56).

6. A tray system for storing a plurality of ball grid array integrated circuit components (11), each integrated circuit component (11) having a planar housing (20) and an array of ball terminals (21) located on a terminal side of the housing, said tray system including first and second trays (10, 10A) each of which comprises:

A. framework means (24) for defining a storage pocket area for each integrated circuit component (11), said storage pocket areas being accessible from first and second opposite sides of said framework means (24),

B. first support means (70-73) carried by said framework means (24) in each said storage pocket area for defining a first support plane (74) for engaging the terminal side of an integrated circuit component (11) about its periphery when

the integrated circuit component (11) is inserted into said storage pocket area from said first side of said framework means (24),

C. second support means (60-67) carried by said framework means (24) in each said storage pocket area for defining a second support plane (56) that is parallel to the first support plane (74) for engaging the other side of an integrated circuit component (11) when the integrated circuit component (11) is inserted into said storage pocket area from said second side of said framework means (24),

D. means for enabling the stacking of said first and second trays (10, 10A) with aligned corresponding storage pocket areas,

E. first stabilizing means (80,81) of said framework means (24) at said first support plane (74) for stabilizing the position of an integrated circuit component (11) inserted from the first side of said framework means (24) onto said first support means (70-73), and

F. second stabilizing means (60-67) of said framework means (24) at said second support plane (56) for stabilizing the position of an integrated circuit component (11) inserted from the second side of said framework means (24) onto said second support means (60-67), whereby, when stacked, said first and second trays (10, 10A) in the tray system are able to capture an integrated circuit component (11) therebetween and said first stabilizing means on one of said stacked trays interengages the second stabilizing means on the other of said stacked trays, to stabilize the position of an integrated circuit component (11)

transverse to said framework means (24) within one of said corresponding storage pocket areas.

7. A tray system for storing integrated circuit components (11) as recited in claim 6 wherein said means for enabling the stacking of each of said trays includes first and second complementary registration means for aligning said first and second trays (10, 10A) in a stacked relationship.

8. A tray system for storing integrated circuit components (11) as recited in claim 7 wherein each of said trays includes an outer peripheral section and said registration means includes complementary location means mounted to extend from opposite sides of each tray for aligning said first and second trays (10, 10A) in the stacked relationship.

9. A tray (10) system for storing integrated circuit components (11) as recited in claim 7 wherein said stabilizing means include a plurality of intersecting wall means (80, 81, 90, 91) for defining the limits of said storage pocket areas proximate the edges of integrated circuit components (11) within the storage pocket areas, wherein said wall means include corner extensions (91, 92) at each intersection (90) of said wall means extending perpendicularly to one side of the first (74) and second support planes (56) and defining central recesses (84) therebetween and central extensions (80) located centrally of each said wall means and extending

perpendicularly to the other side of the first (74) and second (56) support planes, whereby said central extensions and corner extensions (91, 92) are configured to stabilize integrated circuit components (11) within the storage pocket areas when the trays (10) are in the first and second positions, respectively and whereby, when the first and second trays are stacked, said central extensions of one of the stacked first and second trays (10, 10A) interengage with the corner extensions of the other of said stacked first and second trays (10, 10A).

10. A tray system for storing integrated circuit components (11) as recited in claim 9 wherein each of said corner and central extensions include chamfered edges that cooperate during the stacking of said first and second trays (10, 10A) for isolating said trays from contact with the ball terminals (21) and for forming an extended wall surface about each of the integrated circuit components (11).

11. A tray system for storing integrated circuit components (11) as recited in claim 6 wherein said stabilizing means include a plurality of intersecting wall means (80, 81, 90, 91) for defining the limits of said storage pocket areas proximate the edges of integrated circuit components (11) within the storage pocket areas, wherein said wall means include corner extensions (92) at each intersection of said wall means extending perpendicularly to one side of the first (74) and second (56) support planes and defining central recesses (84) therebetween and central extensions (80) located centrally of each said wall means and extending

perpendicularly to the other side of the first (74) and second (56) support planes, whereby said corner extensions and central extensions (91, 92) are configured to stabilize integrated circuit components (11) within the storage pocket areas when the trays (10) are in the first and second positions, respectively and whereby, when the first and second trays are stacked, said central extensions of one of the stacked first and second trays (10, 10A) interengage with the corner extensions of the other of said stacked first and second trays (10,10A).

12. A tray (10) system for storing integrated circuit components (11) as recited in claim 11 wherein each of the integrated circuit components (11) has a predetermined thickness and said first and second support means in adjacent ones of the stacked trays in each said storage pocket area establish a distance between the first (56) and second support planes (74) in the adjacent trays that corresponds to the thickness of the integrated circuit components (11).

13. A tray (10) system for storing integrated circuit components (11) as recited in claim 12 wherein each of said first and second support means comprises a plurality of discrete, spaced tabs carried by said wall means and extending into the storage pocket area.

14. A tray (10) system for storing integrated circuit components (11) as recited in claim 13 wherein each of said tabs extends transversely to the first (74) and second

(56) support planes and wherein each of said tabs forming said first support means terminate at the first support plane (74) and said tabs forming said second support means terminate at the second support plane (56).

15. A tray (10) system for storing integrated circuit components (11) as recited in claim 13 wherein each of said corner and central extensions include chamfered edges that cooperate during the stacking of said first and second trays (10, 10A) for isolating said trays from contact with the ball terminals (21) and forming an extended wall surface about each of the integrated circuit components (11) for stabilizing the position of each integrated circuit component (11) within its corresponding storage pocket area.

16. A tray (10) system for storing integrated circuit components (11) as recited in claim 13 wherein each of said corner extensions extends to a free edge (87, 88) displaced from the housing plane (20) and each of said free edges (97, 98) is chamfered to facilitate placement of an integrated circuit (11) into said storage pocket area.

17. A tray (10) system for storing integrated circuit components (11) as recited in claim 11 wherein each of said corner and central extensions include chamfered edges that cooperate during the stacking of said first and second trays (10, 10A) for isolating said trays from contact with the ball terminals (21) and for forming an

extended wall surface about each of the integrated circuit components (11) for stabilizing the position of each integrated circuit component (11) within its corresponding storage pocket area.

18. A tray (10) system for storing integrated circuit components (11) as recited in claim 11 wherein each of said corner extensions extends to a free edge (97, 98) displaced from the housing plane (20) and each of said free edges (97, 98) is chamfered to facilitate placement of an integrated circuit (11) into said storage pocket area.