Structure and Method of a Strained Channel Transistor and a Second Semiconductor Component in an Integrated Circuit

ABSTRACT OF THE DISCLOSURE

A semiconductor chip includes a semiconductor substrate 126, in which first and second active regions are disposed. A resistor 124 is formed in the first active region and the resistor 124 includes a doped region 128 formed between two terminals 136. A strained channel transistor 132 is formed in the second active region. The transistor includes a first and second stressor 141, formed in the substrate oppositely adjacent a strained channel region 143.

(FIG. 2)