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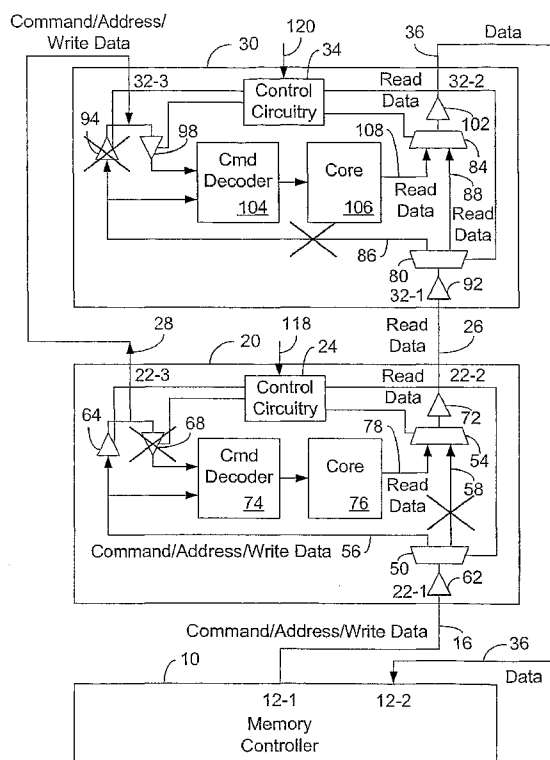
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(54) Title: IDENTICAL CHIPS WITH DIFFERENT OPERATIONS IN A SYSTEM



(57) Abstract: In some embodiments, a chip includes a memory core, control circuitry, and first ports, second ports, and third ports. The first ports are to only receive signals, the second ports are to only provide signals, and the control circuitry is to control whether the third ports are to only receive signals or only provide signals. Other embodiments are described and claimed.



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