# POWER MANAGEMENT OF MULTIPLE PROCESSORS


## ABSTRACT OF THE DISCLOSURE

An information handling system having a plurality of physical processors capable of operating in either a low power or a high power state, and capable of running logical processors that may execute program threads. Each program thread is assigned to be executed in a respective logical processor. The assignment of each program thread to the respective logical processor is determined by whether the program thread requires high-utilization or low-utilization of the plurality of physical processors in the information handling system. To conserve power in the information handling system, high-utilization program threads are assigned to be executed in logical processors running in as few physical processors operating in the high power state, and low-utilization program threads are assigned to physical processors operating in the low power state. To maximize execution speed of program threads in the information handling system, high-utilization program threads are assigned to be executed in logical processors running in different physical processors operating in the high power state, and low-utilization program threads are assigned to any physical processor.


Figure 1



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