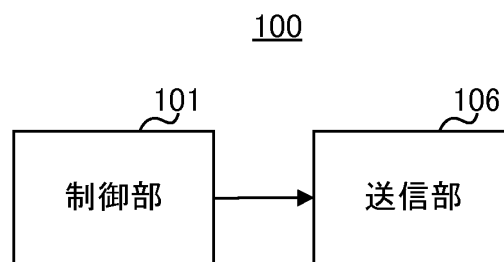
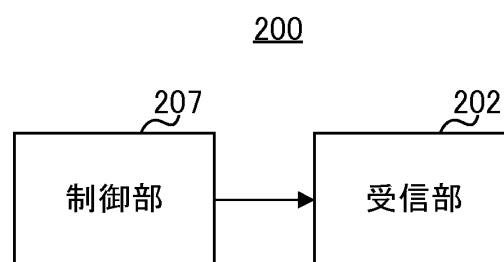


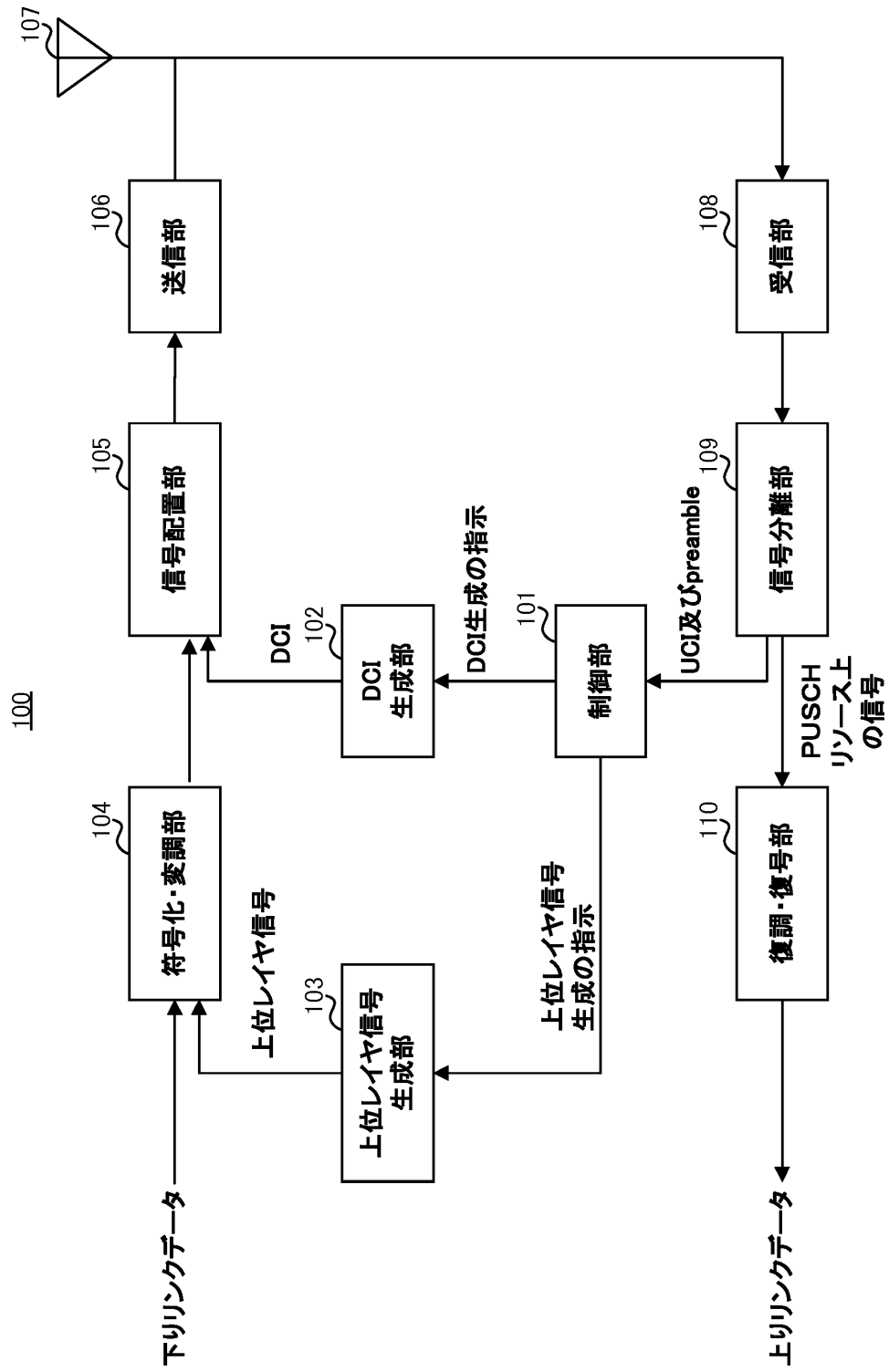
[図1]



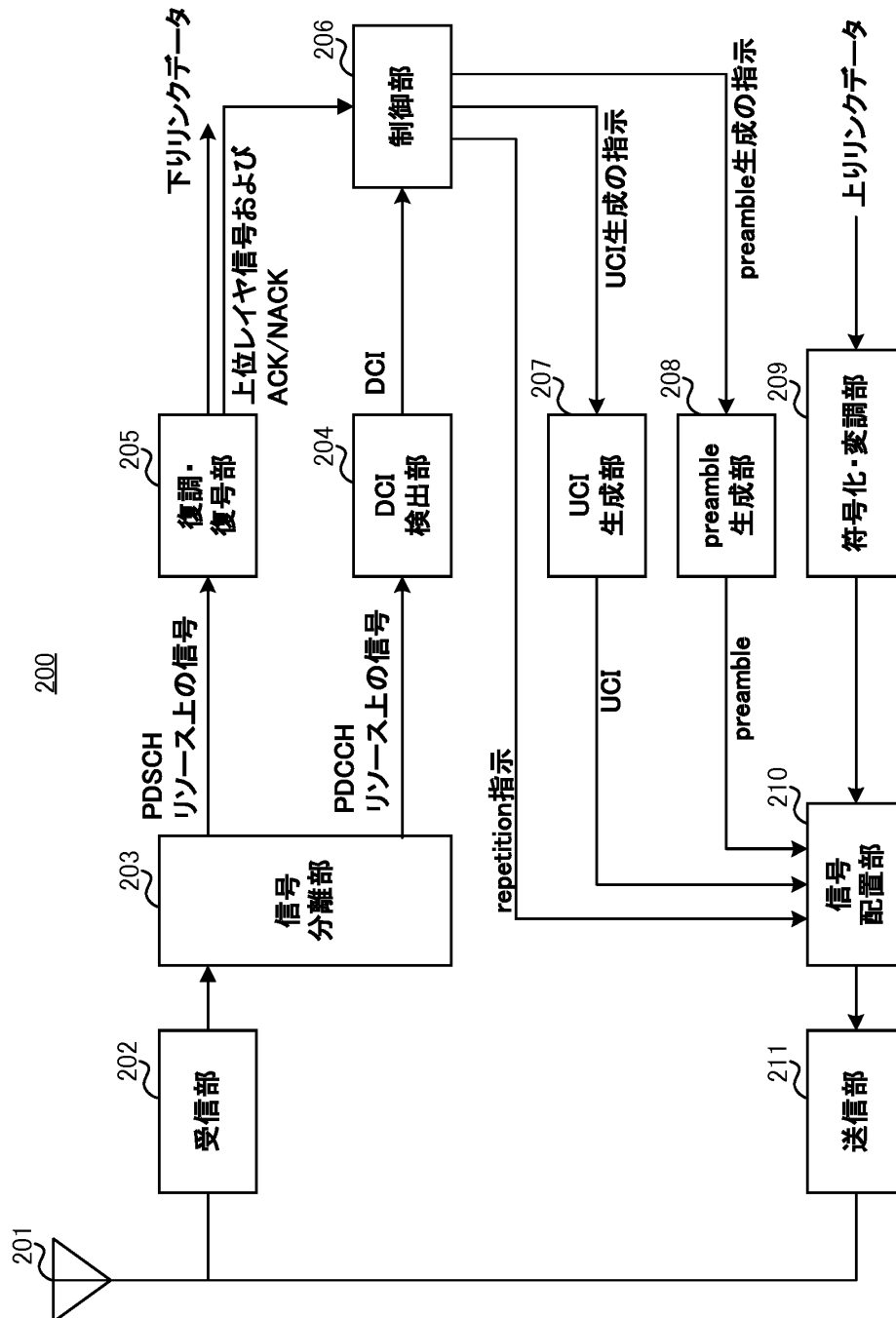
[図2]



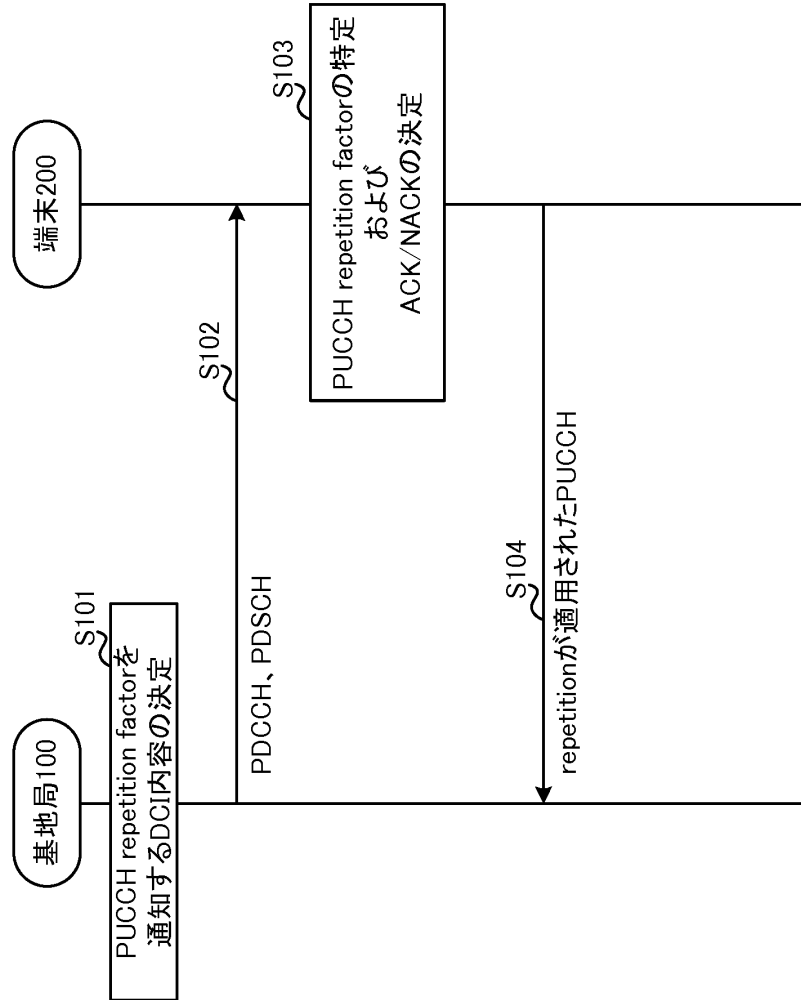
【図3】



[図4]



[図5]



[図6]

Reserved bit (2 ビット) (例えば Downlink assignment index (2 ビット))	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

[図7]

PRI のうち 2 ビット	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

[図8]

PDSCH-to-HARQ-timing-indicator のうち 2 ビット	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

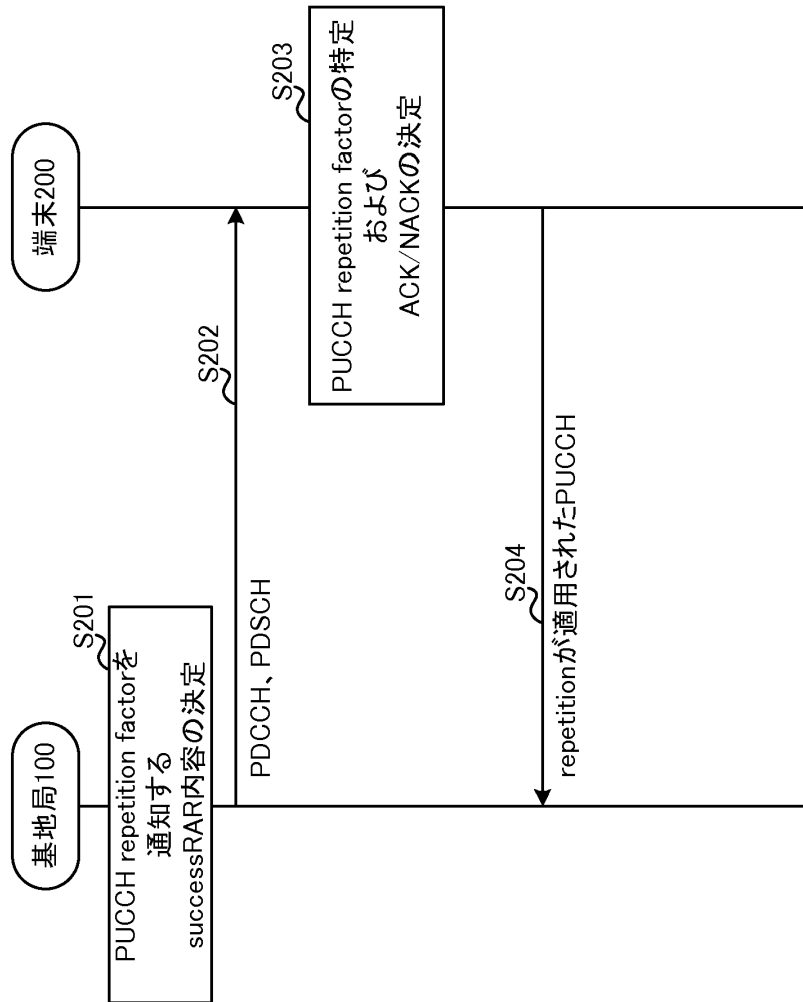
[図9]

TPC command for scheduled PUCCH のうち 1 ビット	Repetition factor
0	RF_{00}
1	RF_{01}

[図10]

MCS のうち 2 ビット	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

[図11]



[図12]

PRI のうち 2 ビット	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

[図13]

PDSCH-to-HARQ-timing-indicator のうち 2 ビット	Repetition factor
00	RF_{00}
01	RF_{01}
10	RF_{10}
11	RF_{11}

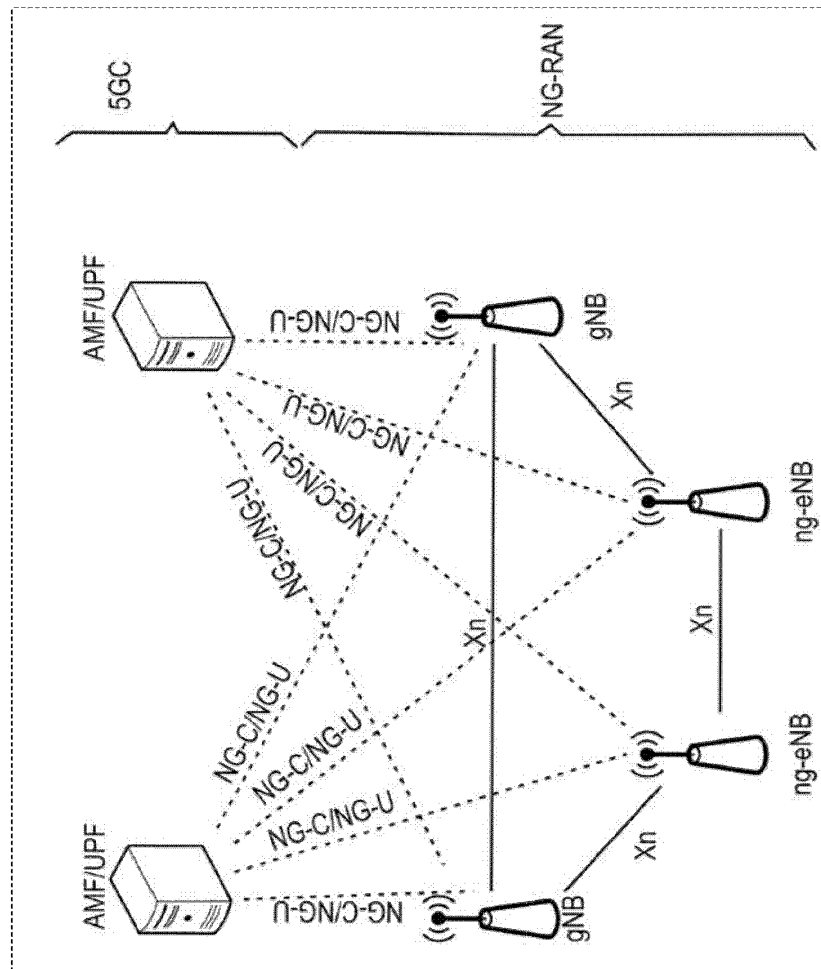
[図14]

TPC command for scheduled PUCCH のうち 1 ビット	Repetition factor
0	RF_{00}
1	RF_{01}

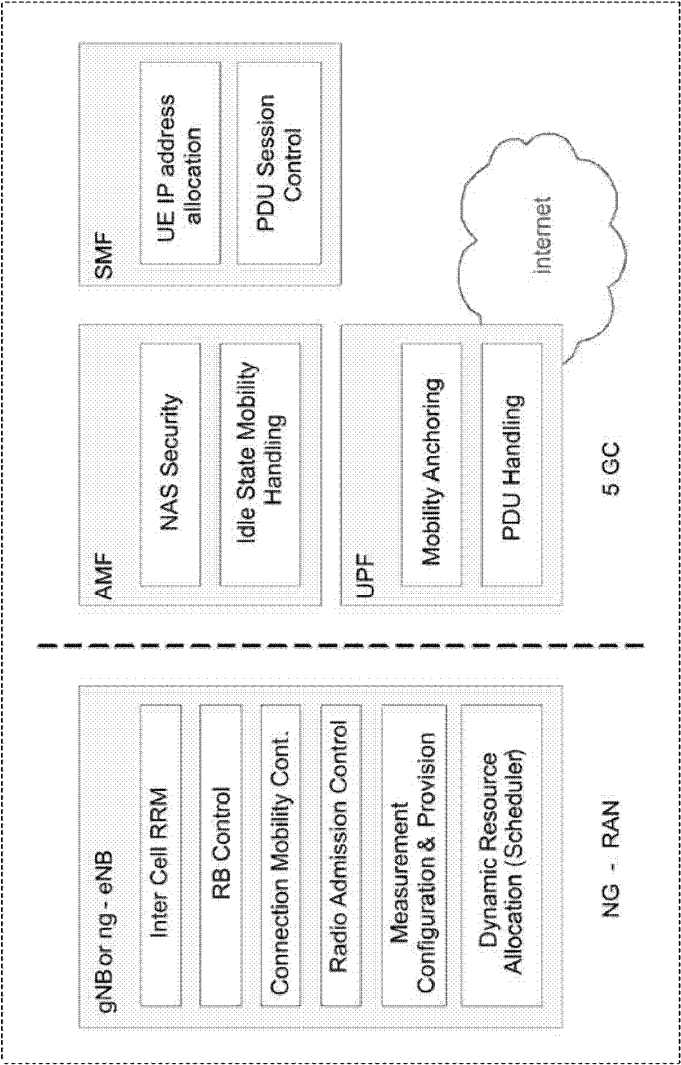
[図15]

DCI または successRAR のうち 3 ビット	Repetition factor	周波数ホッピングの設定
000	1	なし(無効)
001	2	なし(無効)
010	4	なし(無効)
011	8	なし(無効)
100	8	有効、Intra-slot hopping
101	8	有効、Inter-slot hopping
110	16	有効、Intra-slot hopping
111	16	有効、Inter-slot hopping

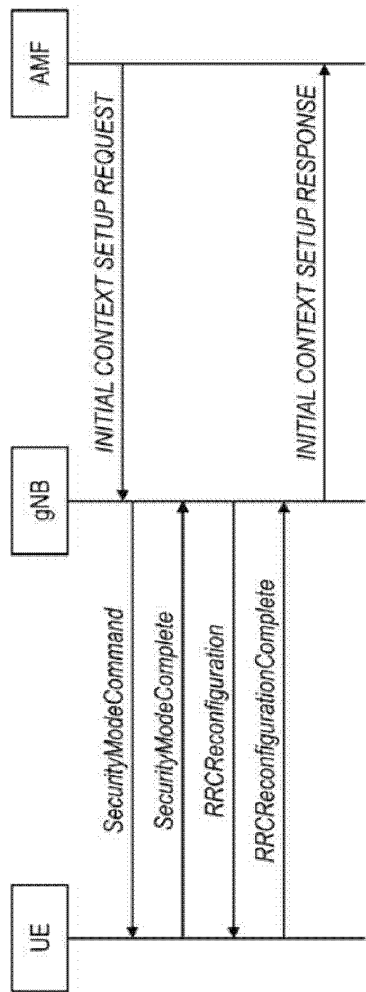
[図16]



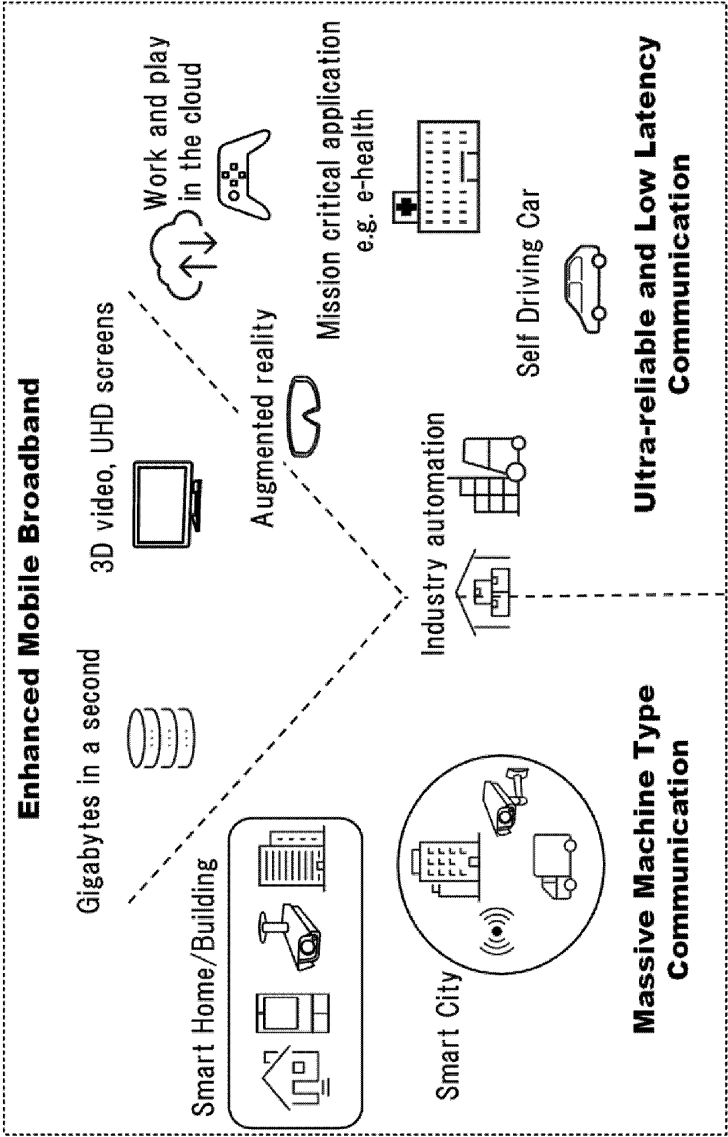
[図17]



[図18]



[図19]



[図20]

