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- (54) **Title:** SORT AND MERGE INSTRUCTION FOR A GENERAL-PURPOSE PROCESSOR

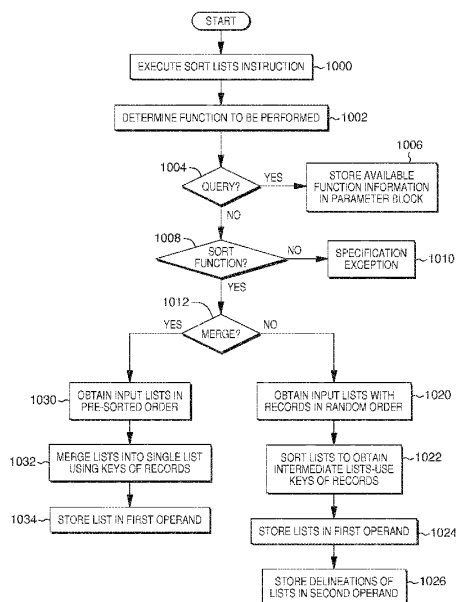


FIG. 10A

(57) **Abstract:** A Sort Lists instruction is provided to perform a sort and/or a merge operation. The instruction is an architected machine instruction of an instruction set architecture and is executed by a general-purpose processor of the computing environment. The executing includes sorting a plurality of input lists to obtain one or more sorted output lists, which are output.

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