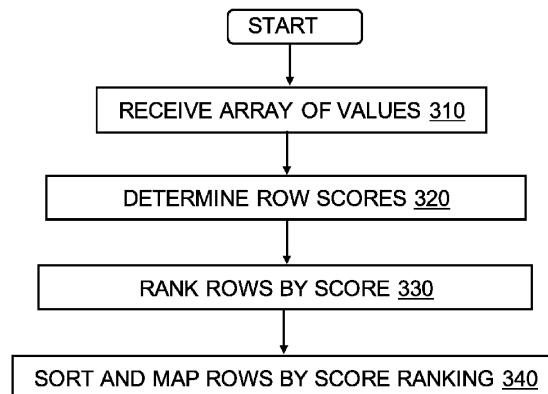




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- (71) Applicant: **INTERNATIONAL BUSINESS MACHINES CORPORATION** [US/US]; New Orchard Road, Armonk, New York, 10504 (US).
- (71) Applicant (*for MG only*): **IBM UNITED KINGDOM LIMITED** [GB/GB]; PO Box 41, North Harbour, Portsmouth Hampshire PO6 3AU (GB).
- (72) Inventors: **MACKIN, Charles**; IBM CORPORATION, 650 Harry Road, Almaden Research Center, San Jose, California 95120-6099 (US). **NARAYANAN, Pritish**; IBM CORPORATION, 650 Harry Road, Almaden Research Center, San Jose, California 95120-6099 (US). **BURR, Geoffrey**; IBM CORPORATION, 650 Harry Road, Almaden Research Center, San Jose, California 95120-6099 (US).
- (74) Agent: **WILLIAMS, Julian**; IBM United Kingdom Limited, Intellectual Property Law, Hursley Park, Hursley, Winchester Hampshire SO21 2JN (GB).
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Fig. 3

(57) Abstract: Distributing multiply-accumulate currents across segment mirrors by providing a circuit including an array of resistive elements, the array including rows and columns and first stage current mirrors, each of the first stage current mirrors being electrically coupled to a segment, wherein the segment comprises a columnar subset of the resistive elements, providing, by the array, a vector of current outputs equal to an analog vector-matrix product between a vector of voltage inputs to the array and a matrix of analog resistive weights within the array, wherein the voltage inputs encode a vector of analog input values, wherein each row of resistive elements corresponds to a specific voltage input, determining a score for each of the rows, determining a ranking of the rows of the array according to the score of each row, and mapping each row to a segment according to the ranking.

[Continued on next page]

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DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN,
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