

ABSTRACT**ACTIVE MATRIX PANEL**

An active matrix panel comprises a picture element matrix (22), which is mounted on a transparent substrate (71, 86) and which includes a plurality of gate lines (24, 25), a plurality of source lines (26, 27, 28) and a plurality of picture elements (32, 33). Each of the picture elements includes a thin film transistor (29, 101). The active matrix panel further includes a gate line drive circuit (21) and a source line drive circuit (12) and being characterised in that at least one of the gate line drive circuit and the source line drive circuit comprises a plurality of complementary thin film transistors (47 to 56; 58, 59; 99, 100) provided on the transparent substrate. The complementary thin film transistors comprise a P type thin film transistor including acceptor impurities in the source region and the drain region thereof and an N type thin film transistor including acceptor impurities and donor impurities in the source region and the drain region thereof, the donor impurities in the N type thin film transistor having a higher concentration than the acceptor impurities in the n type thin film transistor.

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