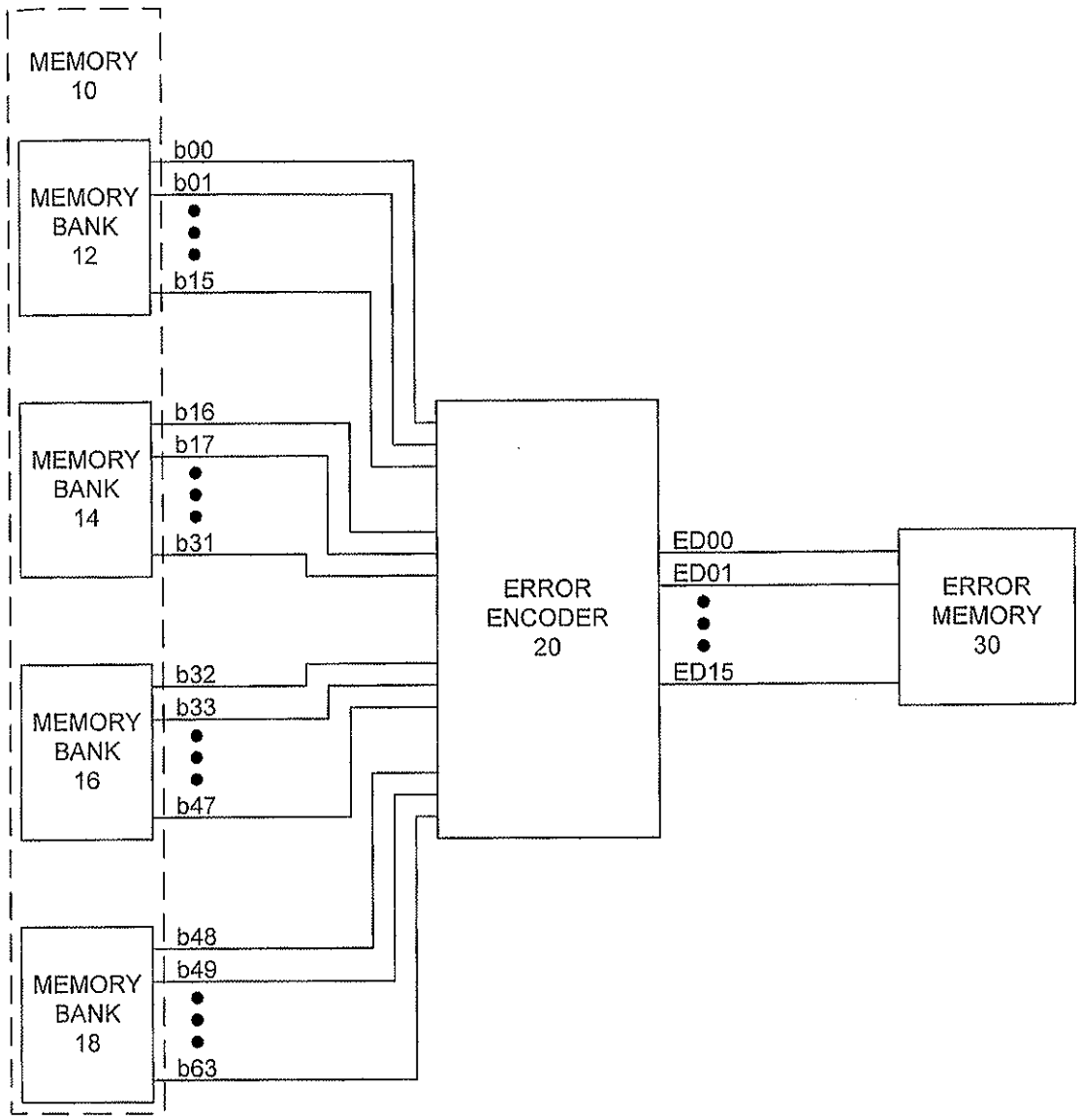

MEMORY
10
MEMORY
BANK
12
MEMORY
BANK
14
MEMORY
BANK
16
MEMORY
BANK
18
b00
b01
b15
b16
b17
b31
b32
b33
b47
b48
b49
b63
ERROR
ENCODER
20
ED00
ED01
ED15
ERROR
MEMORY
30

FIG. 1

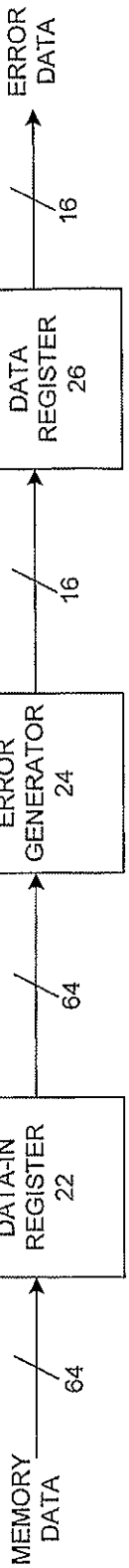

MEMORY DATA
64
DATA-IN REGISTER 22
64
ERROR GENERATOR 24
16
ERROR DATA REGISTER 26
16
ERROR DATA

FIG. 2

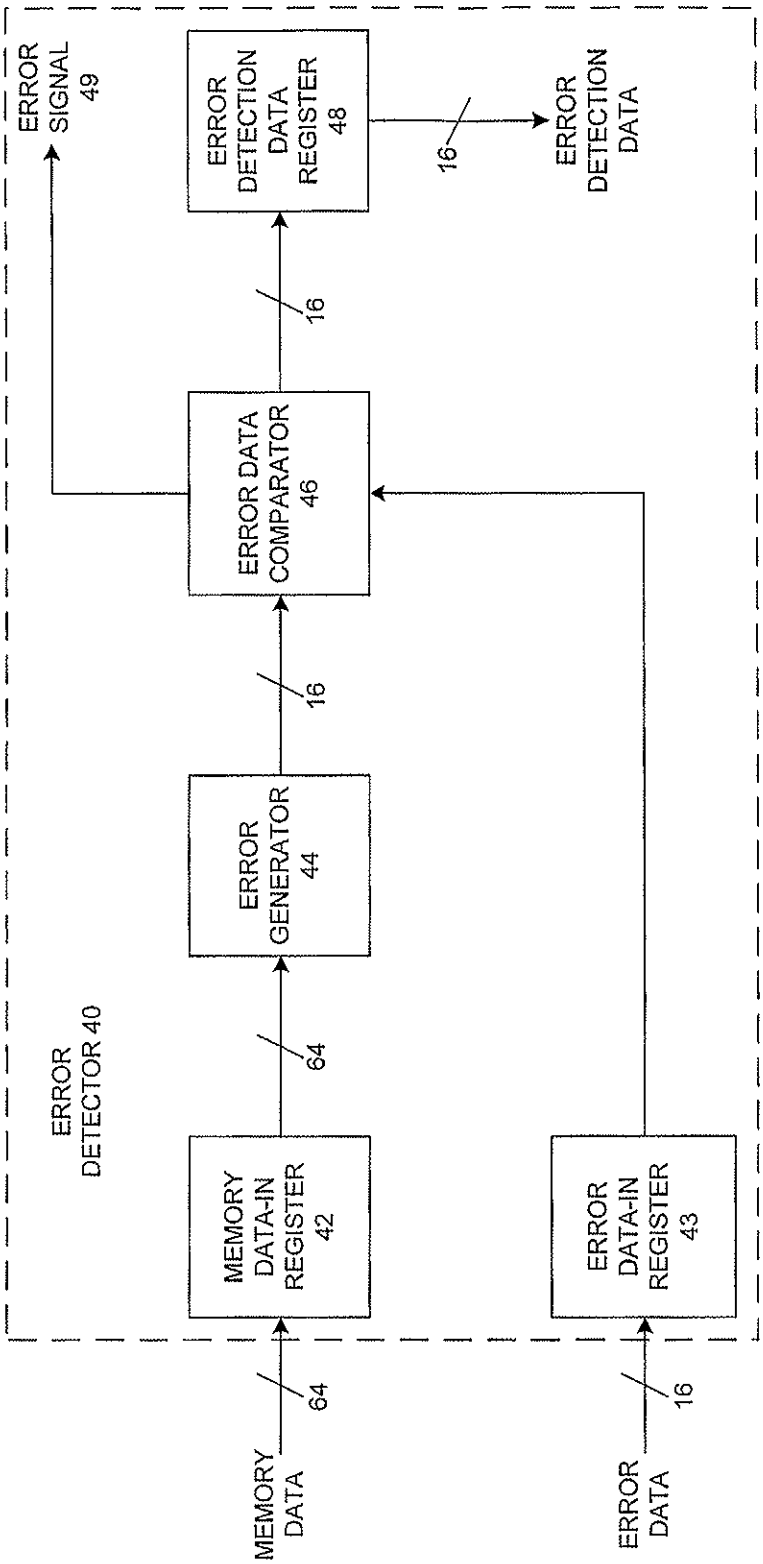

ERROR SIGNAL 49
ERROR DETECTION DATA REGISTER 48
16
ERROR DETECTION DATA
16
ERROR DATA COMPARATOR 46
16
ERROR GENERATOR 44
64
ERROR DETECTOR 40
MEMORY DATA-IN REGISTER 42
ERROR DATA-IN REGISTER 43
64
16
MEMORY DATA
ERROR DATA

FIG. 3

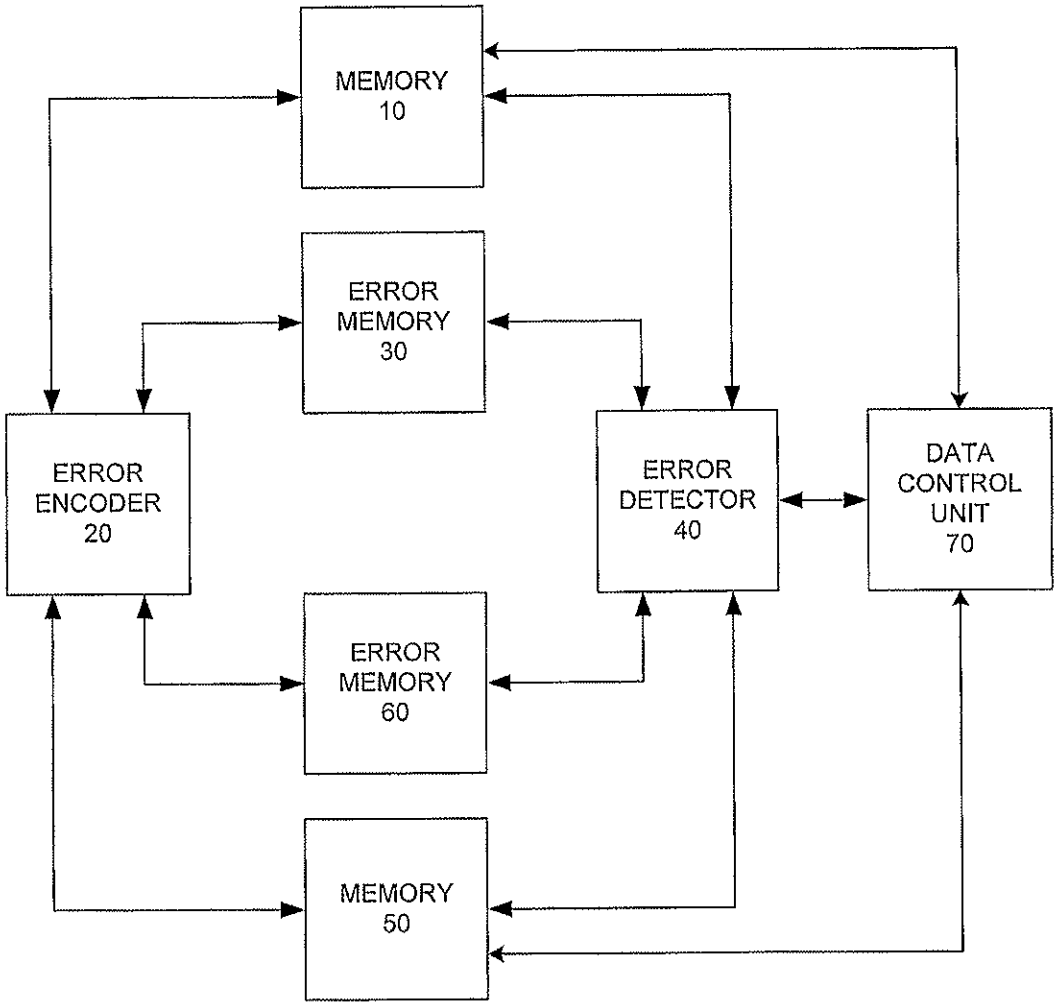

MEMORY
10
ERROR
MEMORY
30
ERROR
ENCODER
20
ERROR
DETECTOR
40
DATA
CONTROL
UNIT
70
ERROR
MEMORY
60
MEMORY
50

FIG. 4

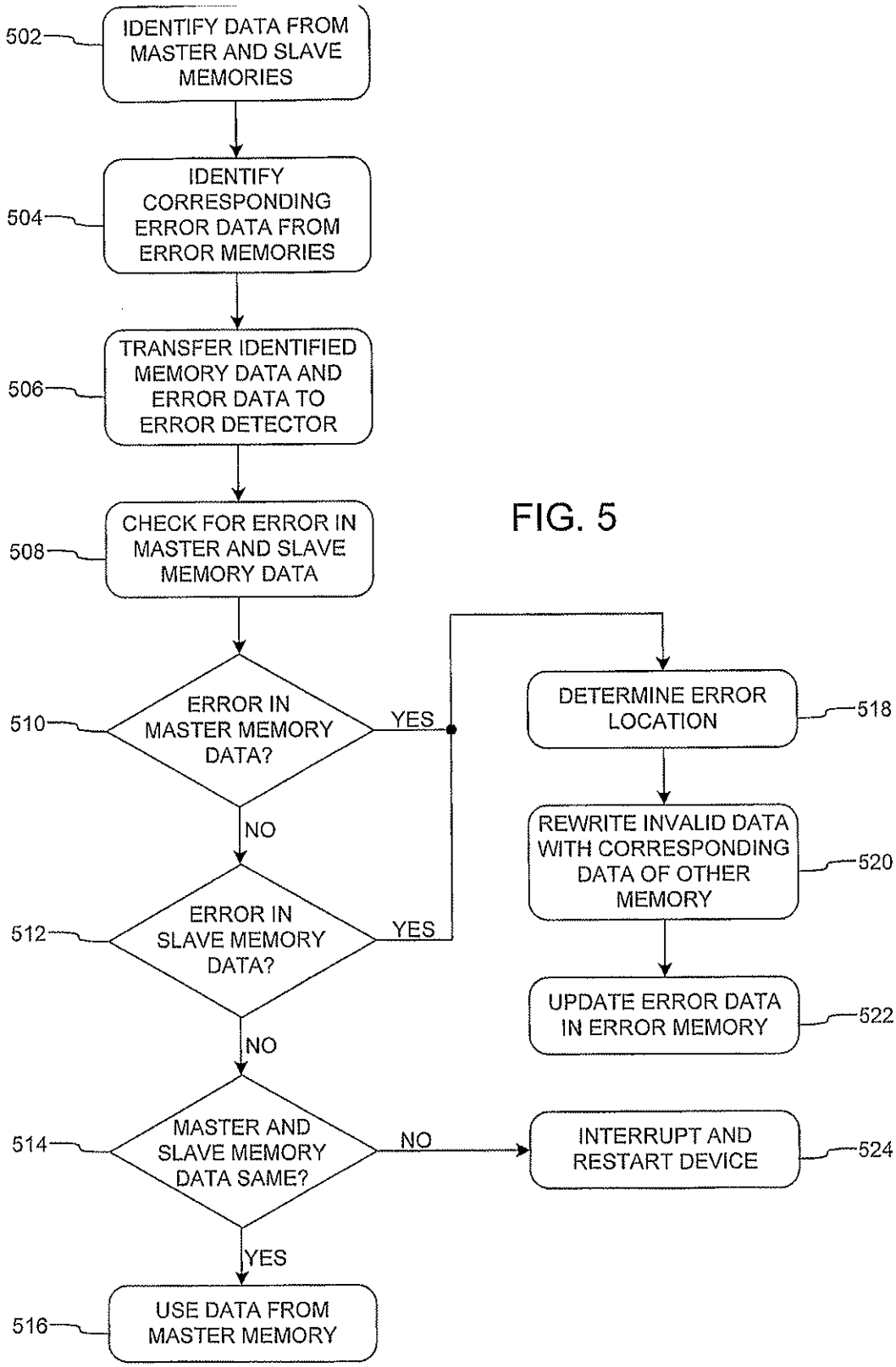

502
IDENTIFY DATA FROM MASTER AND SLAVE MEMORIES
504
IDENTIFY CORRESPONDING ERROR DATA FROM ERROR MEMORIES
506
TRANSFER IDENTIFIED MEMORY DATA AND ERROR DATA TO ERROR DETECTOR
508
CHECK FOR ERROR IN MASTER AND SLAVE MEMORY DATA
510
ERROR IN MASTER MEMORY DATA?
YES
NO
512
ERROR IN SLAVE MEMORY DATA?
YES
NO
514
MASTER AND SLAVE MEMORY DATA SAME?
NO
YES
516
USE DATA FROM MASTER MEMORY
518
DETERMINE ERROR LOCATION
520
REWRITE INVALID DATA WITH CORRESPONDING DATA OF OTHER MEMORY
522
UPDATE ERROR DATA IN ERROR MEMORY
524
INTERRUPT AND RESTART DEVICE

FIG. 5