



162162

ABSTRACT

In order to provide a piezoelectric transformer-driving circuit capable of obtaining a drive pulse signal having a high frequency resolution from a clock having a low frequency, carrying out voltage detection by using a low-cost configuration, and carrying out digital processing, a piezoelectric transformer-driving circuit in accordance with the present invention is configured so that an error voltage calculation circuit multiplies the difference data between the output data of an A/D converter and reference data supplied externally by a constant and outputs the result as error data, so that a frequency setting circuit sets the frequency of the drive pulse signal of the piezoelectric transformer as M-bit data depending on the error data, and so that a frequency division ratio distribution divider circuit divides a clock having a predetermined frequency to generate the drive pulse signal of the piezoelectric transformer, wherein the frequency division ratio of the frequency division ratio distribution divider circuit is distributed for the period of N cycles (N: an integer) of the drive pulse signal of the piezoelectric transformer, and the average frequency division ratio for the period of N cycles is substantially equal to the value obtained by dividing the M-bit data output from the above-mentioned frequency setting circuit by N.



G00002