4. The display of claim 1 wherein the defect map memory is a PROM.

5. The display of claim 1 wherein the display is an active matrix display.

6. The display of claim 1 wherein the display is a simple or passive matrix display.

7. The display in claim 1 wherein the display technology is a thin film transistor/liquid crystal display.

8. The display in claim 1 wherein the display is an electroluminescent display or a plasma display or a field emission display, or electrophoretic display or electrochromic display, or a deformable mirror display or other matrix addressed display technology.

9. The display in claim 1 wherein the data drivers are integrated in the display.

10. The display in claim 1 wherein the data drivers are external to the display.

11. The display of claim 1, wherein said connections are disk bonds.

TFT/LCD ACTIVE DATA LINE REPAIR

ABSTRACT

A matrix addressed display system designed so as to enable data line repair by electronic mechanisms which is efficient and low in cost and thus increases yield. Such active data line repair utilises additional data driver outputs, a defect map memory in the TFT/LCD module (34) and modification of the data stream to the data drivers (36) by additional circuits between the display and the display adapter. A bus configuration on the display substrate is utilized which combines repair flexibility, low parasitic capacitance, and the ability to easily make the necessary interconnections. The number of interconnections is kept to a minimum, the connections are reliable, and the connections may be made with conventional wire bond or laser bond technology, or disk bond technology.

Fig. 2.